



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = 20.2

Alienware Area-51 M15x-R1 (Intel Core 2 Duo T9500)

SPECint_base2006 = 18.3

CPU2006 license: 13

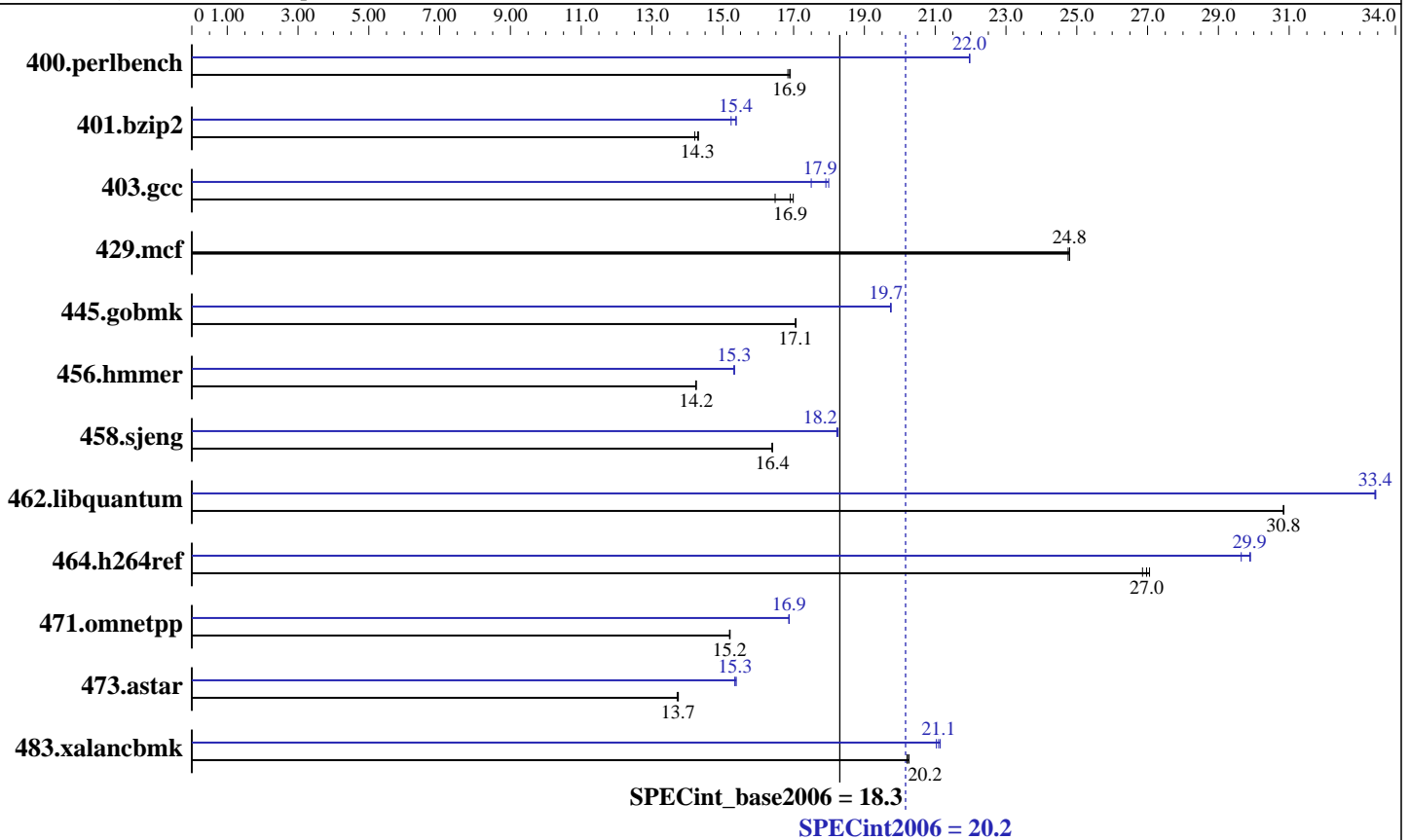
Test date: Dec-2007

Test sponsor: Intel Corporation

Hardware Availability: Jan-2008

Tested by: Intel Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo T9500
 CPU Characteristics:
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Qimonda DDR2-667 CL5)
 Disk Subsystem: Fujitsu 120GB GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate(32-bit)
 Compiler: Intel C++ Compiler for IA32 version 10.1
 Build 20070913 Package ID: w_cc_p_10.1.011
 Microsoft Visual Studio 2005 SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: None
 SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	580	16.8	<u>579</u>	<u>16.9</u>	578	16.9	445	22.0	<u>445</u>	<u>22.0</u>	445	22.0
401.bzip2	679	14.2	674	14.3	<u>675</u>	<u>14.3</u>	<u>628</u>	<u>15.4</u>	633	15.2	627	15.4
403.gcc	<u>476</u>	<u>16.9</u>	489	16.5	474	17.0	<u>449</u>	<u>17.9</u>	460	17.5	447	18.0
429.mcf	368	24.8	368	24.8	<u>368</u>	<u>24.8</u>	368	24.8	368	24.8	<u>368</u>	<u>24.8</u>
445.gobmk	615	17.1	615	17.1	<u>615</u>	<u>17.1</u>	531	19.7	<u>531</u>	<u>19.7</u>	531	19.8
456.hammer	655	14.2	<u>655</u>	<u>14.2</u>	655	14.2	609	15.3	<u>609</u>	<u>15.3</u>	609	15.3
458.sjeng	738	16.4	<u>738</u>	<u>16.4</u>	738	16.4	663	18.3	664	18.2	<u>664</u>	<u>18.2</u>
462.libquantum	<u>672</u>	<u>30.8</u>	672	30.9	672	30.8	620	33.4	<u>620</u>	<u>33.4</u>	620	33.4
464.h264ref	824	26.9	818	27.1	<u>820</u>	<u>27.0</u>	746	29.6	740	29.9	<u>740</u>	<u>29.9</u>
471.omnetpp	411	15.2	<u>411</u>	<u>15.2</u>	411	15.2	370	16.9	371	16.9	<u>370</u>	<u>16.9</u>
473.astar	<u>511</u>	<u>13.7</u>	512	13.7	511	13.7	458	15.3	<u>457</u>	<u>15.3</u>	456	15.4
483.xalanbmk	<u>341</u>	<u>20.2</u>	342	20.2	341	20.3	328	21.0	<u>327</u>	<u>21.1</u>	326	21.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The system bus runs at 800 MHz
 Binaries were built on Windows Vista32
 The following VS 2005 SP1 updates were applied: KB926601 and KB932232
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to physical,0

Base Compiler Invocation

C benchmarks:
 icl -Qvc8 -Qc99

C++ benchmarks:
 icl -Qvc8

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

```
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
libguide40.lib
```

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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000
libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml>



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