SPEC® CINT2006 Result

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Intel Corporation

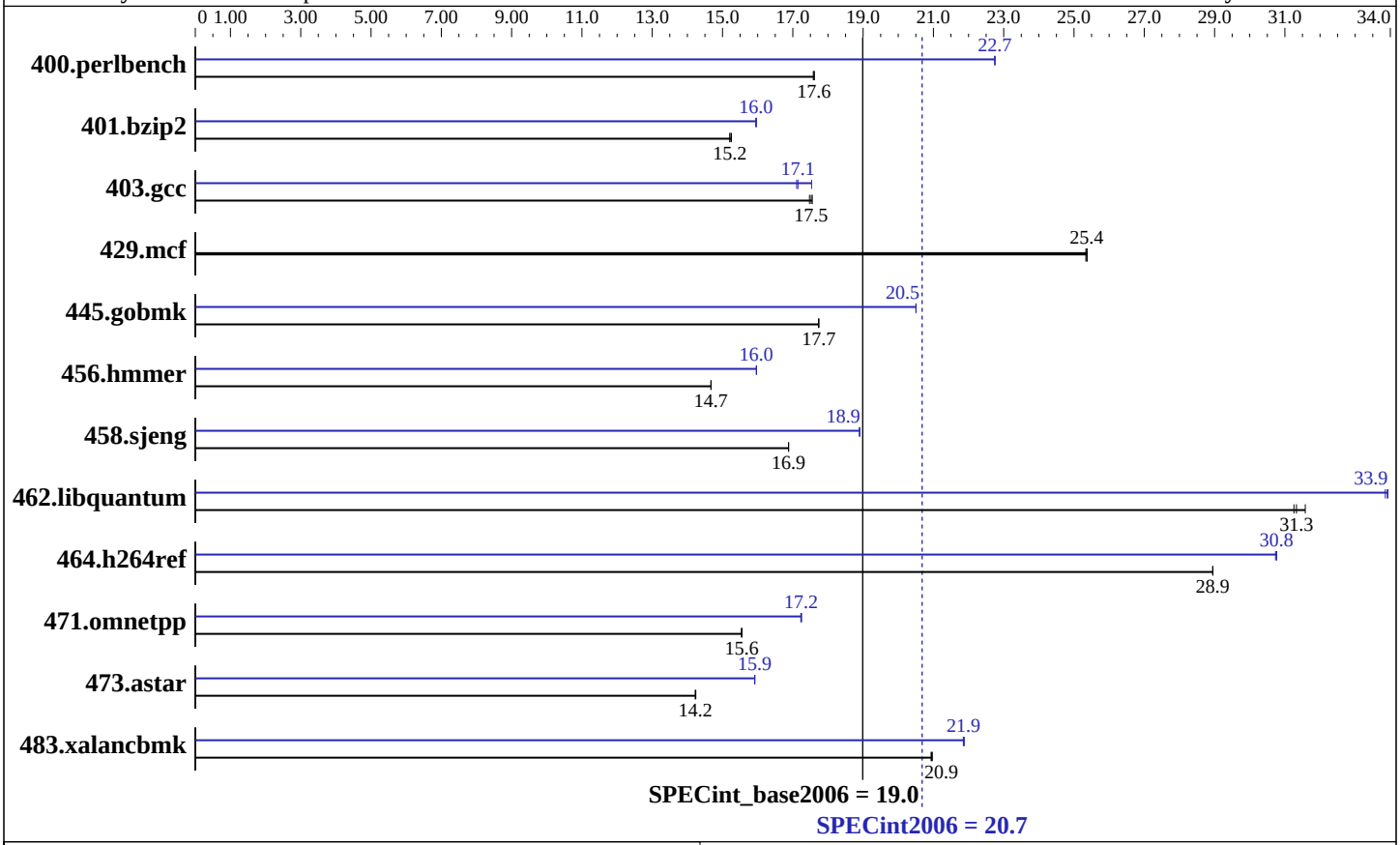
Alienware Area-51 M15x-R1 (Intel Core 2 Extreme X9000)

SPECint®2006 = 20.7

SPECint_base2006 = 19.0

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Extreme X9000
CPU Characteristics:	
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Qimonda DDR2-667 CL5)
Disk Subsystem:	Fujitsu 120GB GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate(32-bit)
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	None SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	556	17.6	**555**	**17.6**	555	17.6	429	22.8	430	22.7	**429**	**22.7**
401.bzip2	**634**	**15.2**	633	15.3	635	15.2	**605**	**16.0**	604	16.0	605	15.9
403.gcc	461	17.5	**459**	**17.5**	459	17.6	459	17.5	**469**	**17.1**	471	17.1
429.mcf	359	25.4	**360**	**25.4**	360	25.3	359	25.4	**360**	**25.4**	360	25.3
445.gobmk	**591**	**17.7**	591	17.7	592	17.7	512	20.5	**512**	**20.5**	512	20.5
456.hmmer	636	14.7	636	14.7	**636**	**14.7**	584	16.0	584	16.0	**584**	**16.0**
458.sjeng	717	16.9	717	16.9	**717**	**16.9**	640	18.9	640	18.9	**640**	**18.9**
462.libquantum	656	31.6	663	31.3	**661**	**31.3**	611	33.9	**611**	**33.9**	612	33.9
464.h264ref	764	29.0	765	28.9	**764**	**28.9**	720	30.7	**719**	**30.8**	719	30.8
471.omnetpp	402	15.5	**402**	**15.6**	402	15.6	363	17.2	**363**	**17.2**	362	17.2
473.astar	**493**	**14.2**	494	14.2	493	14.2	441	15.9	441	15.9	**441**	**15.9**
483.xalancbmk	**329**	**20.9**	330	20.9	329	21.0	316	21.9	315	21.9	**316**	**21.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
The system bus runs at 800 MHz
Binaries were built on Windows Vista32
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qansi-alias -Qprefetch -Qparallel -Qpar-runtime-control /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE`

401.bzip2: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qprefetch /F512000000 libguide40.lib`

403.gcc: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 libguide40.lib`

Continued on next page

Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive /F512000000 libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always -Qparallel -Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=block -Qcxx_features /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml

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