



# SPEC<sup>®</sup> CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## NEC Corporation

Express5800/120Bb-m6  
(Intel Xeon processor X5355)

SPECint<sup>®</sup>2006 = 18.7

SPECint\_base2006 = 17.0

CPU2006 license: 9006

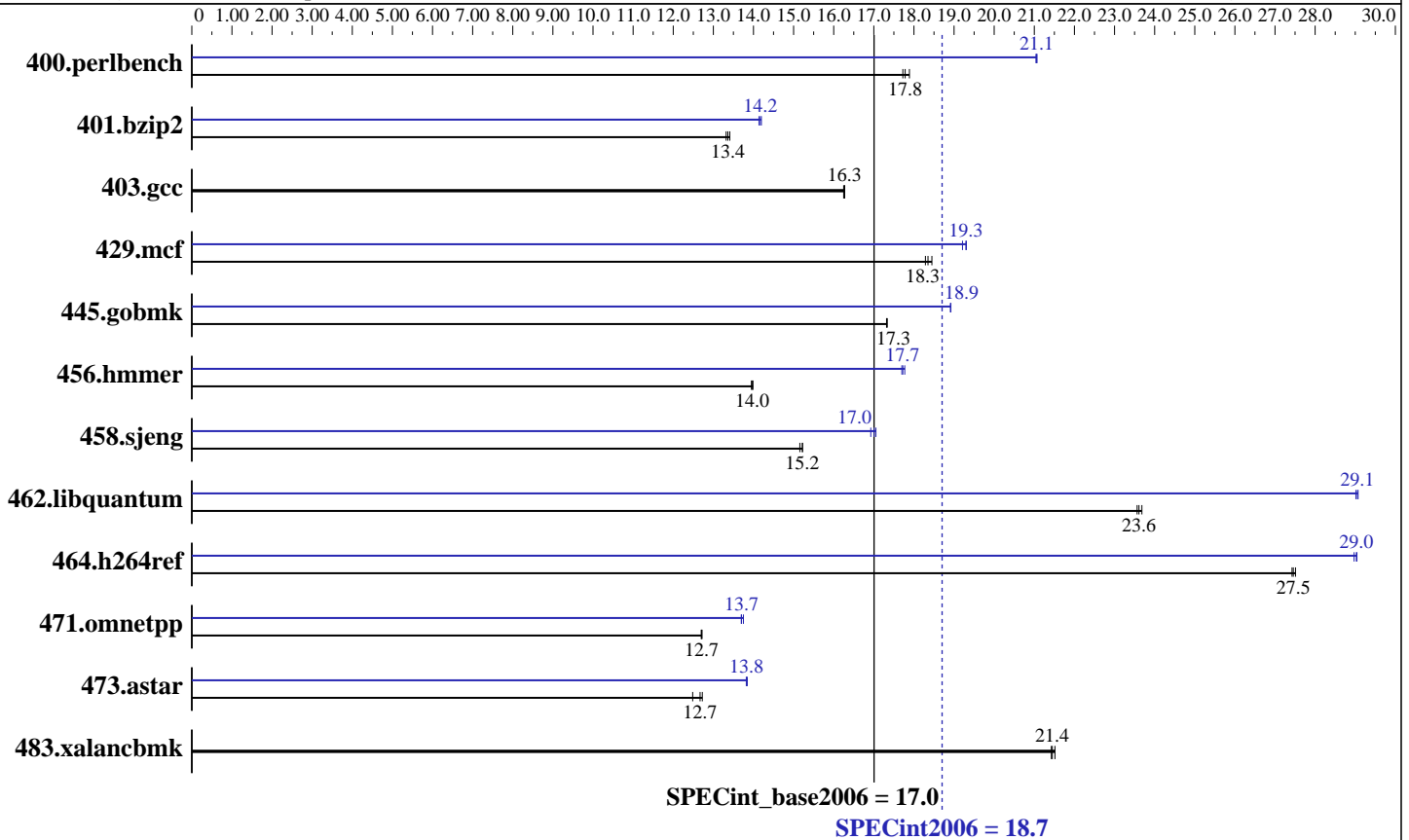
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Nov-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007



### Hardware

CPU Name: Intel Xeon X5355  
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus  
 CPU MHz: 2666  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: 1x73.2 GB SAS, 10000RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64), Kernel 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.0 Build 20070426 Package ID: L\_cc\_p\_10.0.023  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## NEC Corporation

Express5800/120Bb-m6  
(Intel Xeon processor X5355)

SPECint2006 = 18.7

SPECint\_base2006 = 17.0

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Nov-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	551	17.7	<u>549</u>	<u>17.8</u>	546	17.9	<u>464</u>	<u>21.1</u>	464	21.0	464	21.1
401.bzip2	720	13.4	725	13.3	<u>722</u>	<u>13.4</u>	680	14.2	683	14.1	<u>681</u>	<u>14.2</u>
403.gcc	<u>495</u>	<u>16.3</u>	495	16.2	495	16.3	<u>495</u>	<u>16.3</u>	495	16.2	495	16.3
429.mcf	494	18.4	<u>497</u>	<u>18.3</u>	499	18.3	472	19.3	475	19.2	<u>473</u>	<u>19.3</u>
445.gobmk	<u>605</u>	<u>17.3</u>	606	17.3	605	17.3	554	18.9	555	18.9	<u>555</u>	<u>18.9</u>
456.hmmmer	<u>668</u>	<u>14.0</u>	669	13.9	667	14.0	527	17.7	<u>526</u>	<u>17.7</u>	525	17.8
458.sjeng	795	15.2	798	15.2	<u>795</u>	<u>15.2</u>	<u>712</u>	<u>17.0</u>	715	16.9	710	17.1
462.libquantum	879	23.6	875	23.7	<u>878</u>	<u>23.6</u>	713	29.1	714	29.0	<u>713</u>	<u>29.1</u>
464.h264ref	804	27.5	<u>806</u>	<u>27.5</u>	807	27.4	762	29.0	<u>762</u>	<u>29.0</u>	764	29.0
471.omnetpp	492	12.7	<u>492</u>	<u>12.7</u>	492	12.7	456	13.7	455	13.8	<u>456</u>	<u>13.7</u>
473.astar	<u>554</u>	<u>12.7</u>	562	12.5	552	12.7	507	13.8	<u>508</u>	<u>13.8</u>	508	13.8
483.xalancbmk	322	21.4	321	21.5	<u>322</u>	<u>21.4</u>	322	21.4	321	21.5	<u>322</u>	<u>21.4</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**NEC Corporation**

Express5800/120Bb-m6  
(Intel Xeon processor X5355)

**SPECint2006 = 18.7**

**SPECint\_base2006 = 17.0**

**CPU2006 license:** 9006

**Test sponsor:** NEC Corporation

**Tested by:** NEC Corporation

**Test date:** Nov-2007

**Hardware Availability:** Jan-2007

**Software Availability:** Jun-2007

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**NEC Corporation**

Express5800/120Bb-m6  
(Intel Xeon processor X5355)

**SPECint2006 =**

**18.7**

**SPECint\_base2006 =**

**17.0**

**CPU2006 license:** 9006

**Test sponsor:** NEC Corporation

**Tested by:** NEC Corporation

**Test date:** Nov-2007

**Hardware Availability:** Jan-2007

**Software Availability:** Jun-2007

## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec\_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0  
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec\_div -ansi-alias -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmarheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-ic10-INT-ia32-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-ic10-INT-ia32-intel64-linux-flags.xml>



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**NEC Corporation**

Express5800/120Bb-m6  
(Intel Xeon processor X5355)

**SPECint2006 = 18.7**

**SPECint\_base2006 = 17.0**

**CPU2006 license:** 9006

**Test sponsor:** NEC Corporation

**Tested by:** NEC Corporation

**Test date:** Nov-2007

**Hardware Availability:** Jan-2007

**Software Availability:** Jun-2007

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 15:20:29 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 8 January 2008.