SPEC® CINT2006 Result
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Dell Inc.

PowerEdge R200 (Intel Xeon 3085, 3.00 GHz)

SPECint®_rate2006 =   40.3

SPECint_rate_base2006 = 34.9

Copies

400.perlbench — 2: 47.3 / 2: 40.6
401.bzip2 — 2: 28.9 / 2: 27.2
403.gcc — 2: 34.4 / 2: 34.4
429.mcf — 2: 34.9 / 2: 33.4
445.gobmk — 2: 42.9 / 2: 39.2
456.hmmer — 2: 53.9 / 2: 31.6
458.sjeng — 2: 39.5 / 2: 35.9
462.libquantum — 1: 55.0 / 2: 32.2
464.h264ref — 2: 64.6 / 2: 61.0
471.omnetpp — 2: 27.1 / 2: 25.5
473.astar — 2: 27.9 / 2: 25.6
483.xalancbmk — 2: 45.0

SPECint_rate_base2006 = 34.9

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Hardware

CPU Name:	Intel Xeon 3085
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 x 2GB 800MHz DDR2)
Disk Subsystem:	73GB SAS, 10K RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	None

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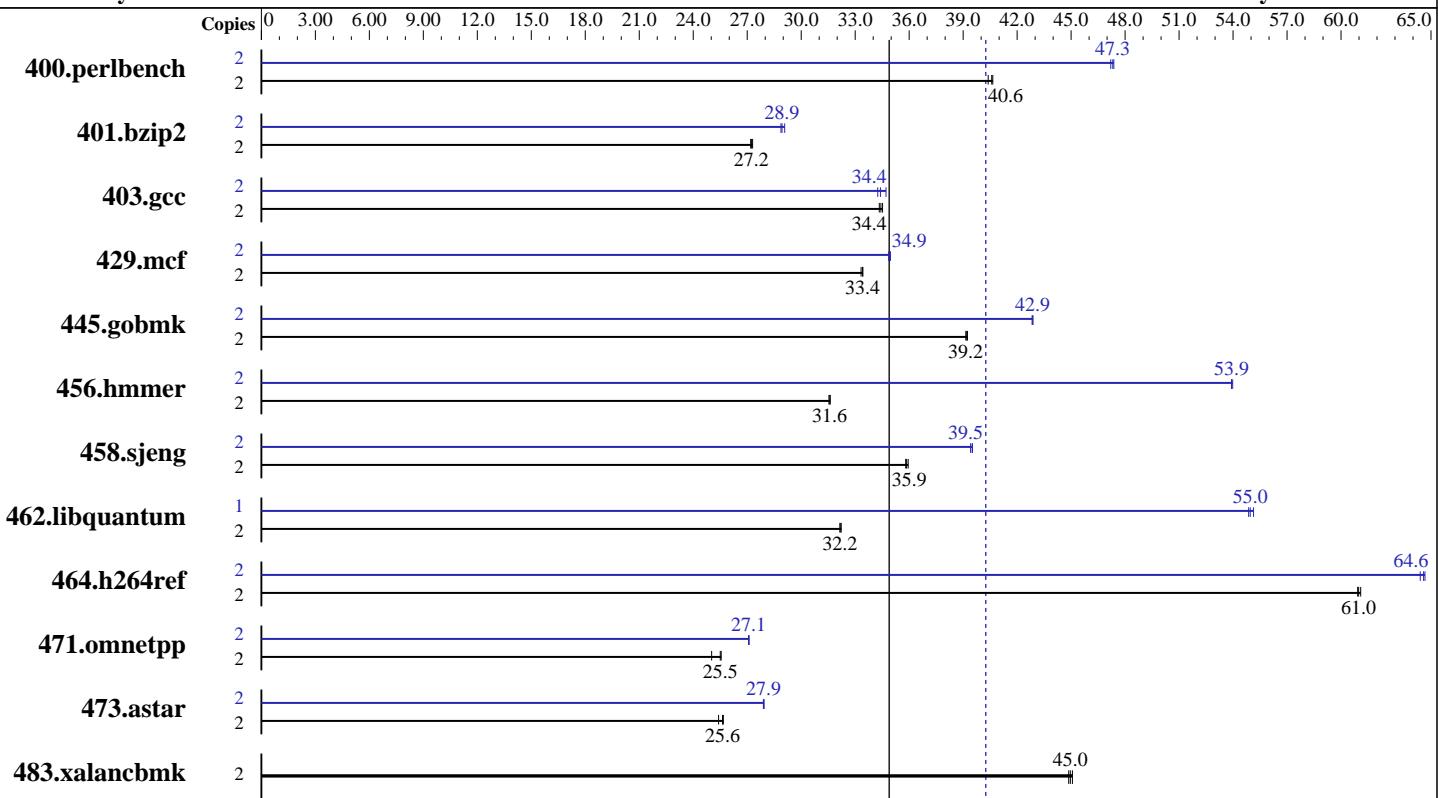
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CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	481	40.6	484	40.4	**481**	**40.6**	2	414	47.2	412	47.4	**413**	**47.3**
401.bzip2	2	710	27.2	707	27.3	**709**	**27.2**	2	664	29.1	668	28.9	**667**	**28.9**
403.gcc	2	466	34.5	469	34.3	**468**	**34.4**	2	464	34.7	470	34.3	**468**	**34.4**
429.mcf	2	**546**	**33.4**	546	33.4	547	33.3	2	**522**	**34.9**	523	34.9	522	35.0
445.gobmk	2	535	39.2	536	39.1	**535**	**39.2**	2	490	42.8	**489**	**42.9**	489	42.9
456.hmmer	2	**591**	**31.6**	592	31.5	590	31.6	2	346	54.0	**346**	**53.9**	346	53.9
458.sjeng	2	**675**	**35.9**	673	35.9	676	35.8	2	**613**	**39.5**	612	39.5	614	39.4
462.libquantum	2	1289	32.2	1286	32.2	**1288**	**32.2**	1	**377**	**55.0**	376	55.1	378	54.9
464.h264ref	2	**726**	**61.0**	724	61.1	726	60.9	2	687	64.4	**685**	**64.6**	684	64.7
471.omnetpp	2	500	25.0	489	25.6	**490**	**25.5**	2	461	27.1	**462**	**27.1**	462	27.1
473.astar	2	**548**	**25.6**	547	25.7	553	25.4	2	**503**	**27.9**	503	27.9	503	27.9
483.xalancbmk	2	306	45.1	308	44.9	**307**	**45.0**	2	306	45.1	308	44.9	**307**	**45.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
    -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

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Base Other Flags

C benchmarks:

       403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
   `icc`

    401.bzip2: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc`
          `-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib`
          `-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

    456.hmmer: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc`
          `-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib`
          `-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

C++ benchmarks:
   `icpc`

Peak Portability Flags

   400.perlbench: `-DSPEC_CPU_LINUX_IA32`
      401.bzip2: `-DSPEC_CPU_LP64`
     456.hmmer: `-DSPEC_CPU_LP64`
  462.libquantum: `-DSPEC_CPU_LINUX`
  483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

   400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias`
              `-prefetch`

      401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch`

        403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

        429.mcf: `-fast -prefetch`

     445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo`
              `-no-prec-div -ansi-alias`

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Peak Optimization Flags (Continued)

```
    456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

    458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
               -opt-streaming-stores always -vec-guard-write
               -opt-malloc-options=3 -parallel -par-runtime-control

   464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
               -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
               -no-prec-div -ansi-alias -opt-ra-region-strategy=block
               -Wl,-z,muldefs
               -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

     473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
               -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
               -Wl,-z,muldefs
               -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

  483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
      403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-ia32-intel64-linux-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-ia32-intel64-linux-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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