SPEC® CINT2006 Result
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Itautec

$SPECint®\_rate2006 =$ Not Run

Servidor Itautec LR101 (AMD Opteron 1218)

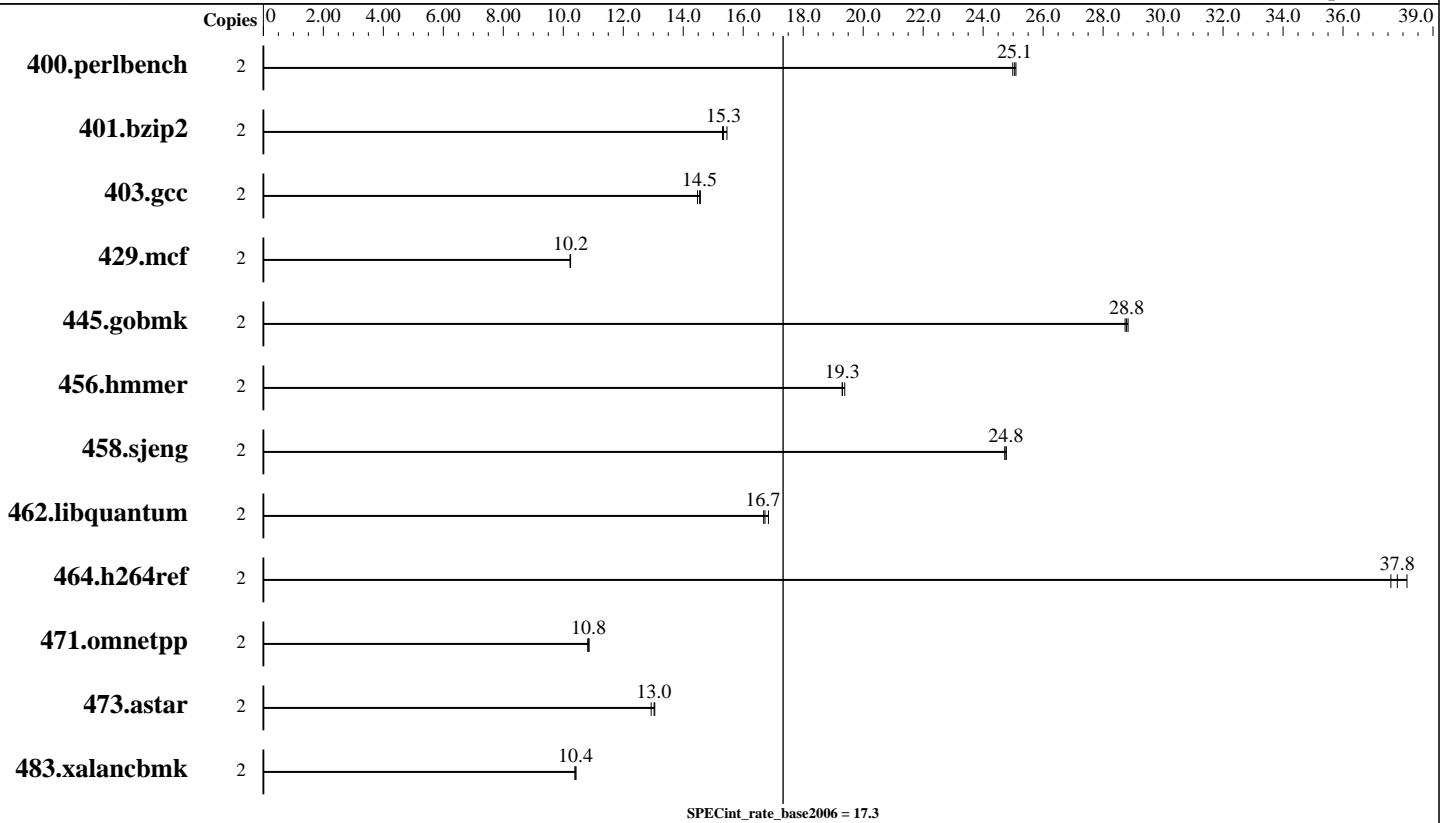$SPECint\_rate\_base2006 =$ 17.3

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Apr-2006

Benchmark	Copies	Result
400.perlbench	2	25.1
401.bzip2	2	15.3
403.gcc	2	14.5
429.mcf	2	10.2
445.gobmk	2	28.8
456.hmmer	2	19.3
458.sjeng	2	24.8
462.libquantum	2	16.7
464.h264ref	2	37.8
471.omnetpp	2	10.8
473.astar	2	13.0
483.xalancbmk	2	10.4

SPECint_rate_base2006 = 17.3

Hardware

CPU Name: AMD Opteron 1218
CPU Characteristics:
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 64 KB I + 64 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core
L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1GB, DDR2-400 CL3)
Disk Subsystem: 200 GB SATA, 7200RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: gcc, g++ 4.1.2
Auto Parallel: No
File System: ReiserFS
System State: Multi-user, run level 3
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other Software: None

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**780**	**25.1**	782	25.0	779	25.1							
401.bzip2	2	1249	15.5	1261	15.3	**1258**	**15.3**							
403.gcc	2	**1107**	**14.5**	1112	14.5	1105	14.6							
429.mcf	2	1784	10.2	**1781**	**10.2**	1781	10.2							
445.gobmk	2	**729**	**28.8**	730	28.7	728	28.8							
456.hmmer	2	963	19.4	967	19.3	**967**	**19.3**							
458.sjeng	2	977	24.8	**977**	**24.8**	979	24.7							
462.libquantum	2	2461	16.8	2484	16.7	**2478**	**16.7**							
464.h264ref	2	1161	38.1	1177	37.6	**1171**	**37.8**							
471.omnetpp	2	1151	10.9	1156	10.8	**1152**	**10.8**							
473.astar	2	**1078**	**13.0**	1086	12.9	1076	13.1							
483.xalancbmk	2	**1325**	**10.4**	1324	10.4	1329	10.4							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Submitted_by: "Richard Antonio Caneca, richard.caneca" <richard@itautec.com>
Submitted: Tue Dec 18 21:31:17 EST 2007
Submission: cpu2006-20071210-02849.sub
```

Base Compiler Invocation

C benchmarks:
    gcc

C++ benchmarks:
    g++

Base Portability Flags

```
  400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
     401.bzip2: -DSPEC_CPU_LP64
       403.gcc: -DSPEC_CPU_LP64
       429.mcf: -DSPEC_CPU_LP64
    445.gobmk: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
     458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

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Base Portability Flags (Continued)

        464.h264ref: `-DSPEC_CPU_LP64`
        471.omnetpp: `-DSPEC_CPU_LP64`
            473.astar: `-DSPEC_CPU_LP64`
    483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
        `-O3 -fno-inline-functions -funroll-loops`

C++ benchmarks:
        `-O3 -funroll-loops`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.11.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.11.xml

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For other inquiries, please contact webmaster@spec.org.

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