



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY RX200 S4, Intel Xeon processor E5430,
2.66 GHz

SPECint_rate2006 = 102

SPECint_rate_base2006 = 102

CPU2006 license: 22

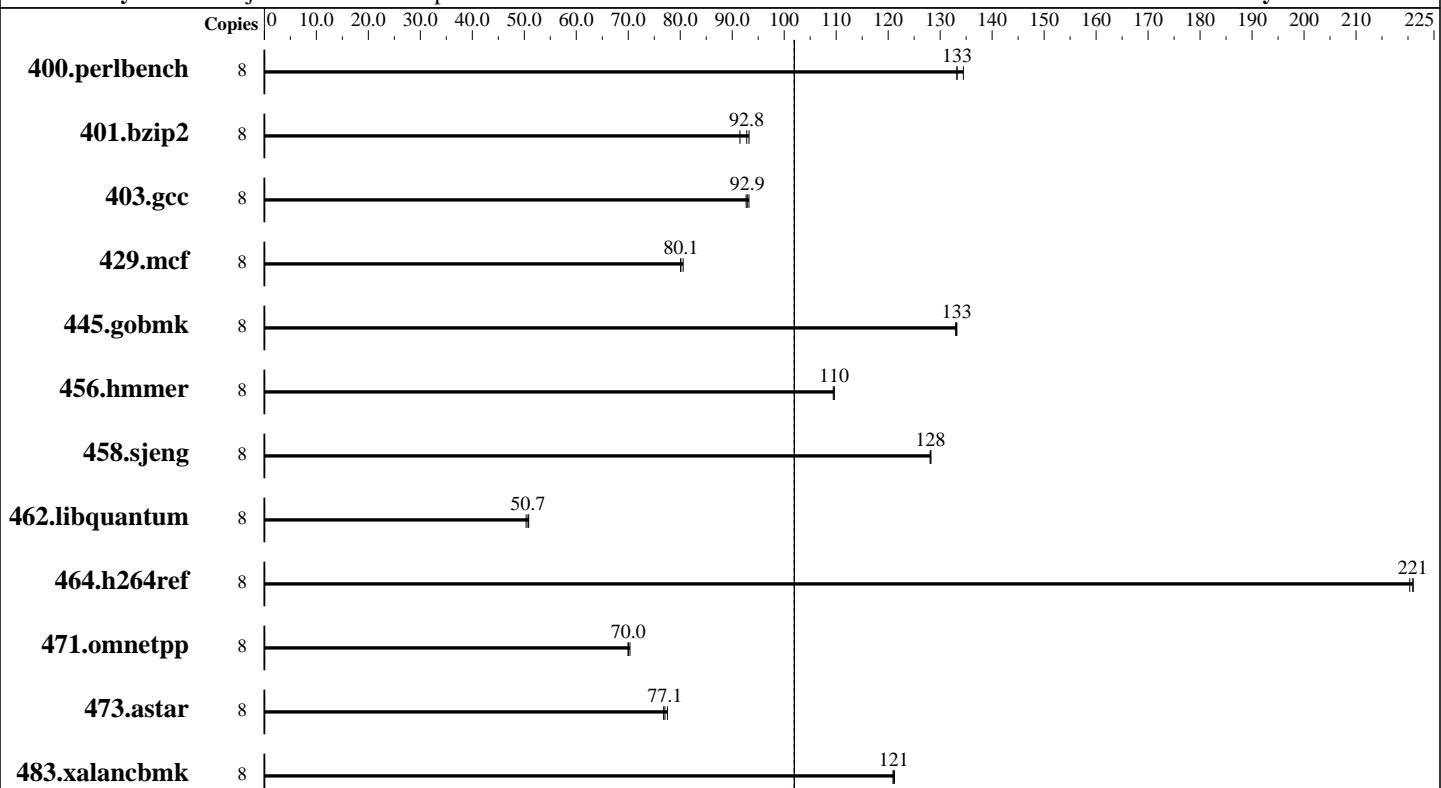
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007



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Hardware

CPU Name: Intel Xeon E5430
CPU Characteristics: 1333 MHz system bus
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: Seagate ST973451SS (SAS, 73GB, 15000rpm)
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
Auto Parallel: No
File System: ext2
System State: Multiuser, Runlevel 3
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	581	134	587	133	587	133	8	581	134	587	133	587	133
401.bzip2	8	832	92.8	828	93.2	844	91.5	8	832	92.8	828	93.2	844	91.5
403.gcc	8	693	92.9	695	92.7	691	93.2	8	693	92.9	695	92.7	691	93.2
429.mcf	8	906	80.6	911	80.1	911	80.1	8	906	80.6	911	80.1	911	80.1
445.gobmk	8	630	133	630	133	631	133	8	630	133	630	133	631	133
456.hmmer	8	681	110	682	109	681	110	8	681	110	682	109	681	110
458.sjeng	8	755	128	755	128	756	128	8	755	128	755	128	756	128
462.libquantum	8	3292	50.4	3269	50.7	3260	50.8	8	3292	50.4	3269	50.7	3260	50.8
464.h264ref	8	803	220	801	221	801	221	8	803	220	801	221	801	221
471.omnetpp	8	715	70.0	711	70.3	714	70.0	8	715	70.0	711	70.3	714	70.0
473.astar	8	724	77.5	731	76.8	729	77.1	8	724	77.5	731	76.8	729	77.1
483.xalancbmk	8	456	121	456	121	455	121	8	456	121	456	121	455	121

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
 icc

C++ benchmarks:
 icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmpllr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: basepeak = yes
```

```
401.bzip2: basepeak = yes
```

```
403.gcc: basepeak = yes
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: basepeak = yes
```

```
456.hmmer: basepeak = yes
```

```
458.sjeng: basepeak = yes
```

```
462.libquantum: basepeak = yes
```

```
464.h264ref: basepeak = yes
```

C++ benchmarks:

```
471.omnetpp: basepeak = yes
```

```
473.astar: basepeak = yes
```

```
483.xalancbmk: basepeak = yes
```



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.06.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.06.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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