



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 32.9

Acer Altos R520 (Intel Xeon 5150)

SPECint_rate_base2006 = 28.9

CPU2006 license: 97

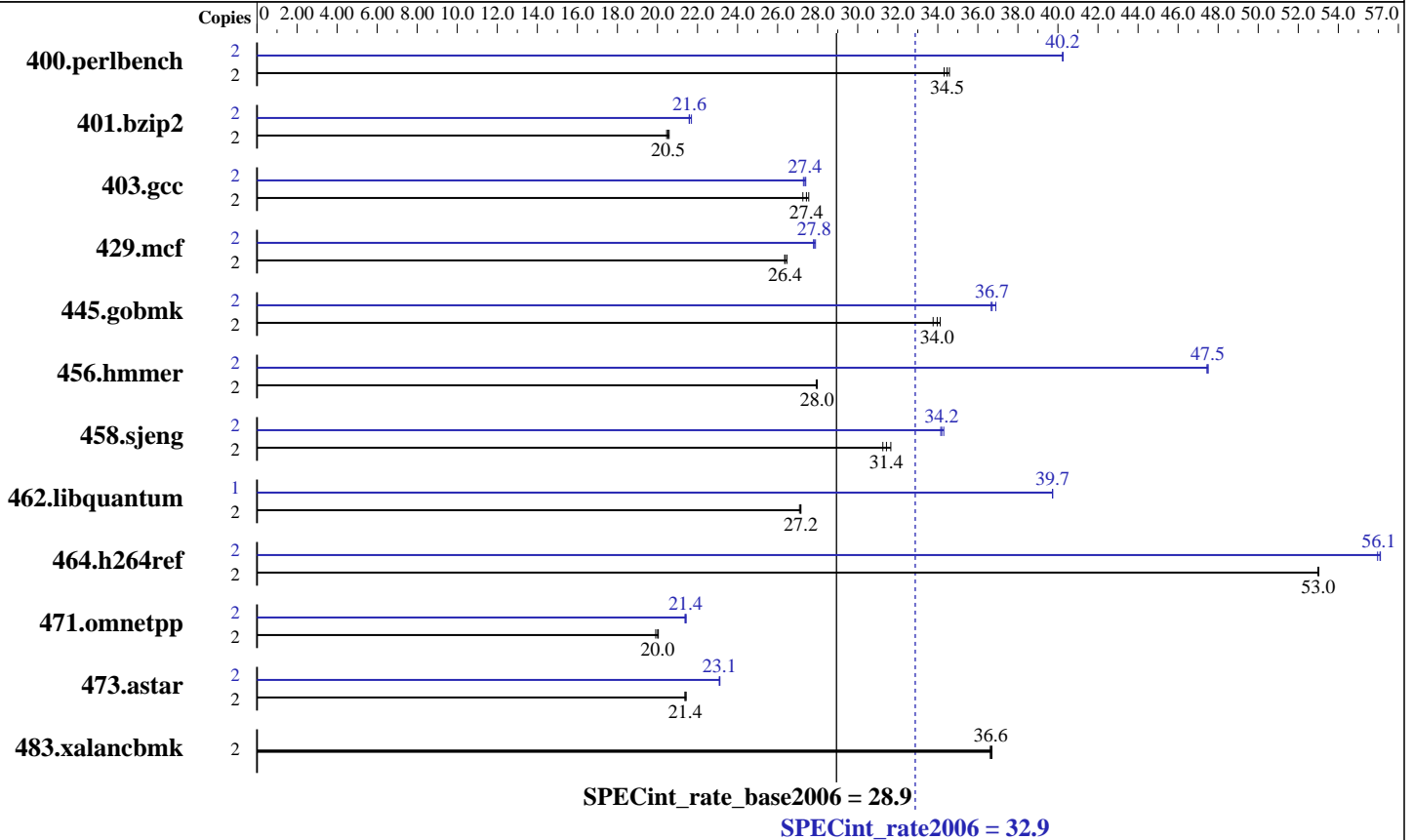
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Nov-2007

Hardware Availability: Nov-2006

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon 5150
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2660
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
 Disk Subsystem: 1 x 73GB 10000PRM SAS HDD
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 SP1 EM64T (kernel 2.6.16.46-0.12-smp)
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	567	34.5	565	34.6	569	34.3	2	485	40.3	486	40.2	486	40.2
401.bzip2	2	940	20.5	938	20.6	943	20.5	2	893	21.6	894	21.6	889	21.7
403.gcc	2	584	27.6	591	27.3	587	27.4	2	590	27.3	588	27.4	587	27.4
429.mcf	2	689	26.5	692	26.4	691	26.4	2	656	27.8	656	27.8	654	27.9
445.gobmk	2	621	33.8	615	34.1	618	34.0	2	569	36.9	572	36.7	571	36.7
456.hammer	2	667	28.0	667	28.0	668	27.9	2	393	47.4	393	47.5	393	47.5
458.sjeng	2	765	31.7	774	31.3	770	31.4	2	708	34.2	709	34.1	706	34.3
462.libquantum	2	1526	27.2	1526	27.2	1530	27.1	1	522	39.7	522	39.7	522	39.7
464.h264ref	2	835	53.0	835	53.0	835	53.0	2	789	56.1	789	56.1	791	56.0
471.omnetpp	2	628	19.9	624	20.0	625	20.0	2	584	21.4	583	21.4	585	21.4
473.astar	2	656	21.4	657	21.4	655	21.4	2	608	23.1	608	23.1	607	23.1
483.xalancbmk	2	376	36.7	377	36.6	377	36.6	2	376	36.7	377	36.6	377	36.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result was measured on the Acer Altos R520.
The Altos R720 and Altos R520 are electronically equivalent.

BIOS Settings

Hardware Prefetcher set to ON

Adjacent Cache Line Prefetch set to OFF

Optimization for high-frequency FSB applications set to ON

All results used taskset to bind processes to cores.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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