



# SPEC® CINT2006 Result

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## Supermicro Motherboard X7DWN+

SPECint®\_rate2006 = 144

SPECint\_rate\_base2006 = 120

CPU2006 license: 001176

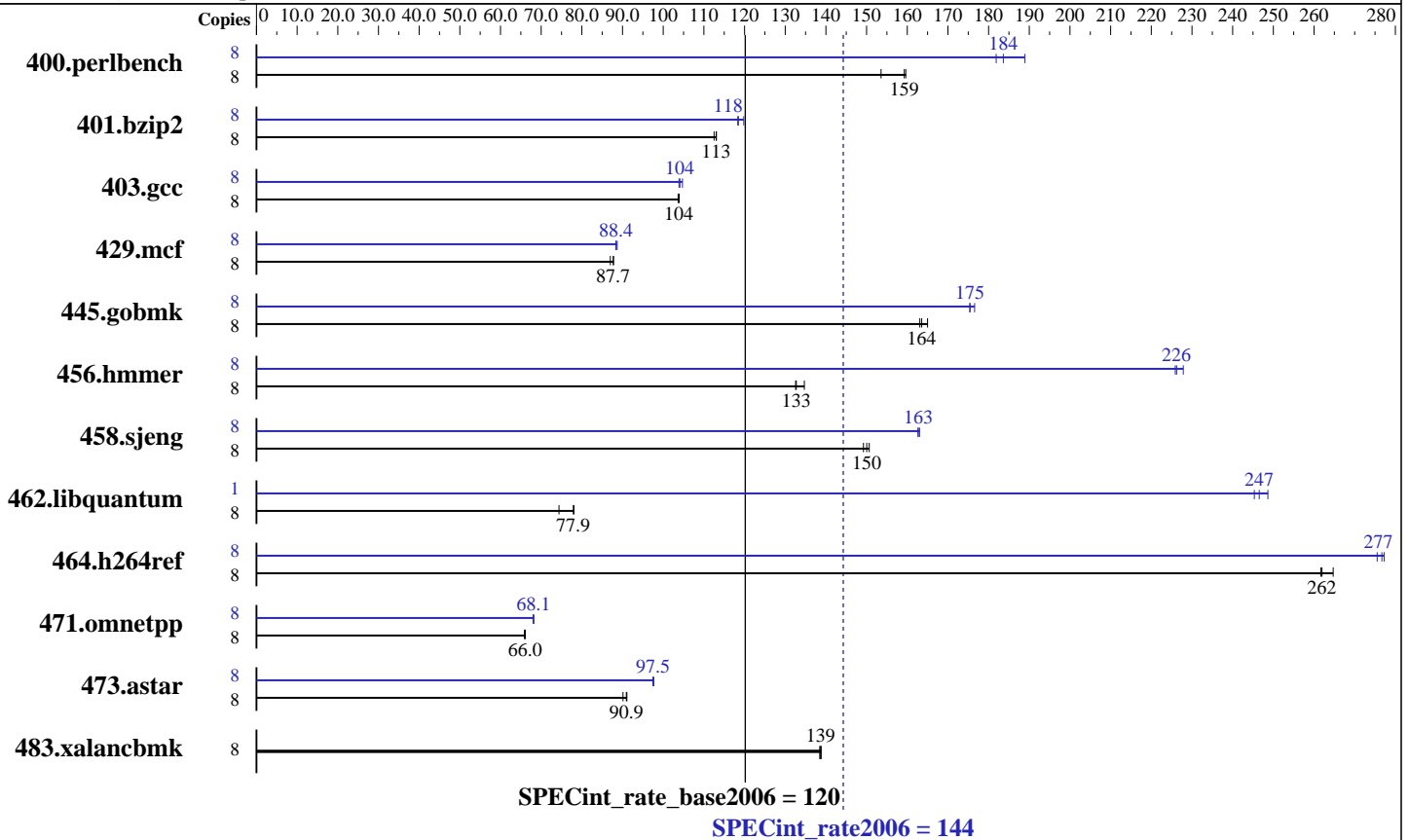
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5482  
 CPU Characteristics: Quad Core, 3.20GHz  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (16 \* 1 GB PC2-6400 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: Seagate Barracuda ST3750640NS, 750GB SATA, 7200RPM  
 Other Hardware: None

### Software

Operating System: 64-Bit Suse Linux Enterprise Server 10 w/ SP1  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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### Results Table

| Benchmark      | Base   |            |            |             |             |            |            | Peak   |             |            |            |             |            |             |
|----------------|--------|------------|------------|-------------|-------------|------------|------------|--------|-------------|------------|------------|-------------|------------|-------------|
|                | Copies | Seconds    | Ratio      | Seconds     | Ratio       | Seconds    | Ratio      | Copies | Seconds     | Ratio      | Seconds    | Ratio       | Seconds    | Ratio       |
| 400.perlbench  | 8      | 509        | 154        | <u>491</u>  | <u>159</u>  | 490        | 160        | 8      | 430         | 182        | <u>426</u> | <u>184</u>  | 414        | 189         |
| 401.bzip2      | 8      | <u>683</u> | <u>113</u> | 683         | 113         | 686        | 113        | 8      | <u>652</u>  | <u>118</u> | 652        | 118         | 644        | 120         |
| 403.gcc        | 8      | 620        | 104        | <u>620</u>  | <u>104</u>  | 621        | 104        | 8      | 615         | 105        | <u>619</u> | <u>104</u>  | 620        | 104         |
| 429.mcf        | 8      | 839        | 87.0       | <u>832</u>  | <u>87.7</u> | 831        | 87.8       | 8      | 826         | 88.4       | <u>825</u> | <u>88.4</u> | 823        | 88.7        |
| 445.gobmk      | 8      | 509        | 165        | 515         | 163         | <u>513</u> | <u>164</u> | 8      | 475         | 177        | 479        | 175         | <u>478</u> | <u>175</u>  |
| 456.hammer     | 8      | 554        | 135        | <u>562</u>  | <u>133</u>  | 563        | 133        | 8      | 330         | 226        | 328        | 228         | <u>330</u> | <u>226</u>  |
| 458.sjeng      | 8      | 643        | 151        | 649         | 149         | <u>645</u> | <u>150</u> | 8      | 595         | 163        | <u>595</u> | <u>163</u>  | 594        | 163         |
| 462.libquantum | 8      | 2229       | 74.4       | <u>2128</u> | <u>77.9</u> | 2124       | 78.0       | 1      | <u>84.0</u> | <u>247</u> | 83.3       | 249         | 84.5       | 245         |
| 464.h264ref    | 8      | 669        | 265        | 677         | 262         | <u>676</u> | <u>262</u> | 8      | <u>640</u>  | <u>277</u> | 639        | 277         | 643        | 276         |
| 471.omnetpp    | 8      | 759        | 65.9       | <u>757</u>  | <u>66.0</u> | 756        | 66.1       | 8      | 735         | 68.0       | <u>734</u> | <u>68.1</u> | 734        | 68.2        |
| 473.astar      | 8      | 623        | 90.1       | <u>618</u>  | <u>90.9</u> | 617        | 91.1       | 8      | 576         | 97.5       | 575        | 97.7        | <u>576</u> | <u>97.5</u> |
| 483.xalancbmk  | 8      | 398        | 139        | 399         | 138         | <u>398</u> | <u>139</u> | 8      | 398         | 139        | 399        | 138         | <u>398</u> | <u>139</u>  |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### General Notes

Tested systems can be used with CSE-825TQ-R700LPV case,  
To ensure system stability, a 500W (minimum)  
ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]  
Product description located as of  
<http://www.supermicro.com/products/motherboard/Xeon1333/5400/X7DWN+.cfm>  
The system bus runs at 1600 MHz  
BIOS Setting: Default

### Base Compiler Invocation

C benchmarks:  
icc  
C++ benchmarks:  
icpc

### Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc  
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include  
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include  
C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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