



SPEC® CINT2006 Result

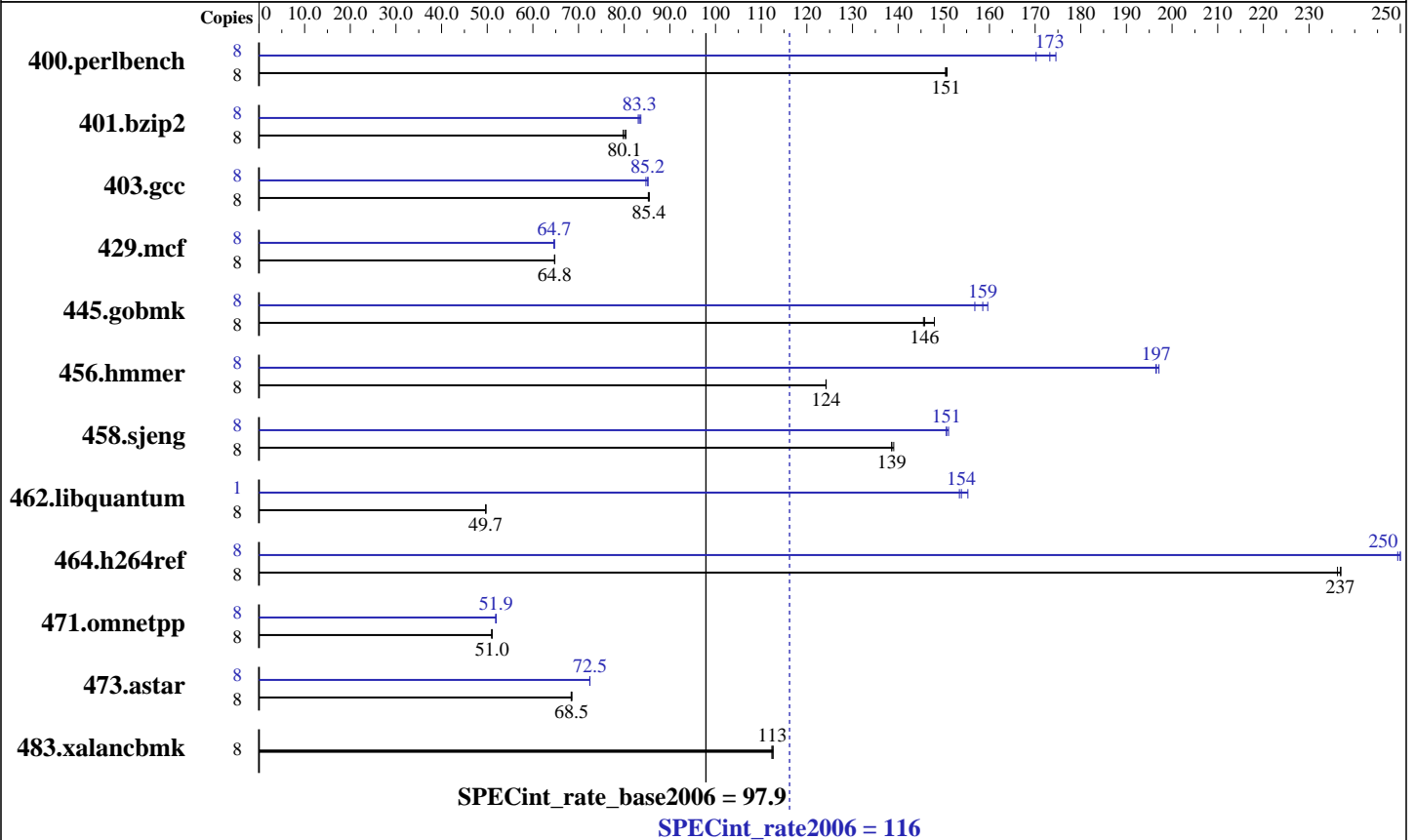
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Supermicro Motherboard X7DBT-INF

SPECint®_rate2006 = 116
SPECint_rate_base2006 = 97.9

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5365
 CPU Characteristics: Quad Core, 3.00GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 X 2GB ECC PC2-5300, CL5, FBDIMM)
 Disk Subsystem: Western Digital WD1600YS-01SHB1 160GB SATA II, 7200RPM
 Other Hardware: None

Software

Operating System: 64-Bit Suse Linux Enterprise Server 10 w/ SP1
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	519	151	520	150	519	151	8	459	170	448	175	451	173
401.bzip2	8	964	80.1	960	80.4	968	79.8	8	929	83.1	927	83.3	923	83.6
403.gcc	8	755	85.3	753	85.5	754	85.4	8	756	85.2	756	85.2	760	84.8
429.mcf	8	1126	64.8	1127	64.7	1126	64.8	8	1129	64.6	1127	64.7	1129	64.7
445.gobmk	8	576	146	577	146	567	148	8	526	160	529	159	535	157
456.hammer	8	601	124	601	124	601	124	8	379	197	380	196	380	197
458.sjeng	8	698	139	696	139	699	139	8	643	151	641	151	643	151
462.libquantum	8	3336	49.7	3337	49.7	3336	49.7	1	135	153	133	155	135	154
464.h264ref	8	747	237	749	236	747	237	8	708	250	710	249	708	250
471.omnetpp	8	981	51.0	979	51.1	980	51.0	8	963	51.9	964	51.9	963	51.9
473.astar	8	820	68.5	819	68.5	821	68.4	8	775	72.5	775	72.5	775	72.5
483.xalancbmk	8	490	113	491	113	491	112	8	490	113	491	113	491	112

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with SC-808T-980 case,
To ensure system stability, a 550W (minimum) ATX power supply
[4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of
<http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DBT-INF.cfm>
The system bus runs at 1333 MHz
BIOS Setting: Default

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.html>



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You can also download the XML flags source by saving the following link:

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For other inquiries, please contact webmaster@spec.org.

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