



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®\_rate2006 = 90.0

PowerEdge 2950 (Intel Xeon E5335, 2.00 GHz)

SPECint\_rate\_base2006 = 79.3

CPU2006 license: 55

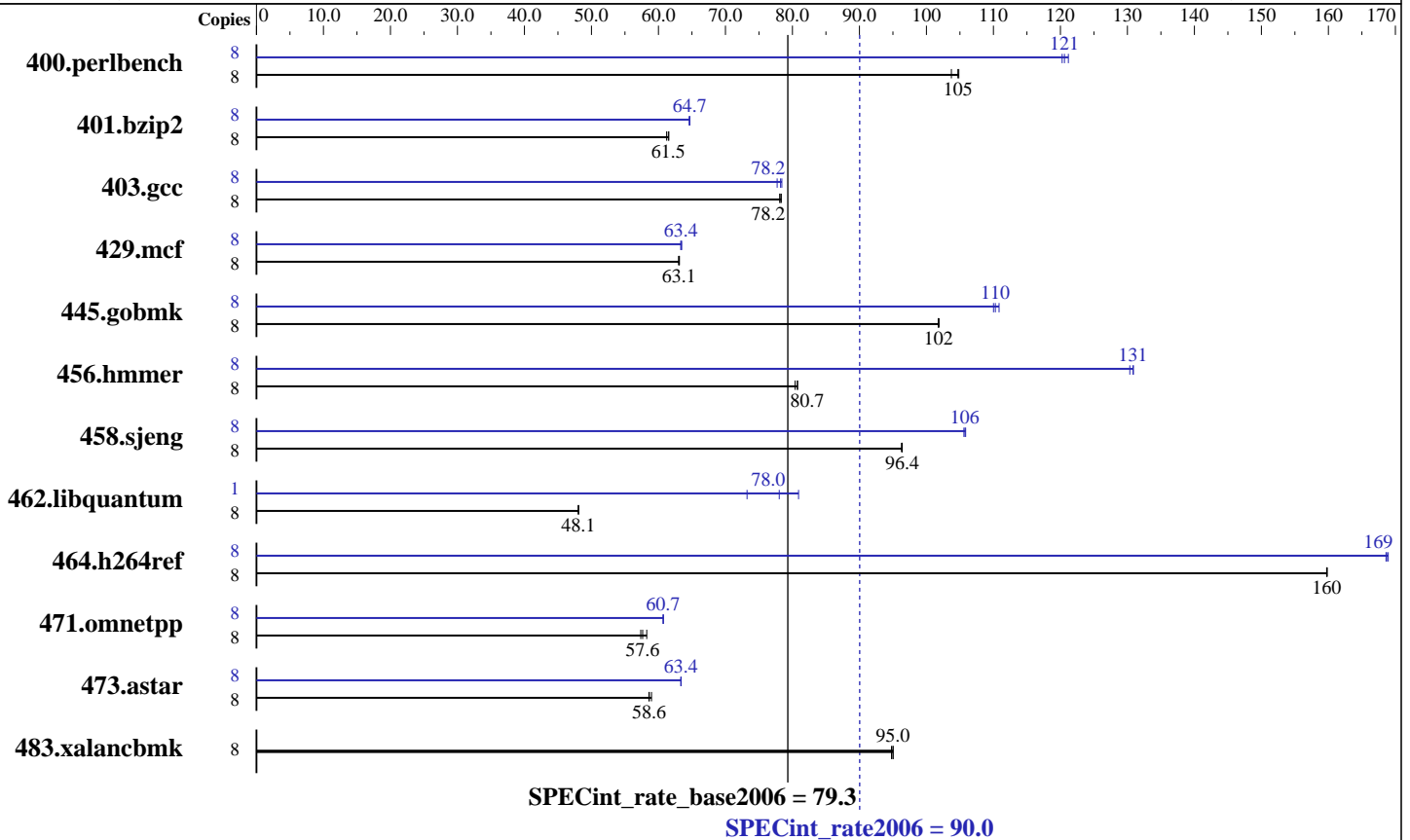
Test date: Sep-2007

Test sponsor: Dell Inc.

Hardware Availability: Dec-2006

Tested by: Dell Inc.

Software Availability: Nov-2007



## Hardware

CPU Name: Intel Xeon E5335  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB 667 MHz ECC CL5 FB-DIMM)  
 Disk Subsystem: 1 x 73 GB SAS 15k RPM  
 Other Hardware: None

## Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap library V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	754	104	746	105	<b>747</b>	<b>105</b>	8	<b>648</b>	<b>121</b>	645	121	650	120
401.bzip2	8	<b>1256</b>	<b>61.5</b>	1261	61.2	1254	61.6	8	<b>1194</b>	<b>64.7</b>	1195	64.6	1193	64.7
403.gcc	8	825	78.1	<b>824</b>	<b>78.2</b>	822	78.3	8	<b>823</b>	<b>78.2</b>	821	78.4	829	77.7
429.mcf	8	<b>1157</b>	<b>63.1</b>	1158	63.0	1156	63.1	8	<b>1152</b>	<b>63.4</b>	1152	63.4	1149	63.5
445.gobmk	8	825	102	<b>824</b>	<b>102</b>	824	102	8	757	111	<b>761</b>	<b>110</b>	763	110
456.hammer	8	928	80.4	<b>925</b>	<b>80.7</b>	924	80.8	8	570	131	<b>570</b>	<b>131</b>	573	130
458.sjeng	8	1005	96.3	1004	96.4	<b>1004</b>	<b>96.4</b>	8	914	106	917	106	<b>915</b>	<b>106</b>
462.libquantum	8	<b>3449</b>	<b>48.1</b>	3450	48.0	3448	48.1	1	283	73.2	256	80.9	<b>266</b>	<b>78.0</b>
464.h264ref	8	1108	160	1108	160	<b>1108</b>	<b>160</b>	8	1048	169	1050	169	<b>1049</b>	<b>169</b>
471.omnetpp	8	858	58.3	<b>867</b>	<b>57.6</b>	872	57.4	8	<b>823</b>	<b>60.7</b>	824	60.7	823	60.7
473.astar	8	959	58.6	952	59.0	<b>958</b>	<b>58.6</b>	8	<b>886</b>	<b>63.4</b>	887	63.3	886	63.4
483.xalancbmk	8	581	95.0	582	94.8	<b>581</b>	<b>95.0</b>	8	581	95.0	582	94.8	<b>581</b>	<b>95.0</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## Platform Notes

BIOS Settings:  
Adjacent Cache Line Prefetch = Disabled (default Enabled)  
Hardware Prefetcher = Disabled (default Enabled)

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 14:49:12 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 16 October 2007.