



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 60.3

ProLiant DL580 G4
(3.0 GHz, Intel Xeon processor 7120M)

SPECint_rate_base2006 = 56.5

CPU2006 license: 3

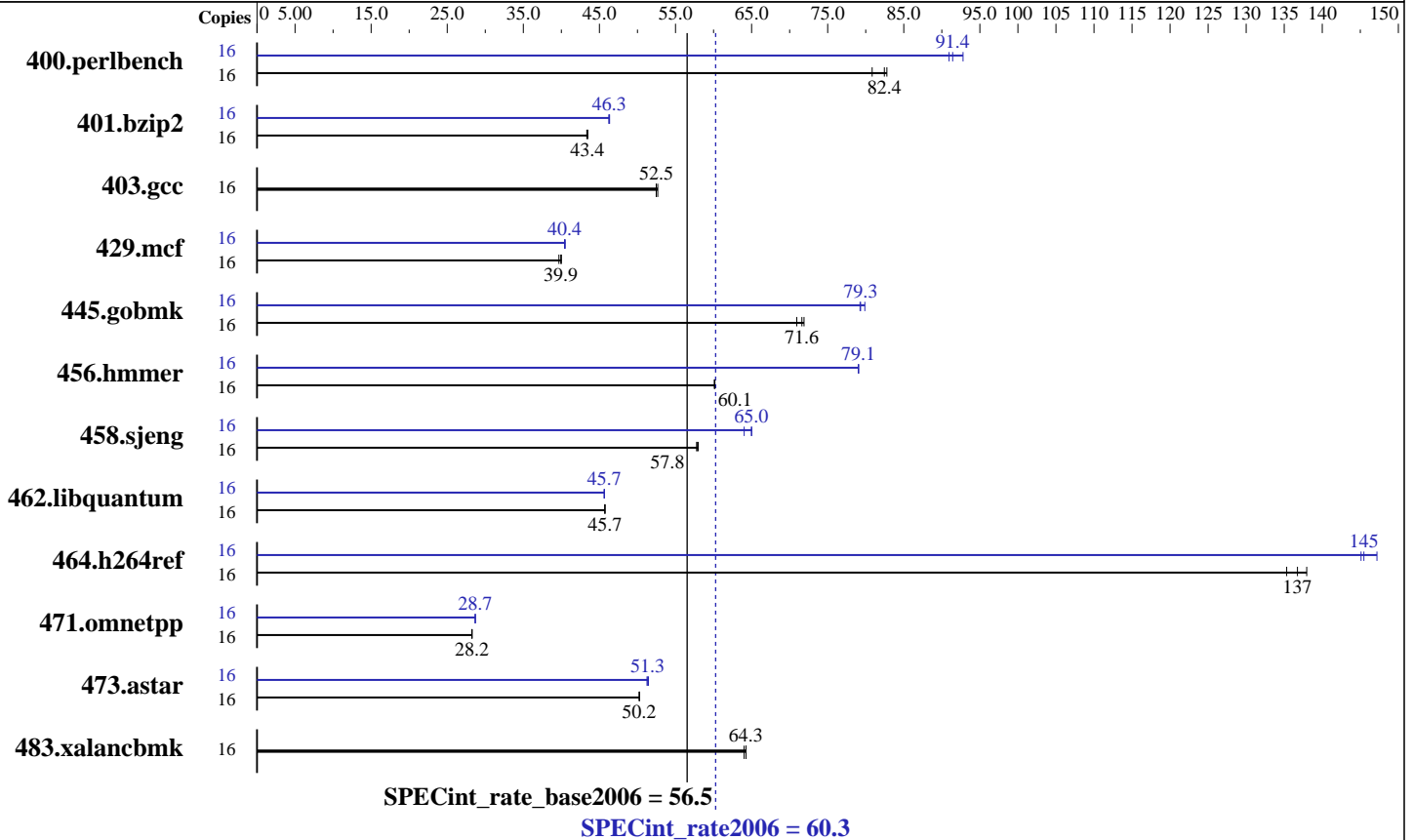
Test date: Jul-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Aug-2006

Tested by: Hewlett-Packard Company

Software Availability: May-2007



Hardware

CPU Name: Intel Xeon 7120M
 CPU Characteristics: 3.0 GHz, 800 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (16x2 GB PC2-3200R)
 Disk Subsystem: 1x72 GB 15K SAS
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64)
 kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for IA32/EM64T
 applications, Version 10.0
 Build 20070426 Package ID: l_cc_p_10.0.023
 Auto Parallel: No
 File System: ext2
 System State: Multi-user run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 8.1 libraries



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1888	82.8	1934	80.8	1896	82.4	16	1719	91.0	1685	92.8	1710	91.4
401.bzip2	16	3562	43.3	3552	43.5	3555	43.4	16	3336	46.3	3332	46.3	3339	46.2
403.gcc	16	2445	52.7	2455	52.5	2455	52.5	16	2445	52.7	2455	52.5	2455	52.5
429.mcf	16	3678	39.7	3655	39.9	3647	40.0	16	3610	40.4	3602	40.5	3609	40.4
445.gobmk	16	2366	70.9	2344	71.6	2335	71.9	16	2118	79.3	2101	79.9	2116	79.3
456.hammer	16	2483	60.1	2481	60.2	2483	60.1	16	1888	79.1	1889	79.0	1888	79.1
458.sjeng	16	3349	57.8	3339	58.0	3350	57.8	16	2978	65.0	3024	64.0	2980	65.0
462.libquantum	16	7250	45.7	7251	45.7	7251	45.7	16	7259	45.7	7268	45.6	7261	45.7
464.h264ref	16	2589	137	2617	135	2567	138	16	2406	147	2434	145	2440	145
471.omnetpp	16	3539	28.3	3540	28.2	3540	28.2	16	3486	28.7	3488	28.7	3485	28.7
473.astar	16	2236	50.2	2236	50.2	2234	50.3	16	2188	51.3	2183	51.4	2191	51.3
483.xalancbmk	16	1718	64.3	1725	64.0	1718	64.3	16	1718	64.3	1725	64.0	1718	64.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Power Regulator Disabled
Adjacent Sector Prefetch Enabled
Hardware Prefetcher Enabled
Hyper-Threading Technology Enabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xP -ipo -O3 -static -no-prec-div

C++ benchmarks:

-xP -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xP -ipo -O3
-no-prec-div -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xP -ipo -O3
-no-prec-div

403.gcc: basepeak = yes

429.mcf: -xP -ipo -O3 -static -no-prec-div -prefetch
-L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xP -O2 -ipo
-no-prec_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -xP -ipo -O3 -static
-no-prec-div -unroll2 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xP -ipo -O3 -static
-no-prec-div -unroll4

462.libquantum: Same as 458.sjeng

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xP -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.xml>



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