



SPEC® CINT2006 Result

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Intel Corporation

(Test Sponsor: Advanced Micro Devices)

Intel S5000PAL Server Board, Intel Xeon processor E5345, 2.33 GHz

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 58.8

CPU2006 license: 49

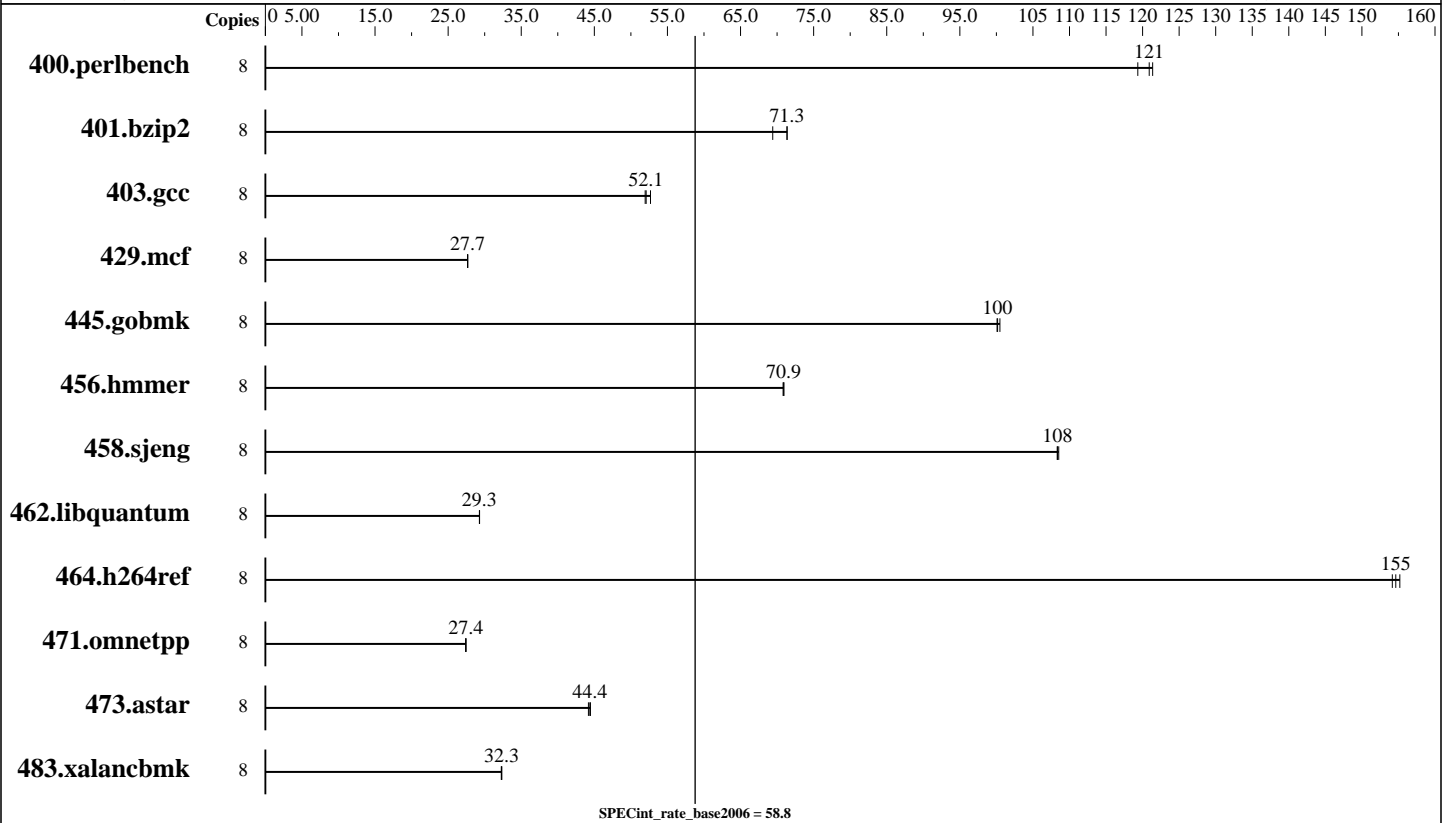
Test sponsor: Advanced Micro Devices

Tested by: Advanced Micro Devices

Test date: Sep-2007

Hardware Availability: May-2007

Software Availability: Jul-2007



Hardware

CPU Name: Intel Xeon E5345
 CPU Characteristics: 2333
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2GB, DDR2-667 FBDIMM CL5 Reg Dual Rank)
 Disk Subsystem: 1x250GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 SP1 64-bit kernel
 Compiler: gcc, g++ 4.1.2
 Auto Parallel: No
 File System: ext3
 System State: Multi-user, run level 3
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other Software: None



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	646	121	644	121	655	119							
401.bzip2	8	1082	71.3	1082	71.4	1112	69.4							
403.gcc	8	1236	52.1	1240	52.0	1222	52.7							
429.mcf	8	2638	27.7	2636	27.7	2636	27.7							
445.gobmk	8	838	100	838	100	835	100							
456.hammer	8	1053	70.9	1054	70.8	1052	70.9							
458.sjeng	8	892	109	893	108	894	108							
462.libquantum	8	5661	29.3	5657	29.3	5660	29.3							
464.h264ref	8	1148	154	1145	155	1141	155							
471.omnetpp	8	1824	27.4	1822	27.4	1823	27.4							
473.astar	8	1263	44.5	1271	44.2	1265	44.4							
483.xalancbmk	8	1711	32.3	1707	32.3	1708	32.3							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'numactl' was used to bind copies to the cores

General Notes

"powersave -f" used to set speed to maximum

The test system can be assembled using a Delta TDPS-600AB 600W 12V power supply.

Base Compiler Invocation

C benchmarks:

gcc

C++ benchmarks:

g++

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

-O3 -fno-inline-functions -funroll-loops

C++ benchmarks:

-O3 -funroll-loops

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.03.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.03.xml

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For other inquiries, please contact webmaster@spec.org.

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