



# SPEC® CINT2006 Result

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Dell Inc.  
(Test Sponsor: Intel Corporation)

SPECint®\_rate2006 = 26.8

XPS M1710 (Intel Core 2 Duo T7600)

SPECint\_rate\_base2006 = 23.9

CPU2006 license: 13

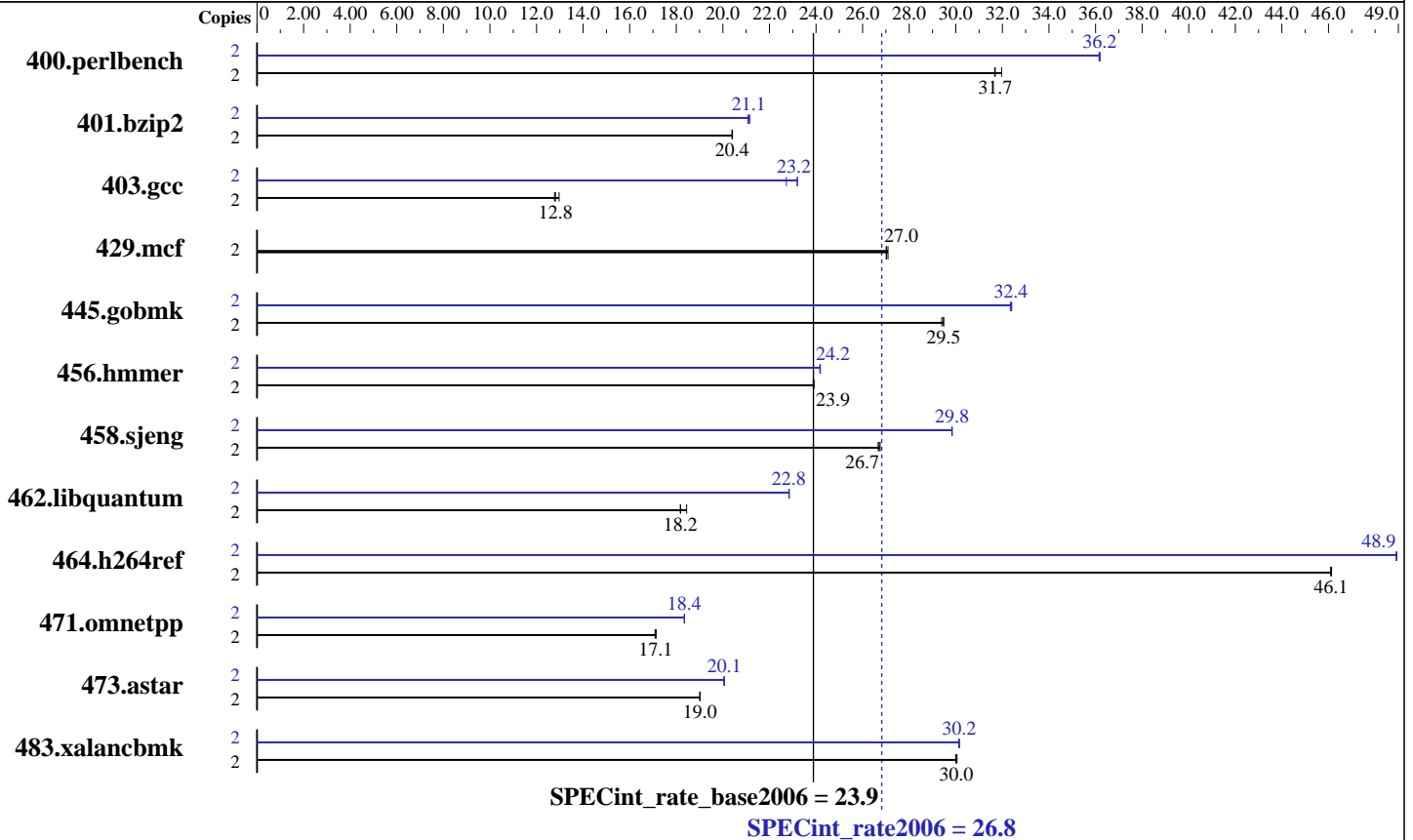
Test date: Jul-2007

Test sponsor: Intel Corporation

Hardware Availability: Jul-2007

Tested by: Intel Corporation

Software Availability: May-2007



## Hardware

CPU Name: Intel Core 2 Duo T7600  
 CPU Characteristics:  
 CPU MHz: 2333  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 2 GB (2x1GB Hynix DDR2-667 CL5)  
 Disk Subsystem: 100GB SATA, 7200 RPM  
 Other Hardware: None

## Software

Operating System: Windows Vista Ultimate (32-bit)  
 Compiler: Intel C++ Compiler for IA32 version 10.0  
 Build 20070426 Package ID: W\_CC\_P\_10.0.025  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: SmartHeap Library Version 8.0 from  
<http://www.microquill.com/>



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	611	32.0	<b>617</b>	<b>31.7</b>	617	31.7	2	541	36.2	540	36.2	<b>540</b>	<b>36.2</b>
401.bzip2	2	946	20.4	945	20.4	<b>946</b>	<b>20.4</b>	2	916	21.1	912	21.2	<b>914</b>	<b>21.1</b>
403.gcc	2	1242	13.0	<b>1256</b>	<b>12.8</b>	1261	12.8	2	708	22.7	<b>694</b>	<b>23.2</b>	694	23.2
429.mcf	2	<b>675</b>	<b>27.0</b>	675	27.0	673	27.1	2	<b>675</b>	<b>27.0</b>	675	27.0	673	27.1
445.gobmk	2	714	29.4	712	29.5	<b>712</b>	<b>29.5</b>	2	<b>648</b>	<b>32.4</b>	647	32.4	649	32.3
456.hammer	2	<b>781</b>	<b>23.9</b>	780	23.9	781	23.9	2	<b>772</b>	<b>24.2</b>	772	24.2	772	24.2
458.sjeng	2	<b>906</b>	<b>26.7</b>	905	26.8	907	26.7	2	<b>811</b>	<b>29.8</b>	811	29.8	811	29.8
462.libquantum	2	<b>2280</b>	<b>18.2</b>	2247	18.4	2280	18.2	2	<b>1815</b>	<b>22.8</b>	1813	22.9	1815	22.8
464.h264ref	2	959	46.1	<b>960</b>	<b>46.1</b>	960	46.1	2	905	48.9	<b>905</b>	<b>48.9</b>	904	48.9
471.omnetpp	2	<b>730</b>	<b>17.1</b>	731	17.1	730	17.1	2	682	18.3	681	18.4	<b>681</b>	<b>18.4</b>
473.astar	2	<b>738</b>	<b>19.0</b>	738	19.0	738	19.0	2	700	20.1	700	20.0	<b>700</b>	<b>20.1</b>
483.xalancbmk	2	<b>460</b>	<b>30.0</b>	460	30.0	459	30.1	2	<b>458</b>	<b>30.2</b>	458	30.1	458	30.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

nVidia GeForce Go 7950 discrete graphics card  
Binaries were built on Windows XP Professional SP2  
The start command with the /affinity switch was used to bind processes to cores

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

C++ benchmarks:  
-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Peak Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Peak Optimization Flags

C benchmarks:  
400.perlbench: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qprefetch /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE  
401.bzip2: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE  
403.gcc: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000  
-link /FORCE:MULTIPLE  
429.mcf: basepeak = yes  
445.gobmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxT -O2 -Qipo  
-Qprec\_div- -Qansi-alias /F512000000  
-link /FORCE:MULTIPLE

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## Peak Optimization Flags (Continued)

456.hmmer: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
-Qansi-alias /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

458.sjeng: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll4  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll4  
-Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:

-Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.47.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.47.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.

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