



SPEC[®] CFP2006 Result

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Itautec

SPECfp[®]_rate2006 = 24.3

Servidor Itautec MR201 (AMD Opteron 2210)

SPECfp_rate_base2006 = 24.3

CPU2006 license: 9001

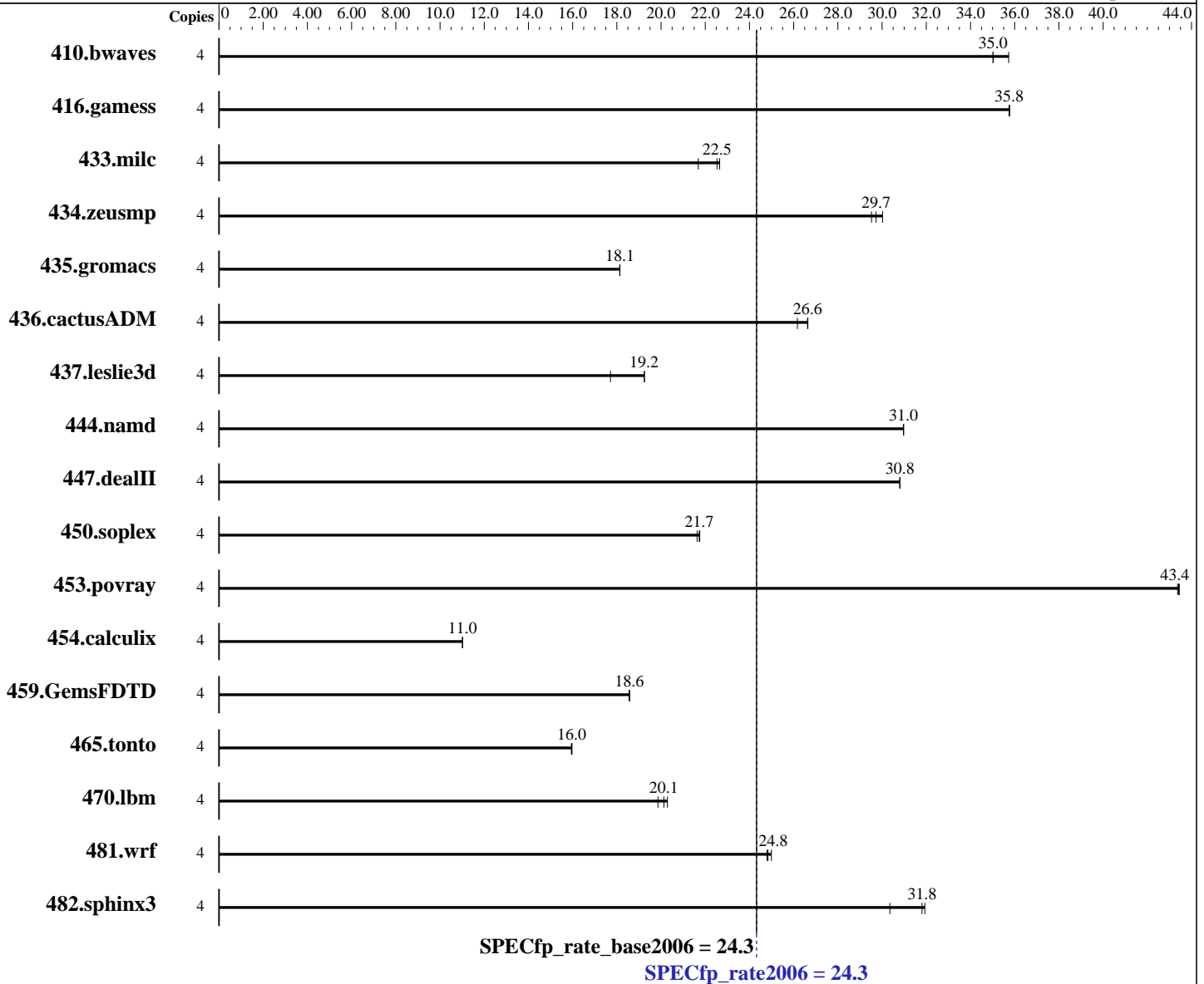
Test date: Aug-2007

Test sponsor: Itautec

Hardware Availability: Oct-2007

Tested by: Itautec

Software Availability: Sep-2006



Hardware

CPU Name: AMD Opteron 2210
 CPU Characteristics:
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core

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Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.032 Build no 20060927Z
 Intel Fortran Compiler for IA32 version 9.1
 Package ID W_FC_C_9.1.032 Build no 20060927Z
 Microsoft Visual Studio .NET 7.1.3088 (for libraries)
 Auto Parallel: No
 File System: NTFS

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L3 Cache: None
Other Cache: None
Memory: 16 GB (16x1GB, DDR2-533 CL5 ECC Dual Rank)
Disk Subsystem: 80 GB SATA, 7200RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: Microquill SmartHeap Library v.8.0 for SMP

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1521	35.7	<u>1552</u>	<u>35.0</u>	1552	35.0	4	1521	35.7	<u>1552</u>	<u>35.0</u>	1552	35.0
416.gamess	4	2189	35.8	2191	35.8	<u>2190</u>	<u>35.8</u>	4	2189	35.8	2191	35.8	<u>2190</u>	<u>35.8</u>
433.milc	4	1693	21.7	<u>1630</u>	<u>22.5</u>	1621	22.7	4	1693	21.7	<u>1630</u>	<u>22.5</u>	1621	22.7
434.zeusmp	4	<u>1225</u>	<u>29.7</u>	1233	29.5	1212	30.0	4	<u>1225</u>	<u>29.7</u>	1233	29.5	1212	30.0
435.gromacs	4	1575	18.1	<u>1575</u>	<u>18.1</u>	1574	18.1	4	1575	18.1	<u>1575</u>	<u>18.1</u>	1574	18.1
436.cactusADM	4	1827	26.2	<u>1795</u>	<u>26.6</u>	1794	26.6	4	1827	26.2	<u>1795</u>	<u>26.6</u>	1794	26.6
437.leslie3d	4	2123	17.7	1953	19.2	<u>1954</u>	<u>19.2</u>	4	2123	17.7	1953	19.2	<u>1954</u>	<u>19.2</u>
444.namd	4	1036	31.0	<u>1036</u>	<u>31.0</u>	1036	31.0	4	1036	31.0	<u>1036</u>	<u>31.0</u>	1036	31.0
447.dealII	4	1486	30.8	1485	30.8	<u>1485</u>	<u>30.8</u>	4	1486	30.8	1485	30.8	<u>1485</u>	<u>30.8</u>
450.soplex	4	1534	21.8	1542	21.6	<u>1536</u>	<u>21.7</u>	4	1534	21.8	1542	21.6	<u>1536</u>	<u>21.7</u>
453.povray	4	490	43.4	491	43.4	<u>490</u>	<u>43.4</u>	4	490	43.4	491	43.4	<u>490</u>	<u>43.4</u>
454.calculix	4	2995	11.0	2995	11.0	<u>2995</u>	<u>11.0</u>	4	2995	11.0	2995	11.0	<u>2995</u>	<u>11.0</u>
459.GemsFDTD	4	<u>2286</u>	<u>18.6</u>	2284	18.6	2287	18.6	4	<u>2286</u>	<u>18.6</u>	2284	18.6	2287	18.6
465.tonto	4	2465	16.0	<u>2466</u>	<u>16.0</u>	2469	15.9	4	2465	16.0	<u>2466</u>	<u>16.0</u>	2469	15.9
470.lbm	4	<u>2730</u>	<u>20.1</u>	2767	19.9	2709	20.3	4	<u>2730</u>	<u>20.1</u>	2767	19.9	2709	20.3
481.wrf	4	<u>1799</u>	<u>24.8</u>	1802	24.8	1788	25.0	4	<u>1799</u>	<u>24.8</u>	1802	24.8	1788	25.0
482.sphinx3	4	<u>2451</u>	<u>31.8</u>	2441	31.9	2569	30.4	4	<u>2451</u>	<u>31.8</u>	2441	31.9	2569	30.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort



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Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DBOOST_NO_INTRINSIC_WCHAR_T
-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-arch:SSE2 -Qprec-div- -O3 -Qipo /F950000000 shlSMPMt.lib
-link /FORCE:MULTIPLE

C++ benchmarks:
-arch:SSE2 -Qprec-div- -O3 -Qipo -Oi- -Qcxx_features /F950000000
shlSMPMt.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-arch:SSE2 -Qprec-div- -O3 -Qipo -Oi- /F950000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-arch:SSE2 -Qprec-div- -O3 -Qipo -Oi- /F950000000
-link /FORCE:MULTIPLE

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes
447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: basepeak = yes

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Peak Optimization Flags (Continued)

Fortran benchmarks:

- 410.bwaves: basepeak = yes
- 416.gamess: basepeak = yes
- 434.zeusmp: basepeak = yes
- 437.leslie3d: basepeak = yes
- 459.GemsFDTD: basepeak = yes
- 465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

- 435.gromacs: basepeak = yes
- 436.cactusADM: basepeak = yes
- 454.calculix: basepeak = yes
- 481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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