



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint®2006 = 20.8

IBM System x3650 (Intel Xeon X5365)

SPECint\_base2006 = 18.8

CPU2006 license: 11

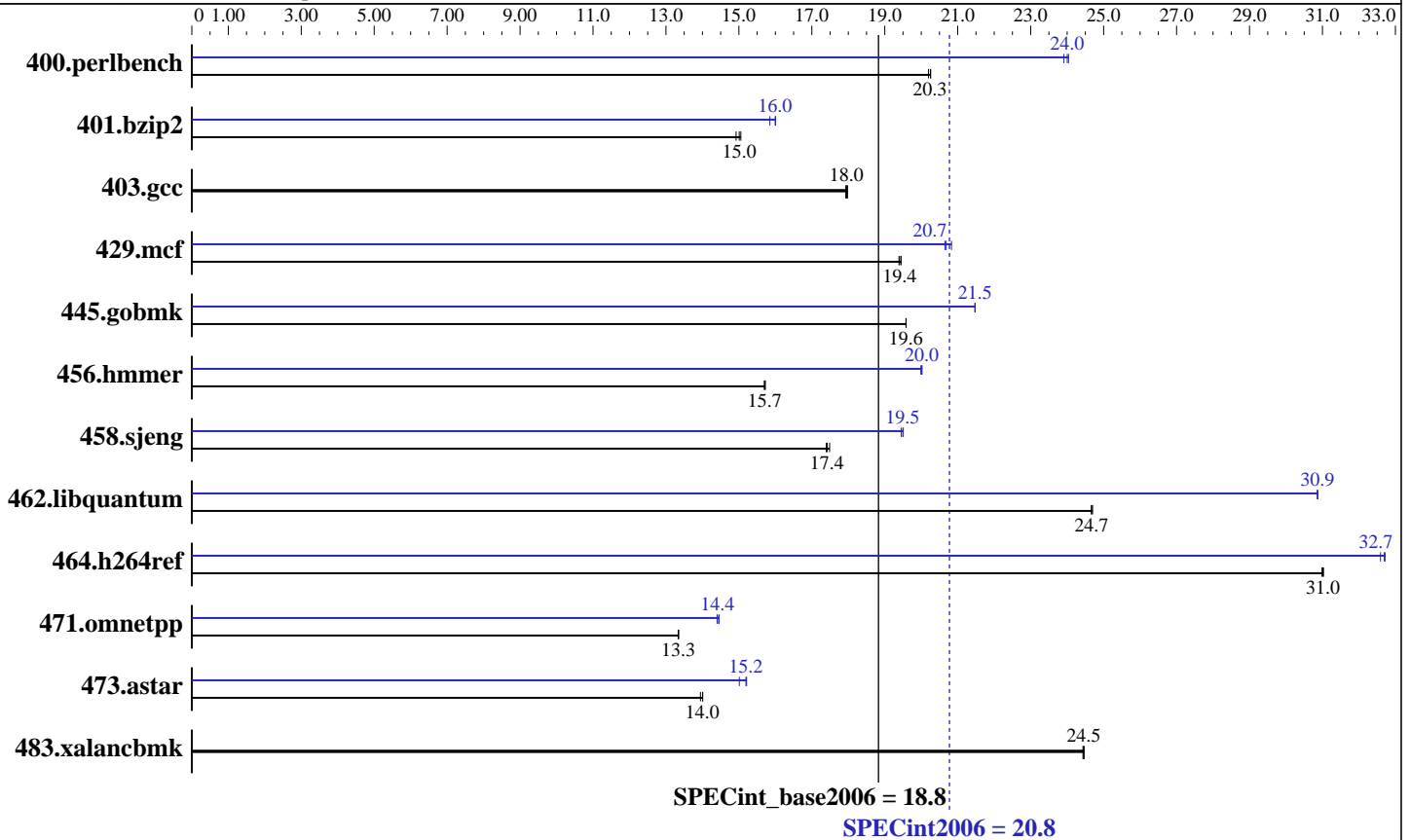
Test date: Aug-2007

Test sponsor: IBM Corporation

Hardware Availability: Sep-2007

Tested by: IBM Corporation

Software Availability: Jul-2007



## Hardware

CPU Name: Intel Xeon X5365  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)  
 Disk Subsystem: 1 x 36 GB SAS, 15000 RPM  
 Other Hardware: None

## Software

Operating System: SLES 10 (x86\_64), 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler for Linux version 10.0  
 Build 20070426 Package ID: 1\_cc\_p\_10.0.023  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap 8.1



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 20.8

IBM System x3650 (Intel Xeon X5365)

SPECint\_base2006 = 18.8

CPU2006 license: 11

Test date: Aug-2007

Test sponsor: IBM Corporation

Hardware Availability: Sep-2007

Tested by: IBM Corporation

Software Availability: Jul-2007

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	482	20.3	484	20.2	<u>482</u>	<u>20.3</u>	406	24.0	409	23.9	<u>407</u>	<u>24.0</u>
401.bzip2	641	15.1	647	14.9	<u>643</u>	<u>15.0</u>	609	15.8	<u>603</u>	<u>16.0</u>	603	16.0
403.gcc	<u>448</u>	<u>18.0</u>	449	17.9	448	18.0	<u>448</u>	<u>18.0</u>	449	17.9	448	18.0
429.mcf	470	19.4	469	19.4	<u>470</u>	<u>19.4</u>	438	20.8	442	20.6	<u>441</u>	<u>20.7</u>
445.gobmk	536	19.6	<u>536</u>	<u>19.6</u>	536	19.6	488	21.5	489	21.5	<u>488</u>	<u>21.5</u>
456.hammer	594	15.7	<u>594</u>	<u>15.7</u>	593	15.7	467	20.0	466	20.0	<u>466</u>	<u>20.0</u>
458.sjeng	696	17.4	692	17.5	<u>695</u>	<u>17.4</u>	622	19.4	620	19.5	<u>621</u>	<u>19.5</u>
462.libquantum	839	24.7	<u>840</u>	<u>24.7</u>	840	24.7	671	30.9	<u>671</u>	<u>30.9</u>	671	30.9
464.h264ref	714	31.0	<u>714</u>	<u>31.0</u>	713	31.0	676	32.7	<u>677</u>	<u>32.7</u>	679	32.6
471.omnetpp	<u>468</u>	<u>13.3</u>	468	13.3	469	13.3	<u>433</u>	<u>14.4</u>	432	14.5	434	14.4
473.astar	503	13.9	<u>501</u>	<u>14.0</u>	501	14.0	<u>462</u>	<u>15.2</u>	468	15.0	461	15.2
483.xalancbmk	<u>282</u>	<u>24.5</u>	282	24.4	282	24.5	<u>282</u>	<u>24.5</u>	282	24.4	282	24.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs

-L/spec/cpu2006.1.0/lib -lsmartheap



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 20.8

IBM System x3650 (Intel Xeon X5365)

SPECint\_base2006 = 18.8

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Aug-2007

Hardware Availability: Sep-2007

Software Availability: Jul-2007

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec\_div -ansi-alias

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 20.8

IBM System x3650 (Intel Xeon X5365)

SPECint\_base2006 = 18.8

CPU2006 license: 11

Test date: Aug-2007

Test sponsor: IBM Corporation

Hardware Availability: Sep-2007

Tested by: IBM Corporation

Software Availability: Jul-2007

## Peak Optimization Flags (Continued)

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0  
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec\_div -ansi-alias -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.45.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.45.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 13:13:14 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 4 September 2007.