SPEC® CINT2006 Result

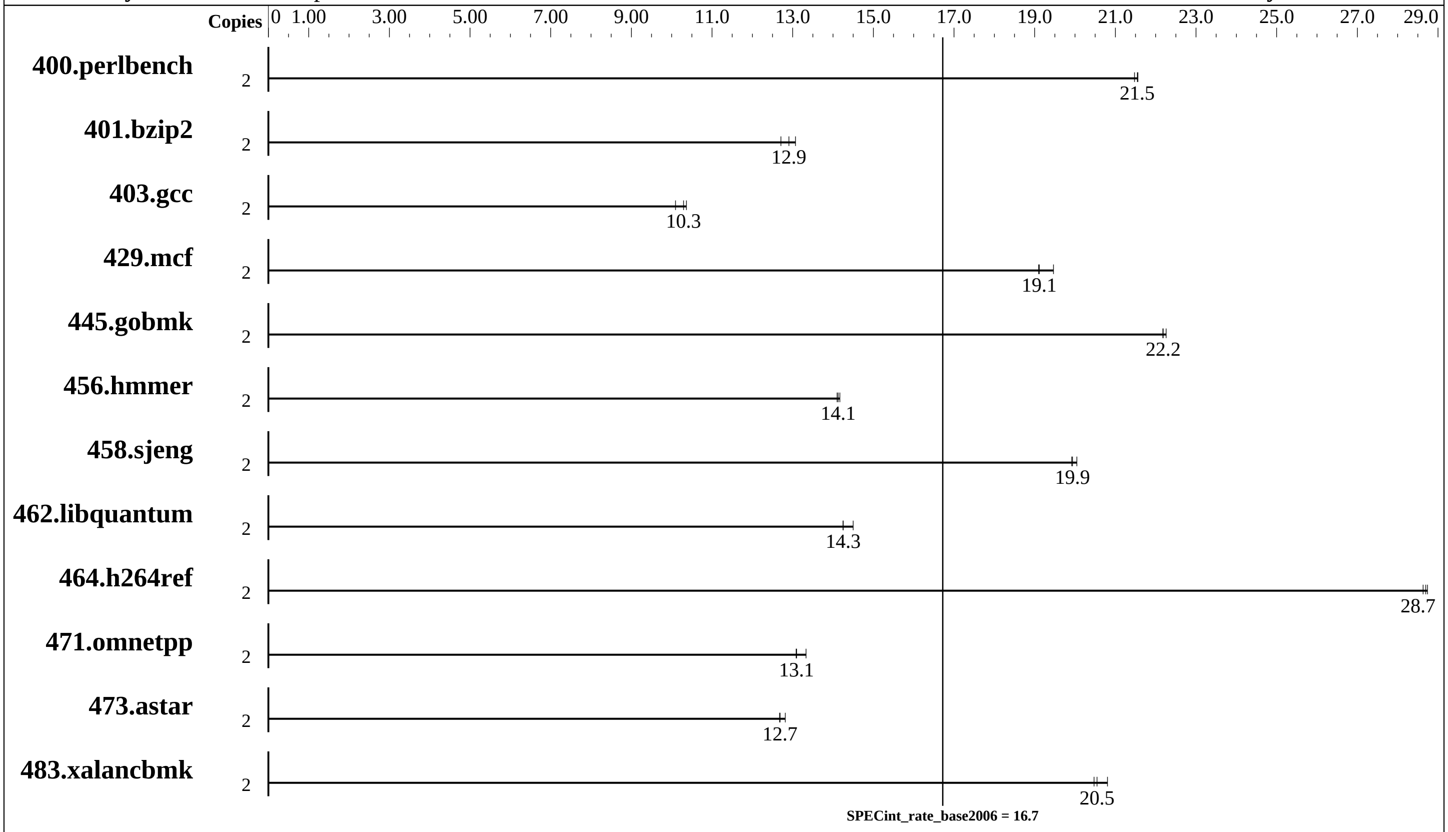
Lenovo Group Limited
(Test Sponsor: Intel Corporation)

Lenovo ThinkPad T43(Intel Core Duo T2400)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 16.7

CPU2006 license: 13		**Test date:**	Jun-2007
Test sponsor:	Intel Corporation	**Hardware Availability:**	Jun-2007
Tested by:	Intel Corporation	**Software Availability:**	Feb-2007



Hardware

CPU Name:	Intel Core Duo T2400
CPU Characteristics:	1.83 GHz 2MB L2
CPU MHz:	1833
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1 GB Qimonda* PC-5300 DDR2-667 5-5-5-12)
Disk Subsystem:	Hitachi* HTS721010G9 SATA 100GB 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Vista32 Business
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	SmartHeap library V7.4 from Microquill

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	2	**907**	**21.5**	910	21.5	906	21.6							
401.bzip2	2	1519	12.7	**1495**	**12.9**	1477	13.1							
403.gcc	2	**1564**	**10.3**	1595	10.1	1553	10.4							
429.mcf	2	**954**	**19.1**	955	19.1	937	19.5							
445.gobmk	2	**946**	**22.2**	946	22.2	943	22.3							
456.hmmer	2	**1321**	**14.1**	1323	14.1	1317	14.2							
458.sjeng	2	**1214**	**19.9**	1215	19.9	1207	20.0							
462.libquantum	2	2908	14.2	**2907**	**14.3**	2858	14.5							
464.h264ref	2	**1542**	**28.7**	1546	28.6	1540	28.7							
471.omnetpp	2	955	13.1	**954**	**13.1**	938	13.3							
473.astar	2	**1107**	**12.7**	1108	12.7	1096	12.8							
483.xalancbmk	2	**672**	**20.5**	674	20.5	663	20.8							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance
```

Base Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
`-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
      -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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