



# SPEC® CINT2006 Result

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Lenovo Group Limited

(Test Sponsor: Intel Corporation)

SPECint®2006 = Not Run

Lenovo ThinkPad T61(Intel Core 2 Duo T7700)

SPECint\_base2006 = 14.9

CPU2006 license: 13

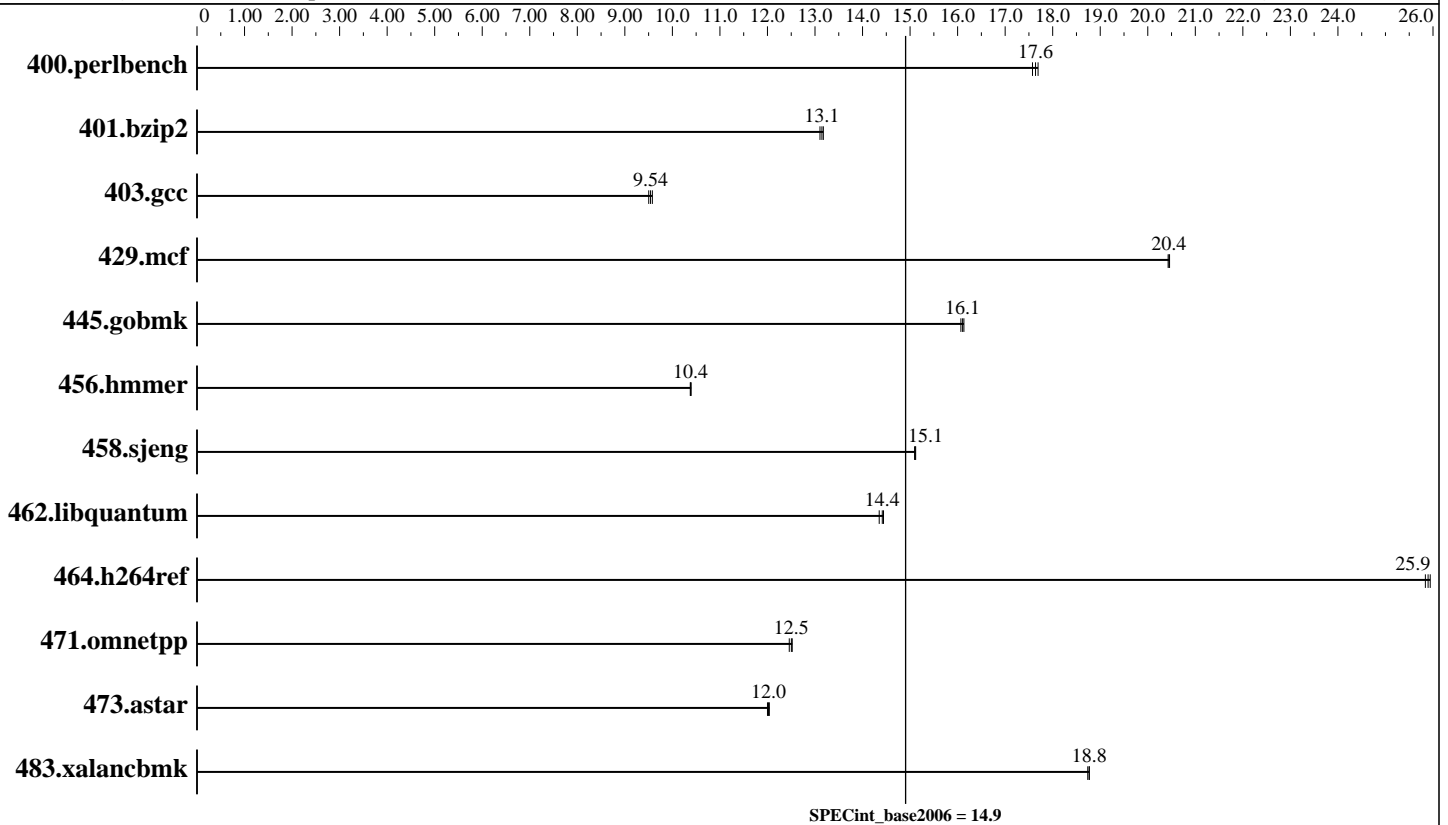
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Jun-2007

Software Availability: Feb-2007



## Hardware

CPU Name: Intel Core 2 Duo T7700  
 CPU Characteristics: 2.40 GHz, 4MB L2, 800 MHz Bus  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 2 GB (2x1GB Qimonda\* PC-5300 DDR2-667 5-5-5-12)  
 Disk Subsystem: Hitachi\* HTS721010G9 SATA 100GB 7200RPM  
 Other Hardware: None

## Software

Operating System: Windows Vista Business  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Build no 20060816  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: SmartHeap library V7.4 from Microquill



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	552	17.7	<b>554</b>	<b>17.6</b>	556	17.6						
401.bzip2	<b>734</b>	<b>13.1</b>	736	13.1	732	13.2						
403.gcc	<b>844</b>	<b>9.54</b>	840	9.58	847	9.50						
429.mcf	<b>446</b>	<b>20.4</b>	446	20.5	446	20.4						
445.gobmk	650	16.1	653	16.1	<b>651</b>	<b>16.1</b>						
456.hammer	899	10.4	898	10.4	<b>899</b>	<b>10.4</b>						
458.sjeng	800	15.1	<b>801</b>	<b>15.1</b>	802	15.1						
462.libquantum	1435	14.4	1444	14.4	<b>1437</b>	<b>14.4</b>						
464.h264ref	856	25.8	<b>855</b>	<b>25.9</b>	853	25.9						
471.omnetpp	499	12.5	502	12.5	<b>500</b>	<b>12.5</b>						
473.astar	583	12.0	585	12.0	<b>584</b>	<b>12.0</b>						
483.xalancbmk	368	18.8	<b>368</b>	<b>18.8</b>	368	18.7						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Binaries built on Windows XP Professional w/ SP2  
Vista Power Profile set to High Performance

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99  
C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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