



SPEC® CINT2006 Result

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Lenovo Group Limited

(Test Sponsor: Intel Corporation)

SPECint®2006 =

Not Run

Lenovo ThinkPad T61(Intel Core 2 Duo T7700)

SPECint_base2006 =

15.6

CPU2006 license: 13

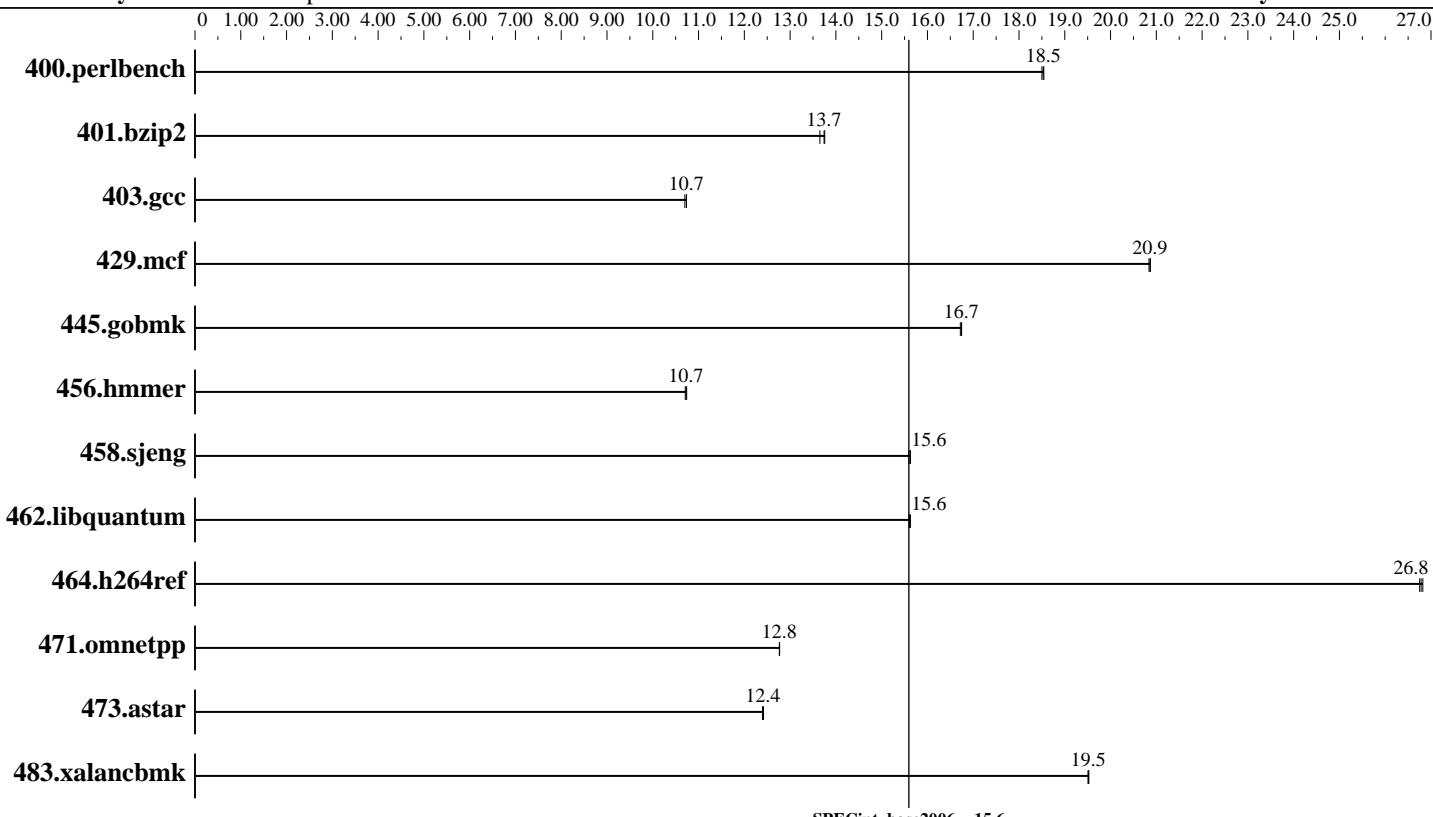
Test date: Jun-2007

Test sponsor: Intel Corporation

Hardware Availability: Jun-2007

Tested by: Intel Corporation

Software Availability: Feb-2007



Hardware

CPU Name: Intel Core 2 Duo T7700
 CPU Characteristics: 2.40 GHz, 4MB L2, 800 MHz Bus
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Qimonda® PC-5300 DDR2-667 5-5-5-12)
 Disk Subsystem: Hitachi® HTS721010G9 SATA 100GB 7200RPM
 Other Hardware: None

Software

Operating System: Windows XP Professional w/ SP2
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: SmartHeap library V7.4 from Microquill



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	527	18.5	527	18.5	528	18.5						
401.bzip2	707	13.7	702	13.8	702	13.7						
403.gcc	752	10.7	750	10.7	750	10.7						
429.mcf	437	20.9	437	20.9	438	20.8						
445.gobmk	627	16.7	626	16.7	627	16.7						
456.hammer	870	10.7	871	10.7	869	10.7						
458.sjeng	774	15.6	775	15.6	776	15.6						
462.libquantum	1326	15.6	1328	15.6	1327	15.6						
464.h264ref	825	26.8	827	26.8	826	26.8						
471.omnetpp	490	12.8	489	12.8	490	12.8						
473.astar	566	12.4	565	12.4	566	12.4						
483.xalancbmk	354	19.5	354	19.5	353	19.5						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries built on Windows XP Professional w/ SP2
XP Power Profile set to AC/High Performance

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib

-link /FORCE:MULTIPLE

Continued on next page



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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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