SPEC® CINT2006 Result
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IBM Corporation

IBM BladeCenter HS21 XM (Intel Xeon E5320)

$SPECint^{®}2006 = 13.6$
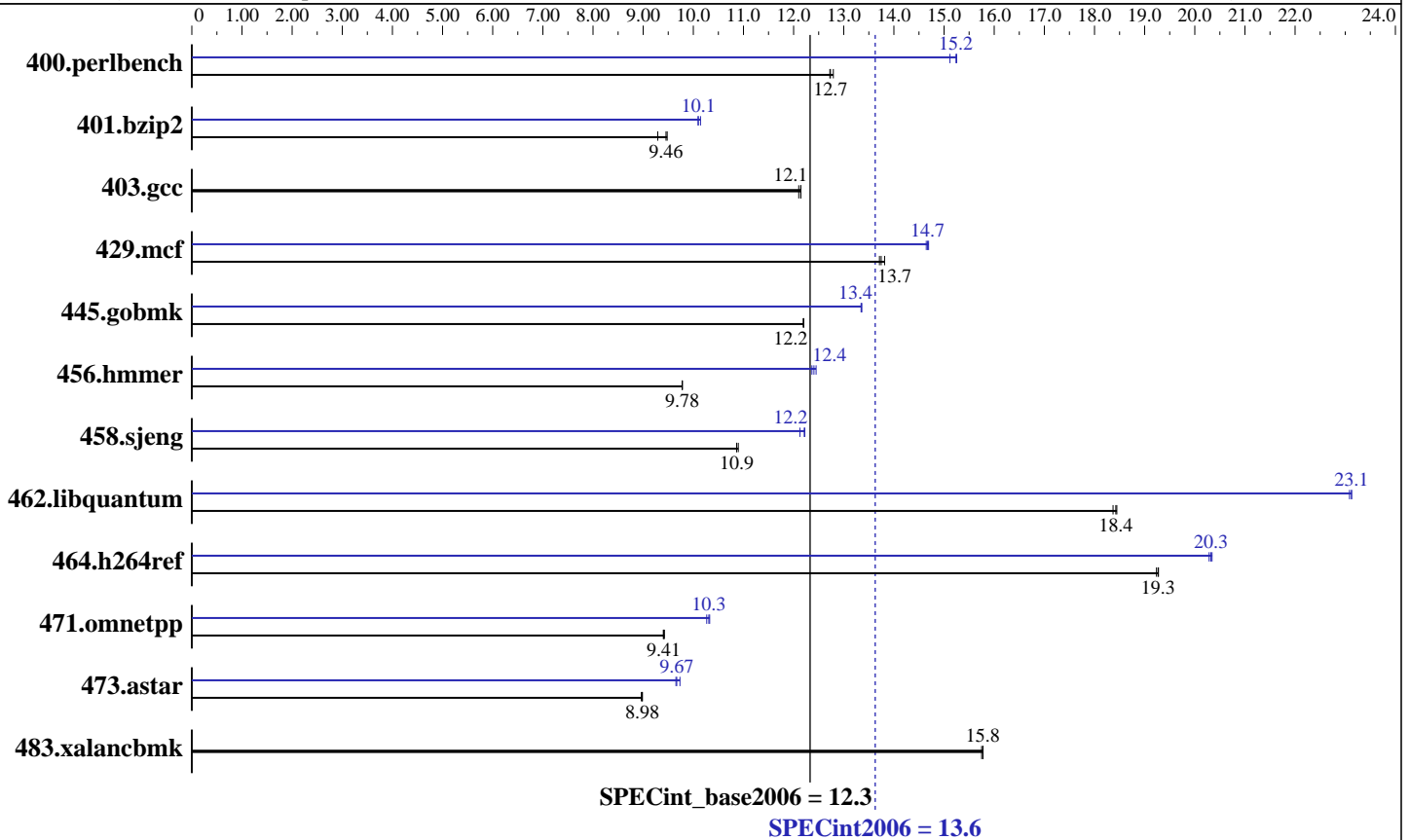
$SPECint\_base2006 = 12.3$

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jul-2007
Hardware Availability: Feb-2007
Software Availability: Jul-2007

Benchmark results (blue = peak, black = base):

- 400.perlbench: 15.2 / 12.7
- 401.bzip2: 10.1 / 9.46
- 403.gcc: 12.1
- 429.mcf: 14.7 / 13.7
- 445.gobmk: 13.4 / 12.2
- 456.hmmer: 12.4 / 9.78
- 458.sjeng: 12.2 / 10.9
- 462.libquantum: 23.1 / 18.4
- 464.h264ref: 20.3 / 19.3
- 471.omnetpp: 10.3 / 9.41
- 473.astar: 9.67 / 8.98
- 483.xalancbmk: 15.8

SPECint_base2006 = 12.3
SPECint2006 = 13.6

Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1066MHz system bus
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2GB DDR2-5300F ECC)
Disk Subsystem:	1 x 36 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	SLES 10 (x86_64), 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Linux version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap 8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	764	12.8	**767**	**12.7**	768	12.7	641	15.3	**641**	**15.2**	646	15.1
401.bzip2	1039	9.29	1018	9.48	**1021**	**9.46**	956	10.1	**955**	**10.1**	951	10.1
403.gcc	**663**	**12.1**	665	12.1	663	12.1	**663**	**12.1**	665	12.1	663	12.1
429.mcf	665	13.7	**663**	**13.7**	660	13.8	623	14.6	621	14.7	**622**	**14.7**
445.gobmk	860	12.2	**860**	**12.2**	860	12.2	786	13.3	785	13.4	**785**	**13.4**
456.hmmer	**954**	**9.78**	954	9.78	954	9.78	755	12.4	749	12.4	**752**	**12.4**
458.sjeng	1110	10.9	1114	10.9	**1114**	**10.9**	998	12.1	990	12.2	**991**	**12.2**
462.libquantum	1128	18.4	**1125**	**18.4**	1123	18.4	898	23.1	896	23.1	**896**	**23.1**
464.h264ref	1148	19.3	1150	19.2	**1149**	**19.3**	1091	20.3	**1089**	**20.3**	1088	20.3
471.omnetpp	665	9.40	663	9.42	**664**	**9.41**	**606**	**10.3**	609	10.3	605	10.3
473.astar	783	8.96	781	8.99	**782**	**8.98**	727	9.65	**726**	**9.67**	721	9.73
483.xalancbmk	438	15.7	437	15.8	**437**	**15.8**	438	15.7	437	15.8	**437**	**15.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
    `icc`

C++ benchmarks:
    `icpc`

Base Portability Flags

    400.perlbench: `-DSPEC_CPU_LINUX_IA32`
    462.libquantum: `-DSPEC_CPU_LINUX`
    483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
    `-fast`

C++ benchmarks:
    `-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
    `-L/spec/cpu2006.1.0/lib -lsmartheap`

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Base Other Flags

C benchmarks:

        403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
    `icc`

    401.bzip2: `/opt/intel/cce/10.0.023/bin/icc`
            `-L/opt/intel/cce/10.0.023/lib`
            `-I/opt/intel/cce/10.0.023/include`

    456.hmmer: `/opt/intel/cce/10.0.023/bin/icc`
            `-L/opt/intel/cce/10.0.023/lib`
            `-I/opt/intel/cce/10.0.023/include`

C++ benchmarks:
    `icpc`

Peak Portability Flags

    400.perlbench: `-DSPEC_CPU_LINUX_IA32`
        401.bzip2: `-DSPEC_CPU_LP64`
        456.hmmer: `-DSPEC_CPU_LP64`
    462.libquantum: `-DSPEC_CPU_LINUX`
    483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias`
            `-prefetch`

        401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast`

        403.gcc: `basepeak = yes`

        429.mcf: `-fast -prefetch`

        445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo`
            `-no-prec_div -ansi-alias`

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Peak Optimization Flags (Continued)

456.hmmer: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias`

458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

462.libquantum: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:

471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/spec/cpu2006.1.0/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.44.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.44.xml

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 12:32:14 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 August 2007.