



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

### SPECint®\_rate2006 = 45.7

PRIMERGY TX200 S3, Intel Xeon processor 5130, 2.0 GHz

### SPECint\_rate\_base2006 = 41.9

CPU2006 license: 22

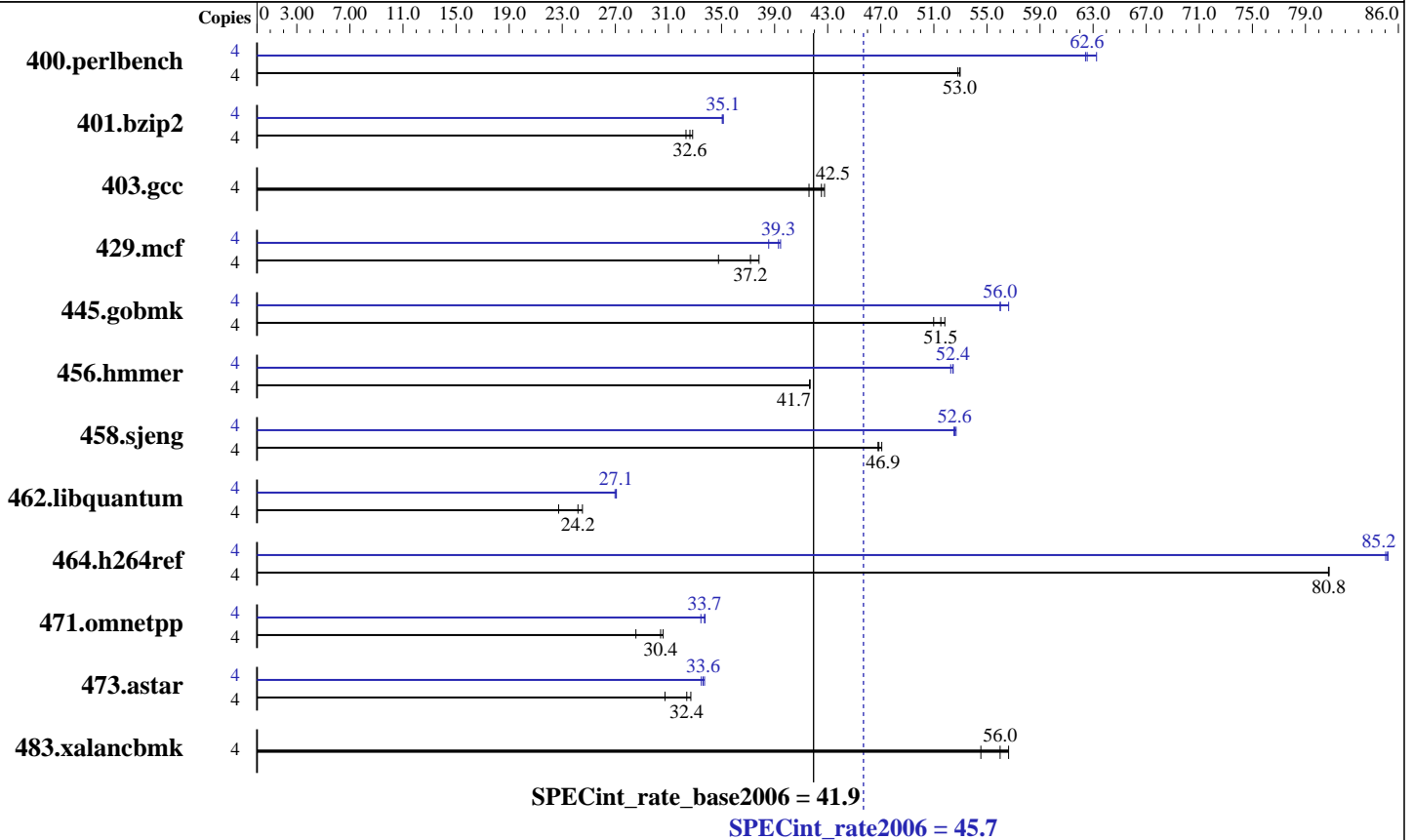
Test date: Jul-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Jul-2006

Tested by: Fujitsu Siemens Computers

Software Availability: Jun-2007



### Hardware

CPU Name: Intel Xeon 5130  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: Seagate ST336754SS (SAS, 36GB, 15000rpm)  
 Other Hardware: None

### Software

Operating System: SUSE LINUX Enterprise Server 10 (x86\_64), Kernel 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: I\_cc\_p\_10.0.023  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1  
 binutils-2.17.tar.gz, Version 2.17



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>738</b>	<b>53.0</b>	740	52.8	738	53.0	4	<b>625</b>	<b>62.6</b>	618	63.3	626	62.4
401.bzip2	4	1194	32.3	<b>1183</b>	<b>32.6</b>	1176	32.8	4	1099	35.1	1101	35.1	<b>1099</b>	<b>35.1</b>
403.gcc	4	774	41.6	<b>757</b>	<b>42.5</b>	753	42.8	4	774	41.6	<b>757</b>	<b>42.5</b>	753	42.8
429.mcf	4	1049	34.8	<b>981</b>	<b>37.2</b>	964	37.8	4	946	38.6	<b>929</b>	<b>39.3</b>	925	39.4
445.gobmk	4	<b>814</b>	<b>51.5</b>	823	51.0	810	51.8	4	749	56.0	741	56.6	<b>749</b>	<b>56.0</b>
456.hmmmer	4	896	41.6	896	41.7	<b>896</b>	<b>41.7</b>	4	712	52.4	714	52.3	<b>712</b>	<b>52.4</b>
458.sjeng	4	1034	46.8	1028	47.1	<b>1033</b>	<b>46.9</b>	4	919	52.7	<b>921</b>	<b>52.6</b>	922	52.5
462.libquantum	4	3647	22.7	<b>3427</b>	<b>24.2</b>	3380	24.5	4	3072	27.0	<b>3063</b>	<b>27.1</b>	3061	27.1
464.h264ref	4	<b>1096</b>	<b>80.8</b>	1096	80.8	1096	80.7	4	<b>1039</b>	<b>85.2</b>	1039	85.2	1041	85.0
471.omnetpp	4	876	28.5	817	30.6	<b>822</b>	<b>30.4</b>	4	747	33.4	<b>741</b>	<b>33.7</b>	740	33.8
473.astar	4	913	30.7	<b>867</b>	<b>32.4</b>	859	32.7	4	839	33.5	<b>835</b>	<b>33.6</b>	833	33.7
483.xalancbmk	4	506	54.5	<b>493</b>	<b>56.0</b>	487	56.6	4	506	54.5	<b>493</b>	<b>56.0</b>	487	56.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

All binaries were built with 32-bit Intel compiler except:  
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel  
compiler by changing the path for include and library files.

BIOS configuration:  
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:  
<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -O3 -ipo -no-prec-div -ansi-alias

-L/opt/SmartHeap\_8\_1/lib -lsmarheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc  
-I/opt/intel/cce/10.0.023/include  
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc  
-I/opt/intel/cce/10.0.023/include  
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -prefetch  
-L/opt/SmartHeap\_8\_1/lib -lsmarheap

445.gobmk: Same as 400.perlbench

456.hmmer: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -unroll2

458.sjeng: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -unroll4

462.libquantum: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -prefetch  
-opt-streaming-stores always

464.h264ref: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -ansi-alias  
-L/opt/SmartHeap\_8\_1/lib -lsmarheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/FSC\\_Intel\\_flags.html](http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/FSC\\_Intel\\_flags.xml](http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml)



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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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