SPEC® CINT2006 Result

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Fujitsu Siemens Computers

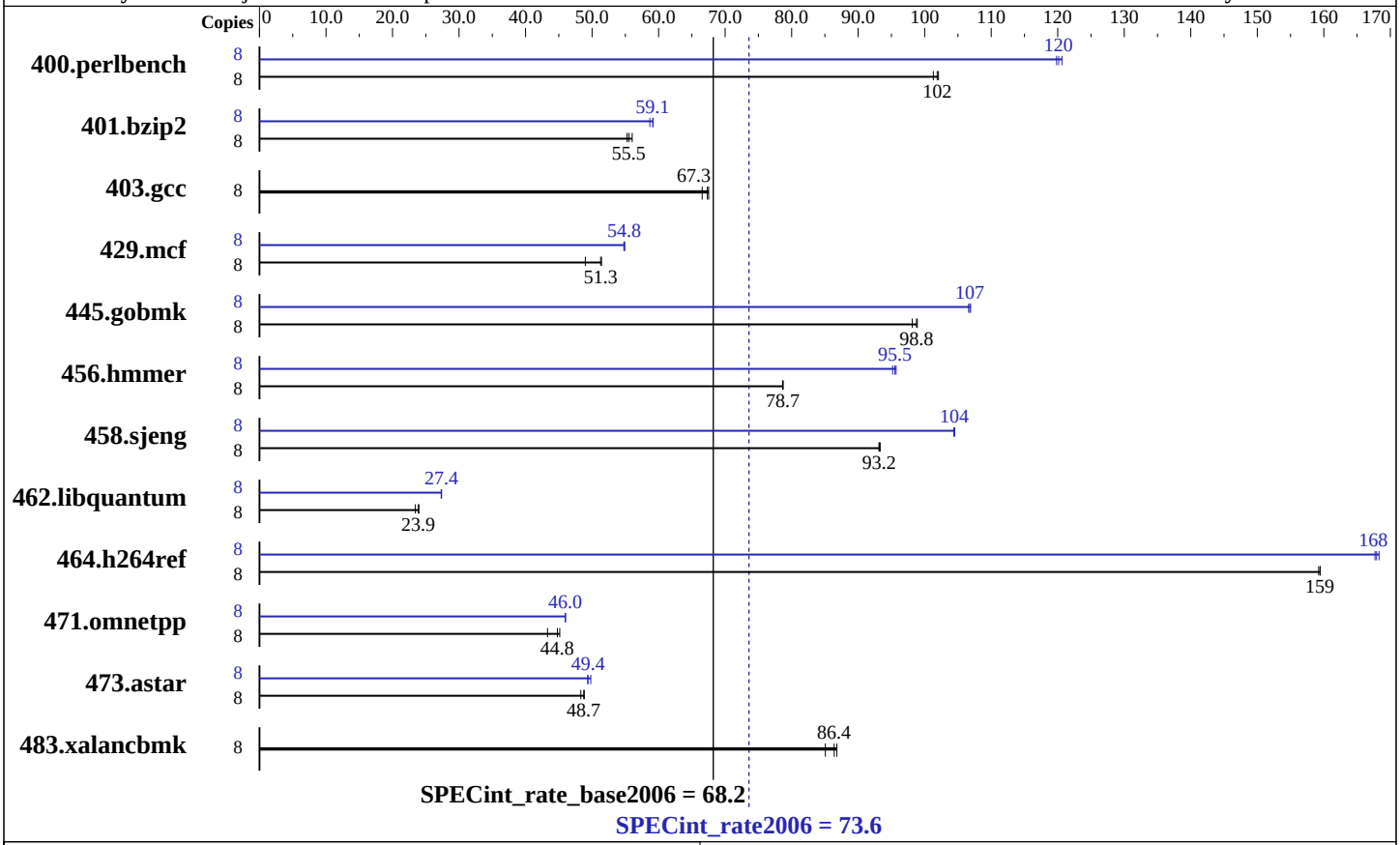
PRIMERGY TX200 S3, Intel Xeon processor E5335, 2.0 GHz

SPECint®_rate2006 = 73.6

SPECint_rate_base2006 = 68.2

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Aug-2007
Hardware Availability: Feb-2007
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon E5335
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Seagate ST336754SS (SAS, 36GB, 15000rpm)
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	772	101	**767**	**102**	766	102	8	**651**	**120**	652	120	648	121
401.bzip2	8	1397	55.3	**1390**	**55.5**	1378	56.0	8	1316	58.7	1305	59.2	**1306**	**59.1**
403.gcc	8	968	66.5	**956**	**67.3**	954	67.5	8	968	66.5	**956**	**67.3**	954	67.5
429.mcf	8	1489	49.0	**1423**	**51.3**	1419	51.4	8	1332	54.8	**1331**	**54.8**	1329	54.9
445.gobmk	8	855	98.1	849	98.9	**850**	**98.8**	8	787	107	785	107	**786**	**107**
456.hmmer	8	948	78.7	949	78.6	**949**	**78.7**	8	**781**	**95.5**	780	95.7	784	95.2
458.sjeng	8	**1039**	**93.2**	1037	93.3	1039	93.1	8	928	104	**926**	**104**	926	105
462.libquantum	8	7074	23.4	**6940**	**23.9**	6916	24.0	8	6069	27.3	6056	27.4	**6060**	**27.4**
464.h264ref	8	1110	159	1112	159	**1110**	**159**	8	1056	168	1052	168	**1054**	**168**
471.omnetpp	8	1155	43.3	**1116**	**44.8**	1108	45.1	8	1088	46.0	**1087**	**46.0**	1087	46.0
473.astar	8	1163	48.3	1150	48.8	**1152**	**48.7**	8	**1137**	**49.4**	1128	49.8	1138	49.3
483.xalancbmk	8	649	85.1	636	86.8	**639**	**86.4**	8	649	85.1	636	86.8	**639**	**86.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xT -O3 -ipo -no-prec-div -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch -L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 400.perlbench`

456.hmmer: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2`

458.sjeng: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch -opt-streaming-stores always`

464.h264ref: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -ansi-alias -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml`

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 12:30:45 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 August 2007.