



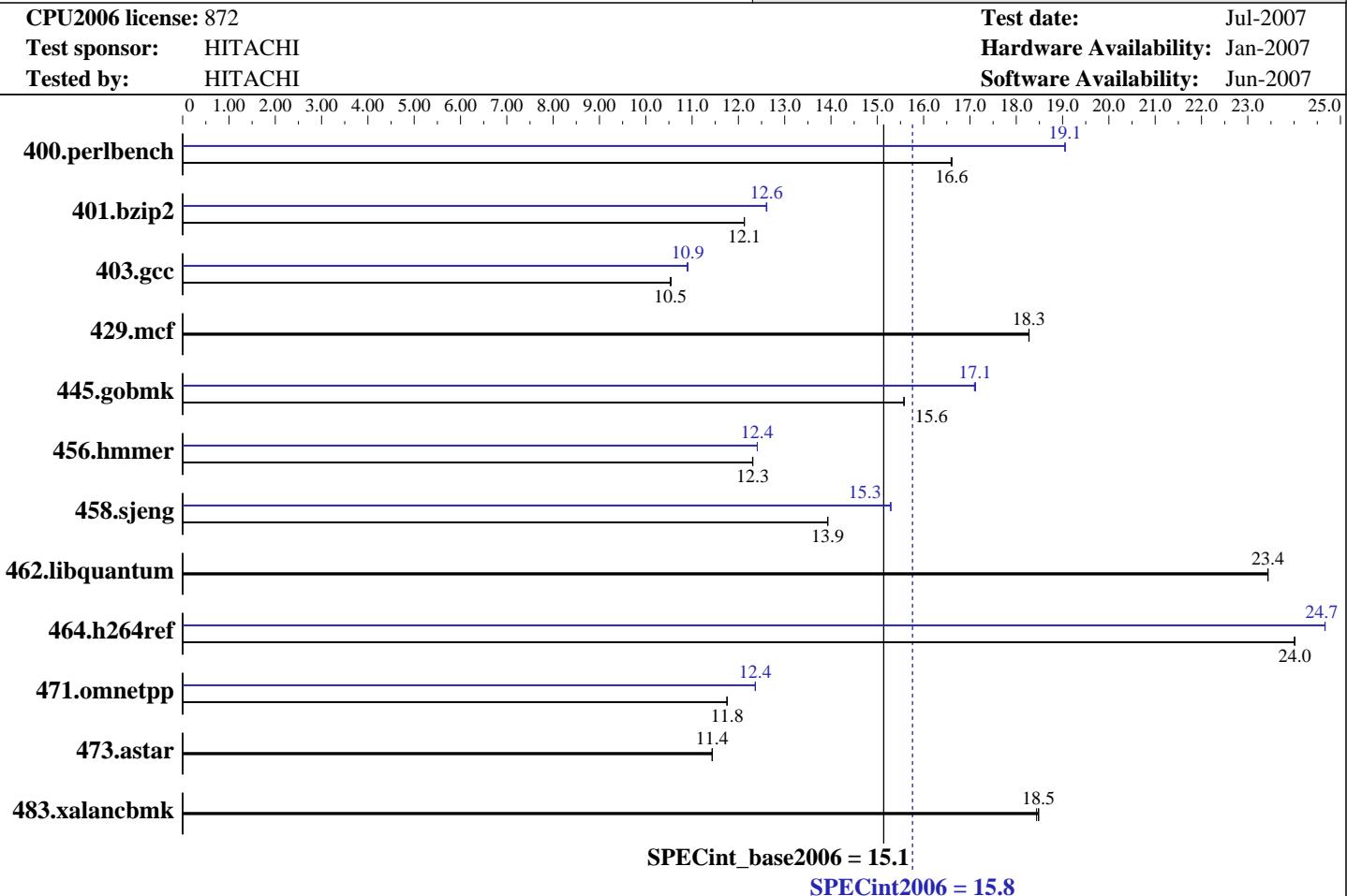
# SPEC® CINT2006 Result

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**HITACHI**

BladeSymphony BS320 (Intel Xeon E5345)

**SPECint®2006 = 15.8**



## Hardware

CPU Name: Intel Xeon E5345  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2333  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB(4 x 2 GB PC2-5300F CAS 5-5-5)  
 Disk Subsystem: 2 x 73 GB 10000rpm SAS  
 Other Hardware: None

## Software

Operating System: Microsoft Windows Server 2003 R2, Enterprise x64 Edition  
 Compiler: Intel C++ Compiler for IA32 version 10.0 Build 20070426  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: SmartHeap Library, Version 8.0



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**SPECint\_base2006 = 15.1**

CPU2006 license: 872

Test date: Jul-2007

Test sponsor: HITACHI

Hardware Availability: Jan-2007

Tested by: HITACHI

Software Availability: Jun-2007

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	588	16.6	<b>588</b>	<b>16.6</b>	589	16.6	<b>513</b>	<b>19.1</b>	513	19.1	513	19.1
401.bzip2	796	12.1	796	12.1	<b>796</b>	<b>12.1</b>	<b>765</b>	<b>12.6</b>	766	12.6	765	12.6
403.gcc	<b>764</b>	<b>10.5</b>	763	10.5	765	10.5	738	10.9	<b>738</b>	<b>10.9</b>	739	10.9
429.mcf	499	18.3	<b>499</b>	<b>18.3</b>	499	18.3	499	18.3	<b>499</b>	<b>18.3</b>	499	18.3
445.gobmk	<b>673</b>	<b>15.6</b>	674	15.6	673	15.6	614	17.1	<b>613</b>	<b>17.1</b>	613	17.1
456.hammer	758	12.3	758	12.3	<b>758</b>	<b>12.3</b>	<b>752</b>	<b>12.4</b>	752	12.4	752	12.4
458.sjeng	869	13.9	<b>869</b>	<b>13.9</b>	869	13.9	791	15.3	791	15.3	<b>791</b>	<b>15.3</b>
462.libquantum	884	23.4	884	23.4	<b>884</b>	<b>23.4</b>	884	23.4	884	23.4	<b>884</b>	<b>23.4</b>
464.h264ref	<b>922</b>	<b>24.0</b>	922	24.0	922	24.0	897	24.7	897	24.7	<b>897</b>	<b>24.7</b>
471.omnetpp	532	11.8	532	11.8	<b>532</b>	<b>11.8</b>	<b>505</b>	<b>12.4</b>	506	12.4	505	12.4
473.astar	614	11.4	614	11.4	<b>614</b>	<b>11.4</b>	614	11.4	614	11.4	<b>614</b>	<b>11.4</b>
483.xalancbmk	373	18.5	<b>373</b>	<b>18.5</b>	374	18.4	<b>373</b>	<b>18.5</b>	<b>373</b>	<b>18.5</b>	374	18.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:

  icl -Qvc7.1 -Qc99

C++ benchmarks:

  icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32

464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:

  -fast /F512000000 shlw32m.lib                          -link /FORCE:MULTIPLE

C++ benchmarks:

  -fast -Qcxx\_features /F512000000 shlw32m.lib  
                          -link /FORCE:MULTIPLE



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

## Peak Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32

464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Peak Optimization Flags

C benchmarks:

400.perlbench: ONESTEP -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: basepeak = yes

464.h264ref: Same as 400.perlbench

C++ benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

```
471.omnetpp: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast  
-Qcxx_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/ic100.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/ic100.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.1.

Report generated on Tue Jul 22 12:24:23 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 21 August 2007.