



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 98.3

Acer Altos R720 (Intel Xeon X5355)

SPECint_rate_base2006 = 91.1

CPU2006 license: 97

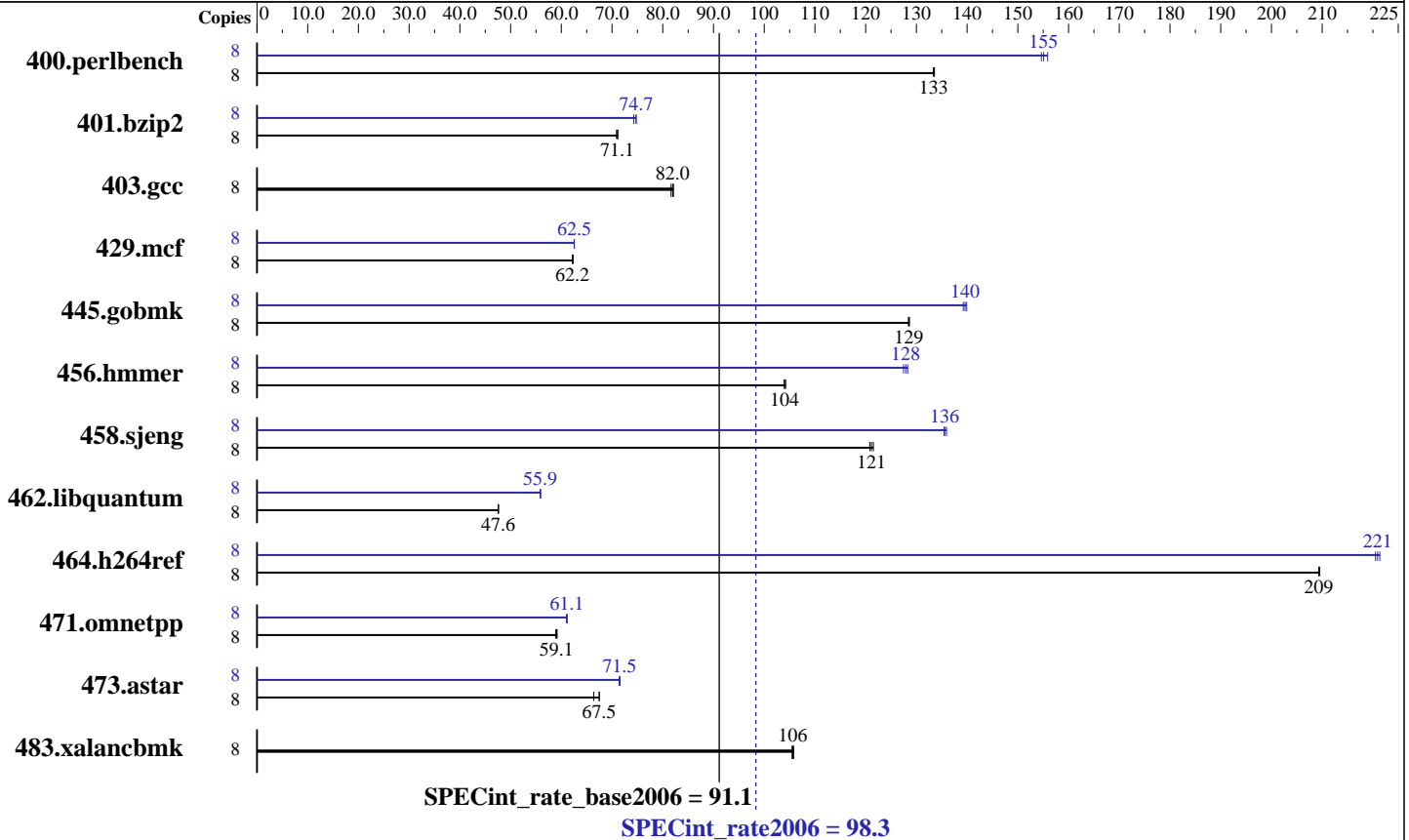
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jun-2007

Hardware Availability: Nov-2006

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2660
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
 Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 EM64T (kernel 2.6.16.21-0.8-smp)
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.0
 Build 20070426 Package ID: l_cc_p_10.0.023
 Auto Parallel: No
 File System: ReiserFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	585	134	586	133	586	133	8	501	156	505	155	504	155
401.bzip2	8	1086	71.1	1085	71.1	1089	70.9	8	1033	74.7	1039	74.3	1033	74.7
403.gcc	8	789	81.6	784	82.1	786	82.0	8	789	81.6	784	82.1	786	82.0
429.mcf	8	1173	62.2	1173	62.2	1171	62.3	8	1167	62.5	1166	62.6	1166	62.5
445.gobmk	8	652	129	653	128	653	129	8	601	140	600	140	603	139
456.hammer	8	718	104	716	104	718	104	8	582	128	584	128	586	127
458.sjeng	8	801	121	799	121	797	122	8	712	136	715	135	714	136
462.libquantum	8	3484	47.6	3483	47.6	3483	47.6	8	2968	55.9	2968	55.9	2965	55.9
464.h264ref	8	846	209	845	210	846	209	8	800	221	803	221	801	221
471.omnetpp	8	849	58.9	845	59.2	847	59.1	8	818	61.1	819	61.0	818	61.1
473.astar	8	846	66.4	832	67.5	832	67.5	8	785	71.6	786	71.5	787	71.4
483.xalancbmk	8	523	105	523	106	522	106	8	523	105	523	106	522	106

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result was measured on the Acer Altos R520
The Altos R720 and Altos R520 are electronically equivalent.

BIOS settings
Hardware Prefetch is set to disabled
Adjacent Cache Line Prefetch is set to disabled

taskset was used to bind copies to cores

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.41.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.41.xml>



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For other inquiries, please contact webmaster@spec.org.

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