



SPEC® CINT2006 Result

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Intel Corporation

Intel DQ965GF motherboard (Intel Core 2 Quad Q6600)

SPECint®2006 = 18.2

SPECint_base2006 = 16.3

CPU2006 license: 13

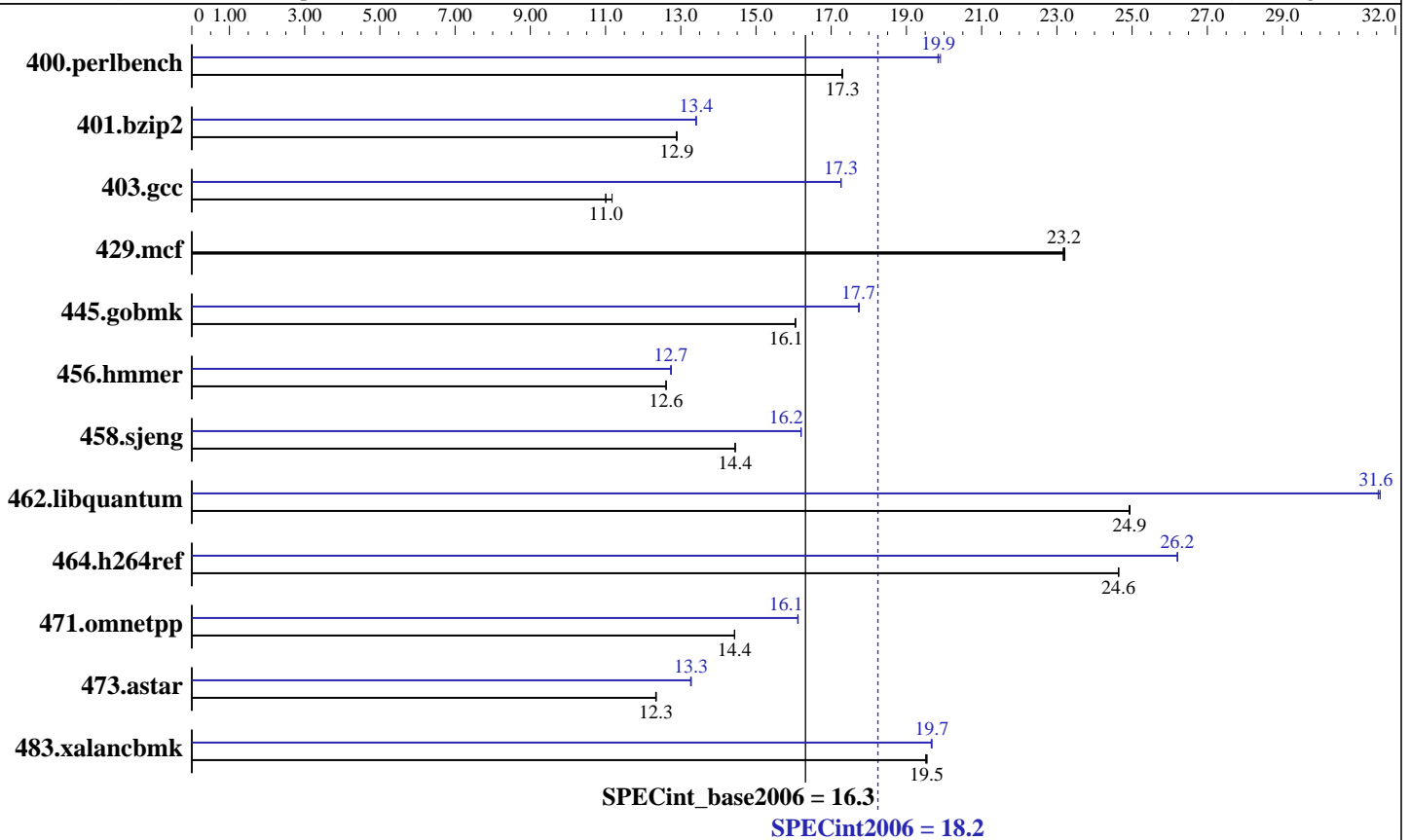
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Jan-2007

Software Availability: Aug-2006



Hardware

CPU Name: Intel Core 2 Quad Q6600
 CPU Characteristics: 2.40 GHz, 1066 MHz bus
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2 1GB Micron MT16HTF12864AY-80ED4 DDR2 800, CL5)
 Disk Subsystem: Seagate ST3320620AS 320GB Barracuda 7200.10 NCQ SATA II
 Other Hardware: None

Software

Operating System: Windows Vista32 Ultimate
 Compiler: Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.0 from <http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	565	17.3	565	17.3	<u>565</u>	<u>17.3</u>	<u>492</u>	<u>19.9</u>	492	19.8	491	19.9
401.bzip2	749	12.9	<u>748</u>	<u>12.9</u>	748	12.9	719	13.4	<u>720</u>	<u>13.4</u>	720	13.4
403.gcc	<u>731</u>	<u>11.0</u>	732	11.0	720	11.2	<u>466</u>	<u>17.3</u>	466	17.3	467	17.3
429.mcf	394	23.2	393	23.2	<u>393</u>	<u>23.2</u>	394	23.2	393	23.2	<u>393</u>	<u>23.2</u>
445.gobmk	654	16.0	<u>654</u>	<u>16.1</u>	653	16.1	592	17.7	<u>591</u>	<u>17.7</u>	591	17.7
456.hmmmer	740	12.6	<u>740</u>	<u>12.6</u>	740	12.6	733	12.7	732	12.8	<u>732</u>	<u>12.7</u>
458.sjeng	<u>837</u>	<u>14.4</u>	838	14.4	837	14.5	747	16.2	747	16.2	<u>747</u>	<u>16.2</u>
462.libquantum	831	24.9	831	24.9	<u>831</u>	<u>24.9</u>	656	31.6	657	31.5	<u>657</u>	<u>31.6</u>
464.h264ref	<u>898</u>	<u>24.6</u>	898	24.6	898	24.6	845	26.2	844	26.2	<u>845</u>	<u>26.2</u>
471.omnetpp	433	14.4	433	14.4	<u>433</u>	<u>14.4</u>	388	16.1	<u>388</u>	<u>16.1</u>	388	16.1
473.astar	<u>569</u>	<u>12.3</u>	569	12.3	568	12.4	529	13.3	<u>529</u>	<u>13.3</u>	529	13.3
483.xalancbmk	353	19.5	<u>353</u>	<u>19.5</u>	354	19.5	351	19.7	<u>351</u>	<u>19.7</u>	351	19.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 7/2007:

<http://www.intel.com/products/motherboard/DQ965GF/index.htm>

The system bus runs at 1066 MHz

System was configured with integrated graphics card

Binaries were built on Windows XP Professional SP2 with 4GB of RAM and /3GB boot switch

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```



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Base Optimization Flags

C benchmarks:

`-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:

`-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:

`icl -Qvc7.1 -Qc99`

C++ benchmarks:

`icl -Qvc7.1`

Peak Portability Flags

`403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

`400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE`

`401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE`

`403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
-link /FORCE:MULTIPLE`

`429.mcf: basepeak = yes`

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Peak Optimization Flags (Continued)

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec_div- -Qansi-alias /F512000000
-link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
-Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.xml>

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