



SPEC[®] CFP2006 Result

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Intel Corporation

SPECfp[®]_rate2006 = 26.2

Intel DG965WH motherboard (Intel Core 2 Duo E6700)

SPECfp_rate_base2006 = 25.5

CPU2006 license: 13

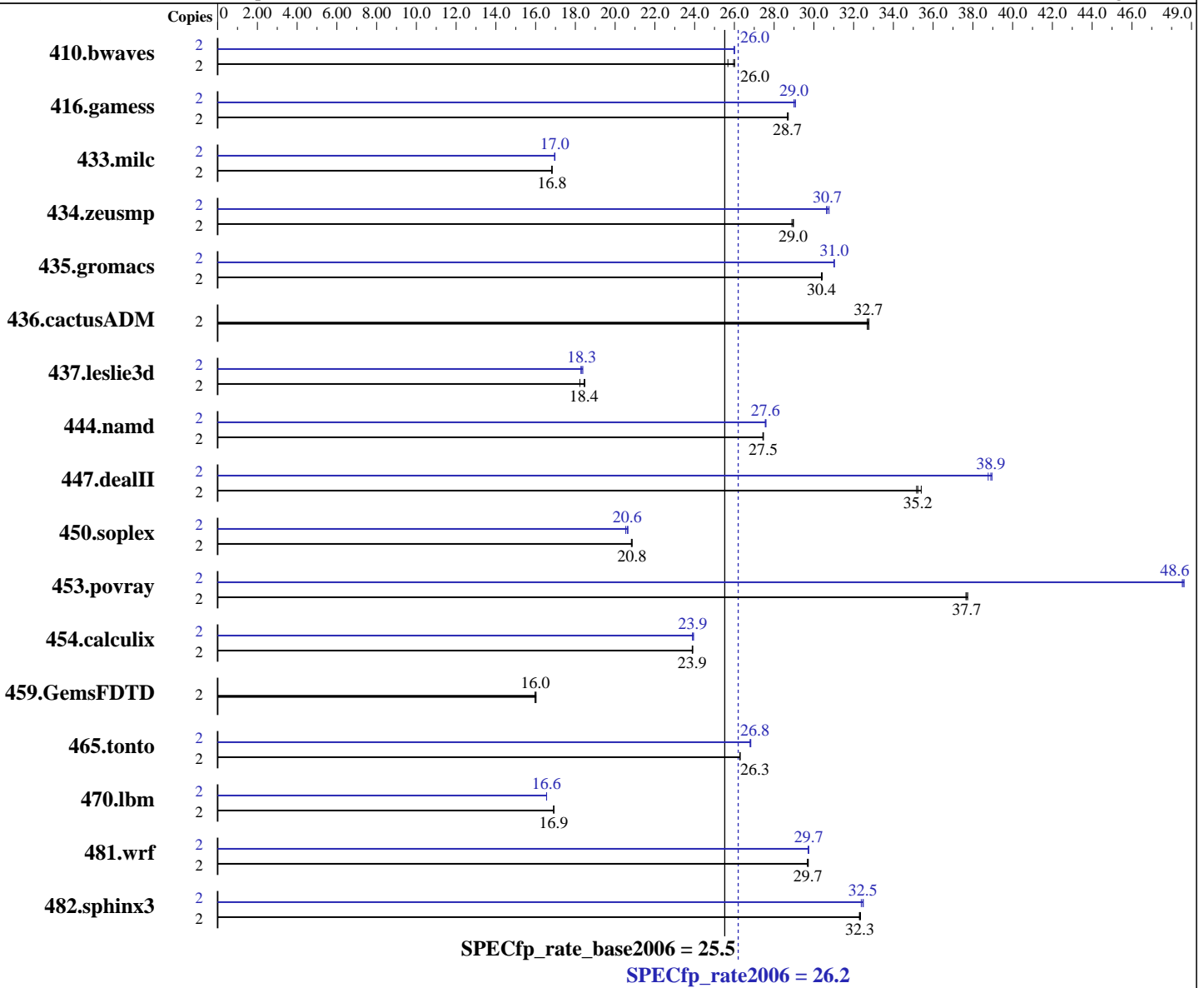
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Aug-2006

Software Availability: Aug-2006



Hardware

CPU Name: Intel Core 2 Duo E6700
 CPU Characteristics: 2.67 GHz, 1066 MHz bus
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip

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Software

Operating System: Windows Vista32 Ultimate
 Compiler: Intel C++ Compiler for IA32 version 10.0
 Build 20070426 Package ID: W_CC_P_10.0.025
 Intel Fortran Compiler for IA32 version 10.0
 Build 20070426 Package ID: W_FC_P_10.0.025
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default

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L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2 1GB Micron MT16HTF12864AY-80ED4 DDR2 800, CL5)
 Disk Subsystem: Seagate ST3320620AS 320GB Barracuda 7200.10 NCQ SATA II
 Other Hardware: None

Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.0 from <http://www.microquill.com/>

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	2	1059	25.7	1045	26.0	1047	26.0	2	1046	26.0	1045	26.0	1046	26.0		
416.gamess	2	1366	28.7	1364	28.7	1366	28.7	2	1347	29.1	1350	29.0	1350	29.0		
433.milc	2	1091	16.8	1093	16.8	1091	16.8	2	1082	17.0	1083	17.0	1082	17.0		
434.zeusmp	2	628	29.0	630	28.9	629	29.0	2	592	30.8	593	30.7	594	30.6		
435.gromacs	2	470	30.4	469	30.4	470	30.4	2	460	31.0	461	31.0	460	31.0		
436.cactusADM	2	729	32.8	731	32.7	730	32.7	2	729	32.8	731	32.7	730	32.7		
437.leslie3d	2	1020	18.4	1032	18.2	1018	18.5	2	1029	18.3	1023	18.4	1026	18.3		
444.namd	2	585	27.4	584	27.5	584	27.5	2	581	27.6	582	27.6	582	27.6		
447.dealII	2	646	35.4	649	35.2	651	35.2	2	590	38.8	587	39.0	588	38.9		
450.soplex	2	800	20.8	801	20.8	800	20.9	2	808	20.6	809	20.6	812	20.5		
453.povray	2	283	37.7	282	37.7	282	37.7	2	219	48.6	219	48.6	219	48.5		
454.calculix	2	691	23.9	690	23.9	691	23.9	2	689	23.9	691	23.9	690	23.9		
459.GemsFDTD	2	1324	16.0	1327	16.0	1329	16.0	2	1324	16.0	1327	16.0	1329	16.0		
465.tonto	2	749	26.3	749	26.3	749	26.3	2	734	26.8	734	26.8	735	26.8		
470.lbm	2	1625	16.9	1625	16.9	1625	16.9	2	1660	16.6	1660	16.6	1661	16.5		
481.wrf	2	752	29.7	753	29.7	752	29.7	2	752	29.7	751	29.7	751	29.7		
482.sphinx3	2	1207	32.3	1207	32.3	1205	32.3	2	1200	32.5	1204	32.4	1201	32.5		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
 Product description located as of 7/2007:
<http://www.intel.com/products/motherboard/DG965WH/index.htm>
 The system bus runs at 1066 MHz
 System has a discrete gfx card - Asus EN8800GTX/HTDP/768M w/ nVidia 8800GTX
 Binaries were built on Windows XP Professional SP2 with 4GB of RAM and /3GB boot switch
 The start command with the /affinity switch was used to bind processes to cores



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Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore

444.namd: -TP

447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG

-DBOOST_NO_INTRINSIC_WCHAR_T

453.povray: -DSPEC_CPU_WINDOWS_ICL

454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase

481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:

-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx_features /F950000000 shlw32m.lib

-link /FORCE:MULTIPLE

Fortran benchmarks:

-fast /F950000000

Benchmarks using both Fortran and C:

-fast /F950000000

Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

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Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Oa
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE
470.lbm: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qscalar-rep- -Qprefetch /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE
482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Oa
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE
447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE
450.soplex: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE
453.povray: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: -fast /F950000000

416.gamess: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Ob0
-Qansi-alias -Qscalar-rep- /F950000000

434.zeusmp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qprec_div-
-Qunroll10 -Qscalar-rep- /F950000000

437.leslie3d: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000

459.GemsFDTD: basepeak = yes

465.tonto: Same as 437.leslie3d

Benchmarks using both Fortran and C:

435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Oa
/F950000000

436.cactusADM: basepeak = yes

454.calculix: -fast /F950000000

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.xml>

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For other inquiries, please contact webmaster@spec.org.

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