



SPEC[®] CINT2006 Result

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Hewlett-Packard Company

(Test Sponsor: Intel Corporation)

SPECint[®]2006 = **Not Run**

Compaq 6515B (AMD Turion 64 X2 TL-56)

SPECint_base2006 = **6.93**

CPU2006 license: 13

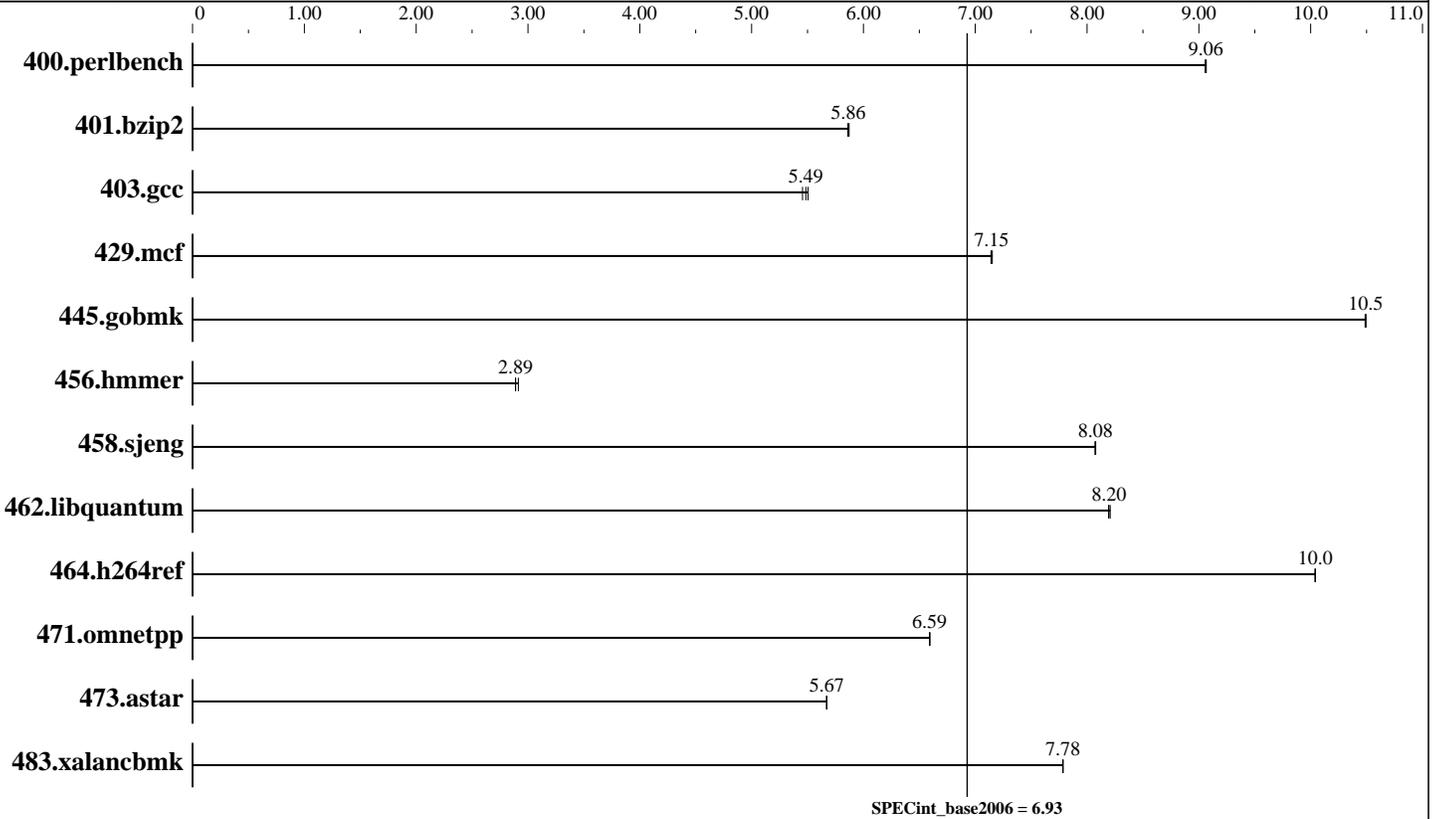
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Jun-2007

Software Availability: Feb-2007



Hardware

CPU Name: AMD Turion 64 X2 TL-56
 CPU Characteristics: 1.8 GHz 2x512KB L2
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Qimonda HYS64T128021HDL-3S-B DDR2-667 CL5)
 Disk Subsystem: Hitachi 100GB 5400rpm SATA HDD
 Other Hardware: None

Software

Operating System: Windows Vista32 Business
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: SmartHeap Library Version 8.0 from <http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	1079	9.06	1078	9.07	1078	9.06						
401.bzip2	1646	5.86	1645	5.86	1643	5.87						
403.gcc	1467	5.49	1476	5.46	1462	5.50						
429.mcf	1276	7.15	1275	7.15	1277	7.14						
445.gobmk	1000	10.5	1000	10.5	1000	10.5						
456.hammer	3231	2.89	3229	2.89	3202	2.91						
458.sjeng	1499	8.07	1498	8.08	1498	8.08						
462.libquantum	2528	8.20	2529	8.19	2525	8.21						
464.h264ref	2204	10.0	2204	10.0	2204	10.0						
471.omnetpp	948	6.59	948	6.59	948	6.59						
473.astar	1238	5.67	1238	5.67	1238	5.67						
483.xalancbmk	886	7.79	886	7.78	887	7.78						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-arch:SSE2 -Qprec-div- -O3 -Qipo /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-arch:SSE2 -Qprec-div- -O3 -Qipo -Oi- -Qcxx_features /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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