



SPEC® CINT2006 Result

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Hewlett-Packard Company

(Test Sponsor: Intel Corporation)

SPECint®2006 = Not Run

Compaq 6515B (AMD Turion 64 X2 TL-60)

SPECint_base2006 = 7.70

CPU2006 license: 13

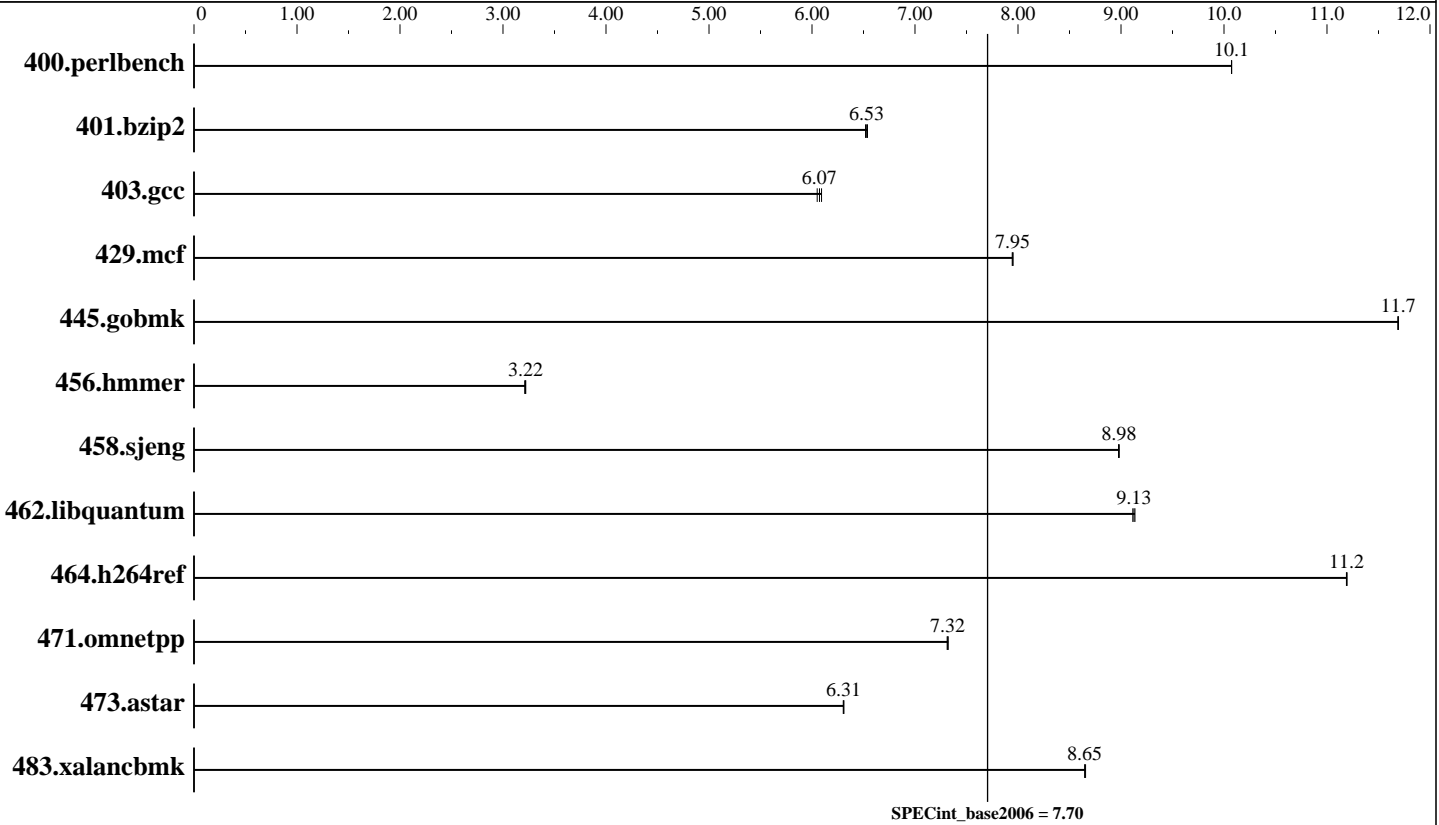
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Jun-2007

Software Availability: Feb-2007



Hardware

CPU Name: AMD Turion 64 X2 TL-60
CPU Characteristics: 2.0 GHz 2x512KB L2
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 64 KB I + 64 KB D on chip per core
Secondary Cache: 512 KB I+D on chip per core
L3 Cache: None
Other Cache: None
Memory: 2 GB (2x1GB Qimonda HYS64T128021HDL-3S-B DDR2-667 CL5)
Disk Subsystem: Hitachi 100GB 5400rpm SATA HDD
Other Hardware: None

Software

Operating System: Windows Vista32 Business
Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: None



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	970	10.1	970	10.1	970	10.1						
401.bzip2	1476	6.54	1480	6.52	1478	6.53						
403.gcc	1321	6.09	1330	6.05	1326	6.07						
429.mcf	1147	7.95	1147	7.95	1148	7.94						
445.gobmk	897	11.7	898	11.7	897	11.7						
456.hammer	2899	3.22	2897	3.22	2906	3.21						
458.sjeng	1348	8.98	1348	8.98	1347	8.98						
462.libquantum	2270	9.13	2268	9.14	2273	9.11						
464.h264ref	1978	11.2	1977	11.2	1977	11.2						
471.omnetpp	854	7.32	855	7.31	854	7.32						
473.astar	1113	6.31	1114	6.30	1113	6.31						
483.xalancbmk	798	8.65	797	8.66	798	8.65						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-arch:SSE2 -Qprec-div- -O3 -Qipo /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-arch:SSE2 -Qprec-div- -O3 -Qipo -Oi- -Qcxx_features /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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