



SPEC® CINT2006 Result

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Hewlett-Packard Company

(Test Sponsor: Intel Corporation)

SPECint®2006 =

Not Run

HP dv2000 (Intel Core 2 Duo T7100)

SPECint_base2006 =

11.8

CPU2006 license: 13

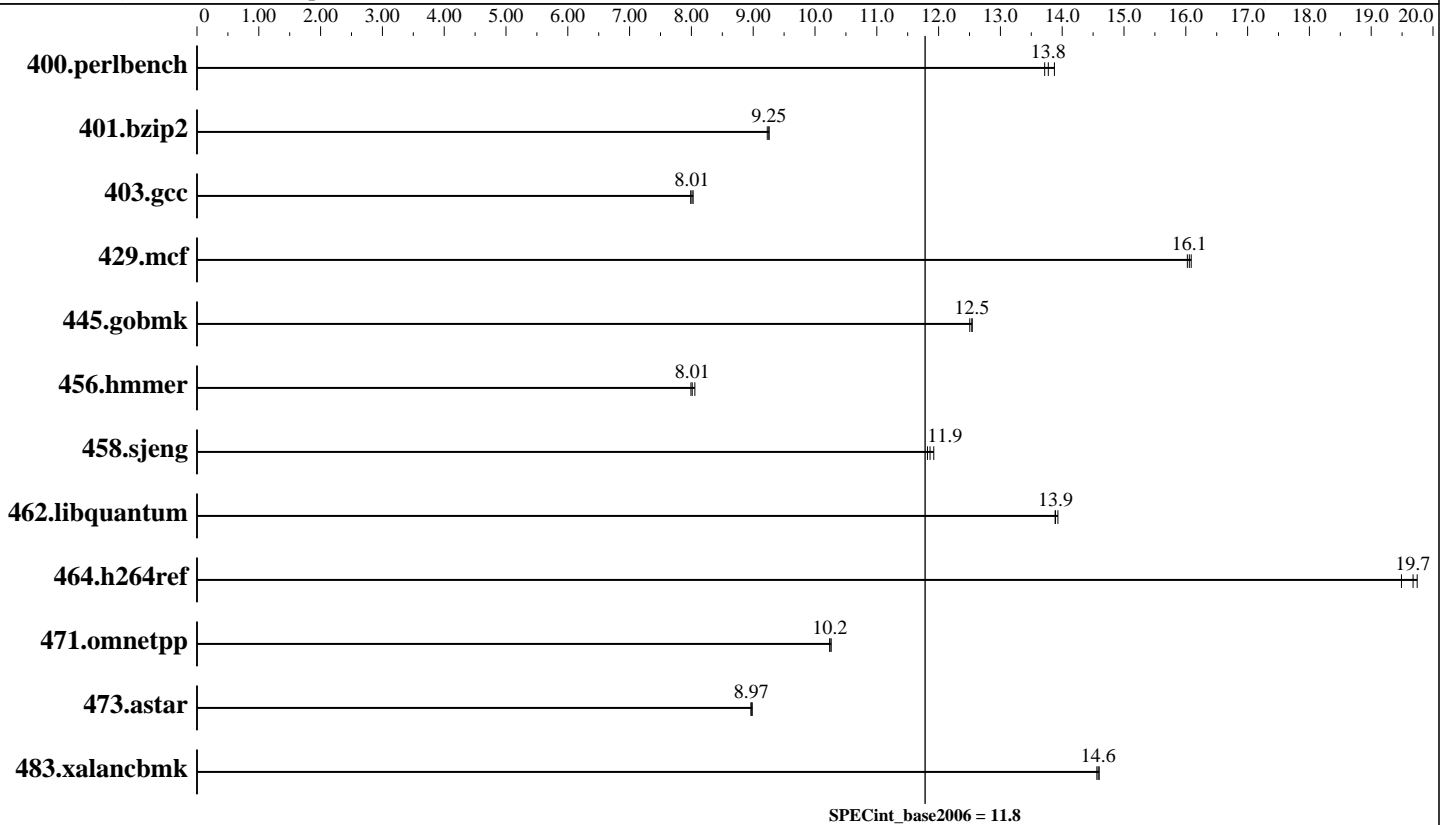
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Jun-2007

Software Availability: Feb-2007



Hardware

CPU Name: Intel Core 2 Duo T7100
 CPU Characteristics: 1.8 GHz 2MB L2
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 2 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Micron MT16HTF12864HY-667B3 DDR2-667 CL5)
 Disk Subsystem: Fujitsu 120GB 5400rpm SATA HDD
 Other Hardware: None

Software

Operating System: Windows Vista32 Business
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: SmartHeap Library Version 8.0 from <http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	712	13.7	704	13.9	709	13.8						
401.bzip2	1043	9.25	1043	9.26	1046	9.23						
403.gcc	1008	7.99	1003	8.03	1005	8.01						
429.mcf	569	16.0	568	16.1	567	16.1						
445.gobmk	839	12.5	837	12.5	836	12.5						
456.hammer	1158	8.06	1167	7.99	1164	8.01						
458.sjeng	1020	11.9	1024	11.8	1015	11.9						
462.libquantum	1492	13.9	1492	13.9	1487	13.9						
464.h264ref	1125	19.7	1121	19.7	1135	19.5						
471.omnetpp	610	10.2	609	10.3	610	10.2						
473.astar	783	8.97	781	8.98	783	8.97						
483.xalancbmk	474	14.6	473	14.6	473	14.6						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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