



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY RX600 S3, Intel Xeon processor 7110M, 2.60 GHz

SPECint®\_rate2006 = 32.5

SPECint\_rate\_base2006 = 30.4

CPU2006 license: 22

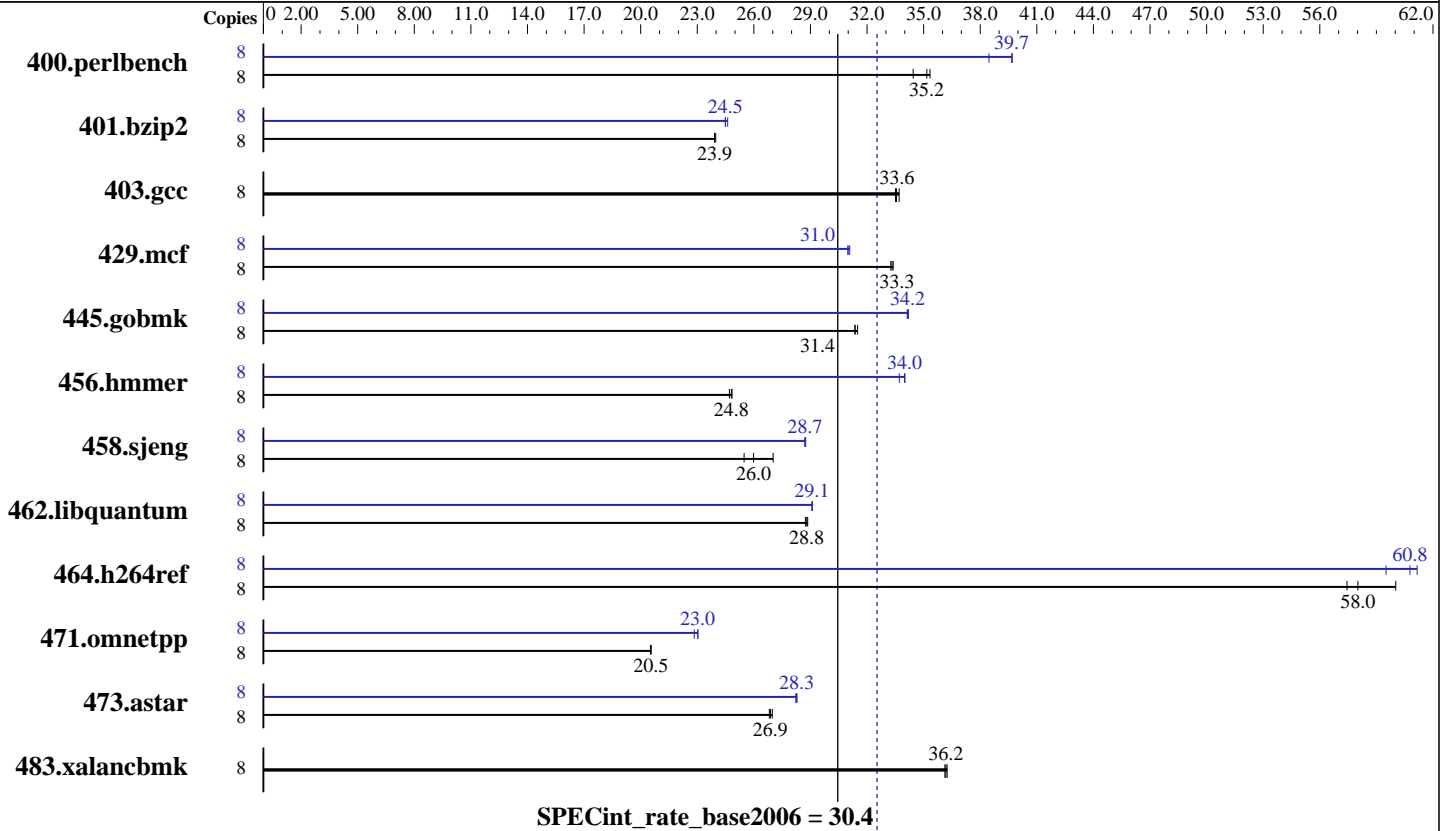
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jun-2007

Hardware Availability: Aug-2006

Software Availability: Mar-2007



### Hardware

CPU Name: Intel Xeon 7110M  
 CPU Characteristics: 800 MHz system bus  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,4 chips  
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (16x2 GB DDR2 PC2-3200R, 1 rank, CAS 3-3-3, with ECC)  
 Disk Subsystem: Seagate ST973401SS (SAS, 73GB, 10000rpm)  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	2269	34.4	2212	35.3	<u>2222</u>	<u>35.2</u>	8	1969	39.7	<u>1970</u>	<u>39.7</u>	2032	38.5
401.bzip2	8	<u>3228</u>	<u>23.9</u>	3228	23.9	3220	24.0	8	3137	24.6	3153	24.5	<u>3152</u>	<u>24.5</u>
403.gcc	8	1922	33.5	1911	33.7	<u>1919</u>	<u>33.6</u>	8	1922	33.5	1911	33.7	<u>1919</u>	<u>33.6</u>
429.mcf	8	<u>2189</u>	<u>33.3</u>	2186	33.4	2194	33.3	8	2355	31.0	<u>2354</u>	<u>31.0</u>	2347	31.1
445.gobmk	8	<u>2675</u>	<u>31.4</u>	2664	31.5	2677	31.4	8	2459	34.1	2454	34.2	<u>2457</u>	<u>34.2</u>
456.hmmmer	8	<u>3009</u>	<u>24.8</u>	3004	24.8	3022	24.7	8	2195	34.0	<u>2196</u>	<u>34.0</u>	2214	33.7
458.sjeng	8	<u>3725</u>	<u>26.0</u>	3799	25.5	3582	27.0	8	<u>3371</u>	<u>28.7</u>	3373	28.7	3367	28.7
462.libquantum	8	5770	28.7	5745	28.9	<u>5755</u>	<u>28.8</u>	8	<u>5699</u>	<u>29.1</u>	5699	29.1	5696	29.1
464.h264ref	8	2949	60.0	3082	57.4	<u>3051</u>	<u>58.0</u>	8	2975	59.5	2895	61.2	<u>2913</u>	<u>60.8</u>
471.omnetpp	8	<u>2434</u>	<u>20.5</u>	2433	20.6	2437	20.5	8	2189	22.8	<u>2171</u>	<u>23.0</u>	2171	23.0
473.astar	8	<u>2090</u>	<u>26.9</u>	2082	27.0	2094	26.8	8	<u>1987</u>	<u>28.3</u>	1985	28.3	1990	28.2
483.xalancbmk	8	1523	36.2	<u>1526</u>	<u>36.2</u>	1528	36.1	8	1523	36.2	<u>1526</u>	<u>36.2</u>	1528	36.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:  
401.bzip2, 456.hmmmer and 462.libquantum in peak were built with  
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:  
Hardware Prefetch = Enable

The PRIMERGY RX600 S3 and the PRIMERGY TX600 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers in your country please see:  
<http://www.fujitsu-siemens.com/countries>

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast

C++ benchmarks:  
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmarheap

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmarheap

445.gobmk: Same as 429.mcf

456.hmmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -xP -O3 -ipo  
-no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmarheap

473.astar: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmarheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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