



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Supermicro X7DB+ (Intel Xeon processor E5310, 1.60 GHz)

SPECint®_rate2006 = 69.0

SPECint_rate_base2006 = 63.8

CPU2006 license: 13

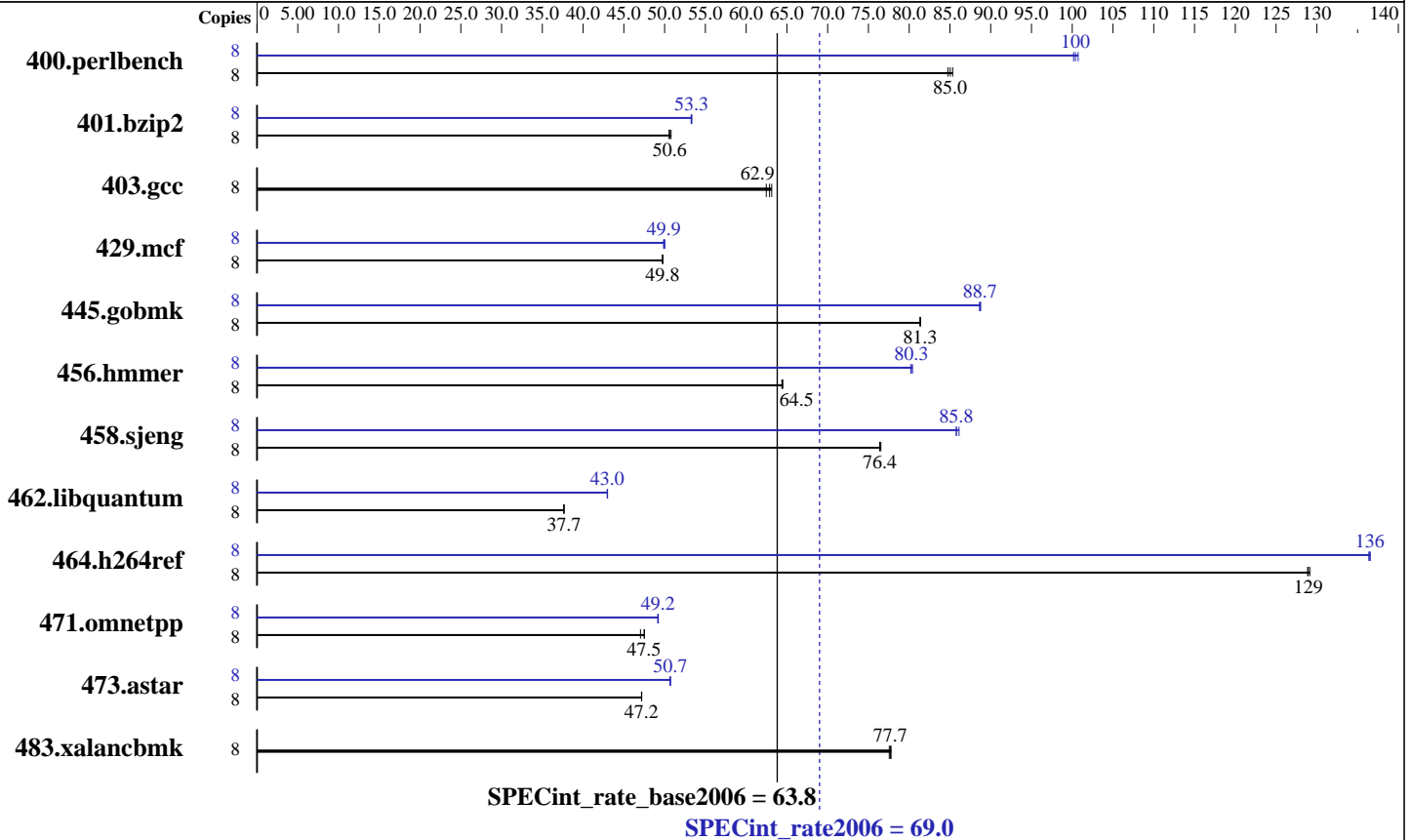
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: Sep-2007

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon E5310
 CPU Characteristics: Quad Core, 1.60 GHz
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2GB Samsung DDR2 4200F, 2 rank, CL4-4-4, ECC)
 Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
 Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor E5310, 1.60 GHz)

SPECint_rate2006 = 69.0

SPECint_rate_base2006 = 63.8

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: Sep-2007

Software Availability: Jun-2007

Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	922	84.7	<u>919</u>	<u>85.0</u>	916	85.3	8	776	101	<u>779</u>	<u>100</u>	780	100
401.bzip2	8	1520	50.8	1527	50.5	<u>1525</u>	<u>50.6</u>	8	1448	53.3	<u>1448</u>	<u>53.3</u>	1450	53.3
403.gcc	8	1020	63.1	1031	62.5	<u>1024</u>	<u>62.9</u>	8	1020	63.1	1031	62.5	<u>1024</u>	<u>62.9</u>
429.mcf	8	1467	49.7	1466	49.8	<u>1466</u>	<u>49.8</u>	8	<u>1461</u>	<u>49.9</u>	1463	49.9	1458	50.0
445.gobmk	8	<u>1032</u>	<u>81.3</u>	1031	81.4	1032	81.3	8	945	88.8	<u>946</u>	<u>88.7</u>	947	88.6
456.hmmer	8	1159	64.4	<u>1158</u>	<u>64.5</u>	1158	64.5	8	928	80.4	<u>930</u>	<u>80.3</u>	931	80.2
458.sjeng	8	<u>1267</u>	<u>76.4</u>	1265	76.5	1267	76.4	8	1128	85.8	<u>1128</u>	<u>85.8</u>	1124	86.1
462.libquantum	8	4401	37.7	<u>4402</u>	<u>37.7</u>	4403	37.6	8	3859	43.0	<u>3857</u>	<u>43.0</u>	3855	43.0
464.h264ref	8	<u>1373</u>	<u>129</u>	1371	129	1374	129	8	1296	137	<u>1297</u>	<u>136</u>	1298	136
471.omnetpp	8	1063	47.0	1052	47.5	<u>1053</u>	<u>47.5</u>	8	<u>1016</u>	<u>49.2</u>	1016	49.2	1017	49.2
473.astar	8	<u>1191</u>	<u>47.2</u>	1191	47.2	1190	47.2	8	<u>1108</u>	<u>50.7</u>	1107	50.7	1109	50.6
483.xalancbmk	8	710	77.8	<u>711</u>	<u>77.7</u>	712	77.6	8	710	77.8	<u>711</u>	<u>77.7</u>	712	77.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios settings:
Hardware Prefetcher: Disabled
Adjacent Sector Prefetch: Disabled
ulimit -s unlimited used to set stack size to unlimited
taskset command used to assign copies to cores
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc
C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor E5310, 1.60 GHz)

SPECint_rate2006 = 69.0

SPECint_rate_base2006 = 63.8

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: Sep-2007

Software Availability: Jun-2007

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor E5310, 1.60 GHz)

SPECint_rate2006 = 69.0

SPECint_rate_base2006 = 63.8

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: Sep-2007

Software Availability: Jun-2007

Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.00.xml>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor E5310, 1.60 GHz)

SPECint_rate2006 = 69.0

SPECint_rate_base2006 = 63.8

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: Sep-2007

Software Availability: Jun-2007

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 11:25:29 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 June 2007.