



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor E5320, 1.86 GHz)

SPECint®\_rate2006 = 75.4

SPECint\_rate\_base2006 = 69.6

CPU2006 license: 13

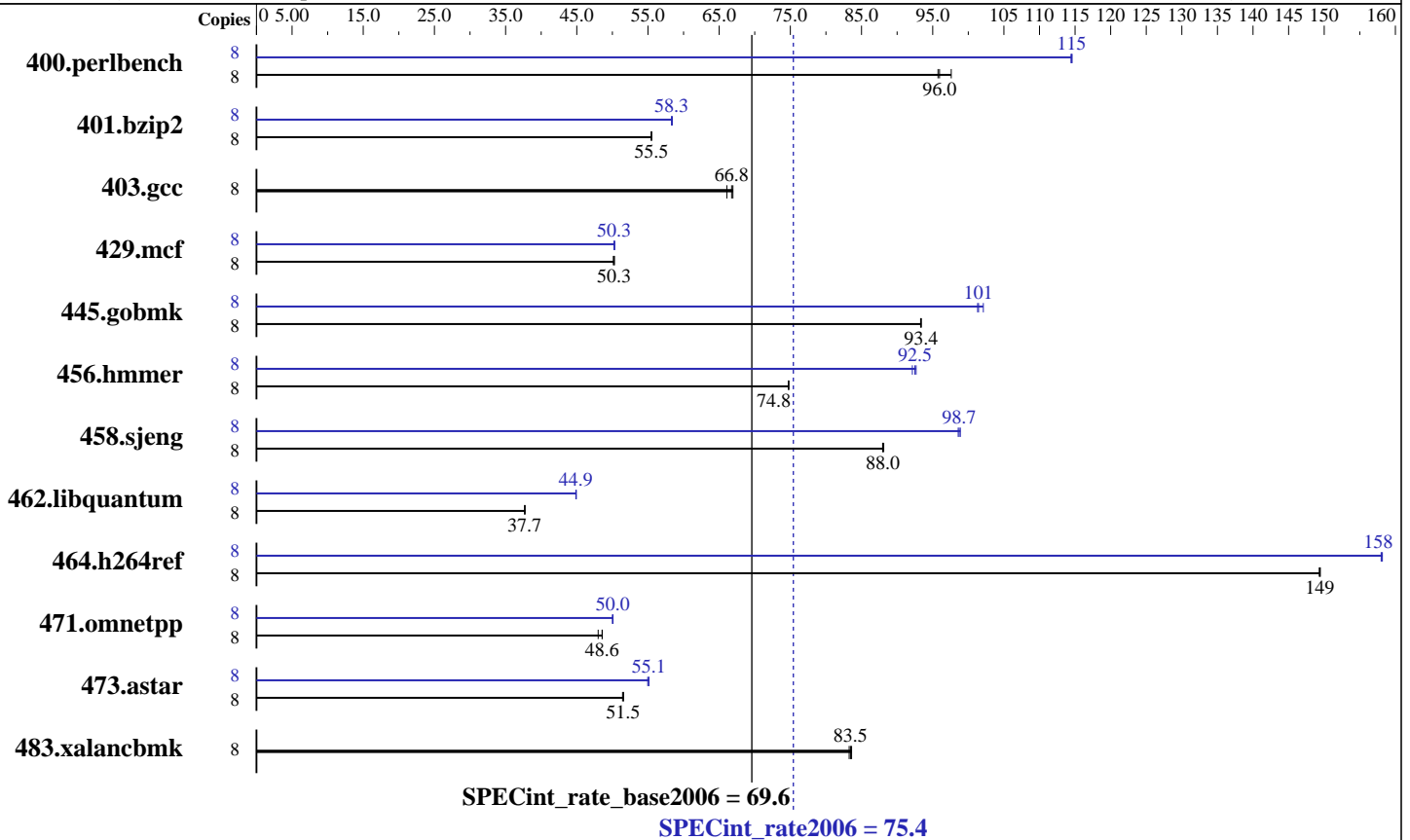
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: Sep-2007

Software Availability: Jun-2007



### Hardware

CPU Name: Intel Xeon E5320  
 CPU Characteristics: Quad Core, 1.86 GHz  
 CPU MHz: 1866  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 \* 2GB Samsung DDR2 4200F, 2 rank, CL4-4-4, ECC)  
 Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86\_64  
 Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l\_cc\_p\_10.0.023  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor E5320, 1.86 GHz)

SPECint\_rate2006 = 75.4

SPECint\_rate\_base2006 = 69.6

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: Sep-2007

Software Availability: Jun-2007

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b>815</b>	<b>96.0</b>	816	95.7	801	97.6	8	<b>682</b>	<b>115</b>	683	114	682	115
401.bzip2	8	1392	55.5	<b>1392</b>	<b>55.5</b>	1391	55.5	8	<b>1323</b>	<b>58.3</b>	1321	58.4	1324	58.3
403.gcc	8	962	66.9	<b>965</b>	<b>66.8</b>	975	66.1	8	962	66.9	<b>965</b>	<b>66.8</b>	975	66.1
429.mcf	8	1451	50.3	<b>1452</b>	<b>50.3</b>	1455	50.1	8	1450	50.3	1453	50.2	<b>1451</b>	<b>50.3</b>
445.gobmk	8	899	93.4	899	93.3	<b>899</b>	<b>93.4</b>	8	<b>827</b>	<b>101</b>	828	101	822	102
456.hmmer	8	998	74.8	<b>998</b>	<b>74.8</b>	999	74.7	8	810	92.1	<b>807</b>	<b>92.5</b>	805	92.7
458.sjeng	8	1098	88.1	<b>1100</b>	<b>88.0</b>	1100	88.0	8	979	98.9	982	98.6	<b>981</b>	<b>98.7</b>
462.libquantum	8	4399	37.7	<b>4399</b>	<b>37.7</b>	4393	37.7	8	3689	44.9	<b>3693</b>	<b>44.9</b>	3694	44.9
464.h264ref	8	1185	149	1186	149	<b>1185</b>	<b>149</b>	8	1121	158	<b>1120</b>	<b>158</b>	1119	158
471.omnetpp	8	1042	48.0	<b>1030</b>	<b>48.6</b>	1029	48.6	8	998	50.1	<b>999</b>	<b>50.0</b>	1000	50.0
473.astar	8	<b>1090</b>	<b>51.5</b>	1089	51.6	1092	51.4	8	1022	55.0	<b>1019</b>	<b>55.1</b>	1019	55.1
483.xalancbmk	8	660	83.6	<b>661</b>	<b>83.5</b>	663	83.3	8	660	83.6	<b>661</b>	<b>83.5</b>	663	83.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Bios settings:  
Hardware Prefetcher: Disabled  
Adjacent Sector Prefetch: Disabled  
ulimit -s unlimited used to set stack size to unlimited  
taskset command used to assign copies to cores  
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,  
for peak, are compiled in 64-bit mode

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Intel Corporation**

**SPECint\_rate2006 = 75.4**

Supermicro X7DB8+ (Intel Xeon processor E5320, 1.86 GHz)

**SPECint\_rate\_base2006 = 69.6**

**CPU2006 license:** 13

**Test date:** May-2007

**Test sponsor:** Intel Corporation

**Hardware Availability:** Sep-2007

**Tested by:** Intel Corporation

**Software Availability:** Jun-2007

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Intel Corporation**

**SPECint\_rate2006 = 75.4**

Supermicro X7DB8+ (Intel Xeon processor E5320, 1.86 GHz)

**SPECint\_rate\_base2006 = 69.6**

**CPU2006 license:** 13

**Test date:** May-2007

**Test sponsor:** Intel Corporation

**Hardware Availability:** Sep-2007

**Tested by:** Intel Corporation

**Software Availability:** Jun-2007

## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec\_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0  
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec\_div -ansi-alias -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.00.xml>



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor E5320, 1.86 GHz)

SPECint\_rate2006 = 75.4

SPECint\_rate\_base2006 = 69.6

**CPU2006 license:** 13

**Test sponsor:** Intel Corporation

**Tested by:** Intel Corporation

**Test date:** May-2007

**Hardware Availability:** Sep-2007

**Software Availability:** Jun-2007

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 11:22:20 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 26 June 2007.