SPEC® CINT2006 Result
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Hewlett-Packard Company

ProLiant DL380 G5
(1.60 GHz, Intel Xeon processor E5310)

SPECint®_rate2006 = 62.2

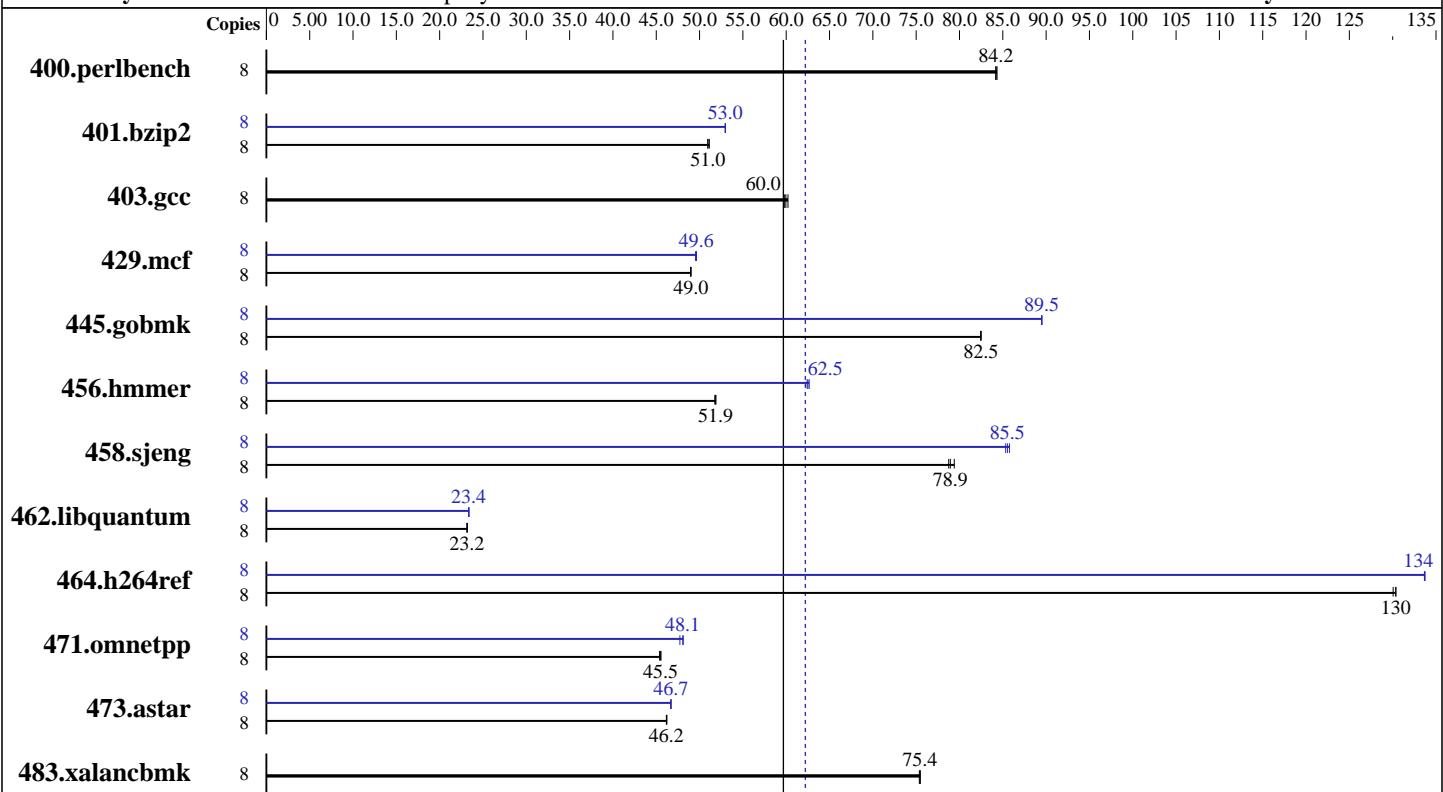SPECint_rate_base2006 = 59.7

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: May-2007

Hardware Availability: Jan-2007

Software Availability: Mar-2007

Copies 0 5.00 10.0 15.0 20.0 25.0 30.0 35.0 40.0 45.0 50.0 55.0 60.0 65.0 70.0 75.0 80.0 85.0 90.0 95.0 100 105 110 115 120 125 135

Benchmark	Copies	Value
400.perlbench	8	84.2
401.bzip2	8	53.0
401.bzip2	8	51.0
403.gcc	8	60.0
429.mcf	8	49.6
429.mcf	8	49.0
445.gobmk	8	89.5
445.gobmk	8	82.5
456.hmmer	8	62.5
456.hmmer	8	51.9
458.sjeng	8	85.5
458.sjeng	8	78.9
462.libquantum	8	23.4
462.libquantum	8	23.2
464.h264ref	8	134
464.h264ref	8	130
471.omnetpp	8	48.1
471.omnetpp	8	45.5
473.astar	8	46.7
473.astar	8	46.2
483.xalancbmk	8	75.4

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Hardware

CPU Name: Intel Xeon E5310

CPU Characteristics: 1.60 GHz, 2x4 MB L2 shared, 1066 MHz system bus

CPU MHz: 1600

FPU: Integrated

CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip

CPU(s) orderable: 1,2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

L3 Cache: None

Other Cache: None

Memory: 16 GB (8x2 GB PC2-5300F CL5)

Disk Subsystem: 1x72 GB 10 K SAS

Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp

Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Build , Package ID: l_cc_c_9.1.049

Auto Parallel: No

File System: ext2

System State: Multi-user run level 3

Base Pointers: 32-bit

Peak Pointers: Not Applicable

Other Software: MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	929	84.2	927	84.3	**928**	**84.2**	8	929	84.2	927	84.3	**928**	**84.2**
401.bzip2	8	1510	51.1	1515	50.9	**1515**	**51.0**	8	1458	53.0	1457	53.0	**1458**	**53.0**
403.gcc	8	1077	59.8	**1074**	**60.0**	1070	60.2	8	1077	59.8	**1074**	**60.0**	1070	60.2
429.mcf	8	1489	49.0	**1489**	**49.0**	1490	49.0	8	**1471**	**49.6**	1473	49.5	1470	49.6
445.gobmk	8	1018	82.5	1017	82.5	**1018**	**82.5**	8	937	89.5	938	89.5	**938**	**89.5**
456.hmmer	8	**1439**	**51.9**	1439	51.9	1442	51.8	8	1196	62.4	1191	62.7	**1194**	**62.5**
458.sjeng	8	1229	78.8	1219	79.4	**1226**	**78.9**	8	1134	85.3	1129	85.8	**1132**	**85.5**
462.libquantum	8	7149	23.2	**7156**	**23.2**	7158	23.2	8	7094	23.4	7093	23.4	**7094**	**23.4**
464.h264ref	8	1358	130	1361	130	**1358**	**130**	8	1324	134	**1324**	**134**	1325	134
471.omnetpp	8	1098	45.5	**1099**	**45.5**	1102	45.4	8	**1040**	**48.1**	1039	48.1	1048	47.7
473.astar	8	1216	46.2	1215	46.2	**1216**	**46.2**	8	1203	46.7	1202	46.7	**1203**	**46.7**
483.xalancbmk	8	**732**	**75.4**	731	75.5	732	75.4	8	**732**	**75.4**	731	75.5	732	75.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
    Power Regulator set to Static High Performance Mode in BIOS.
    Adjacent Sector Prefetch and HW Prefetch Disabled in BIOS.
    taskset utility used to bind CPU(s) to processes
     ulimit -s unlimited set
```

Base Compiler Invocation

C benchmarks:
        icc

C++ benchmarks:
        icpc

Base Portability Flags

```
      400.perlbench: -DSPEC_CPU_LINUX_X64
          401.bzip2: -DSPEC_CPU_LP64
     462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX
```

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Base Optimization Flags

C benchmarks:
```
-fast
```

C++ benchmarks:
```
-xP -O3 -ipo -no-prec-div -L/cpu2006/SmartHeap_8_1/lib -lsmartheap
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/9.1.049/bin/icc
           -I/opt/intel/cce/9.1.049/include
           -L/opt/intel/cce/9.1.049/lib
```

```
456.hmmer: /opt/intel/cce/9.1.049/bin/icc
           -I/opt/intel/cce/9.1.049/include
           -L/opt/intel/cce/9.1.049/lib
```

```
462.libquantum: /opt/intel/cce/9.1.049/bin/icc
                -I/opt/intel/cce/9.1.049/include
                -L/opt/intel/cce/9.1.049/lib
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_X64
   401.bzip2: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: basepeak = yes
```

```
   401.bzip2: -fast -auto_ilp32
```

```
     403.gcc: basepeak = yes
```

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Peak Optimization Flags (Continued)

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -auto_ilp32`

445.gobmk: `Same as 429.mcf`

456.hmmer: `Same as 429.mcf`

458.sjeng: `Same as 429.mcf`

462.libquantum: `Same as 429.mcf`

464.h264ref: `Same as 429.mcf`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo`
`-no-prec-div -fast -auto_ilp32`
`-L/cpu2006/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -auto_ilp32`
`-L/cpu2006/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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