SPEC® CINT2006 Result

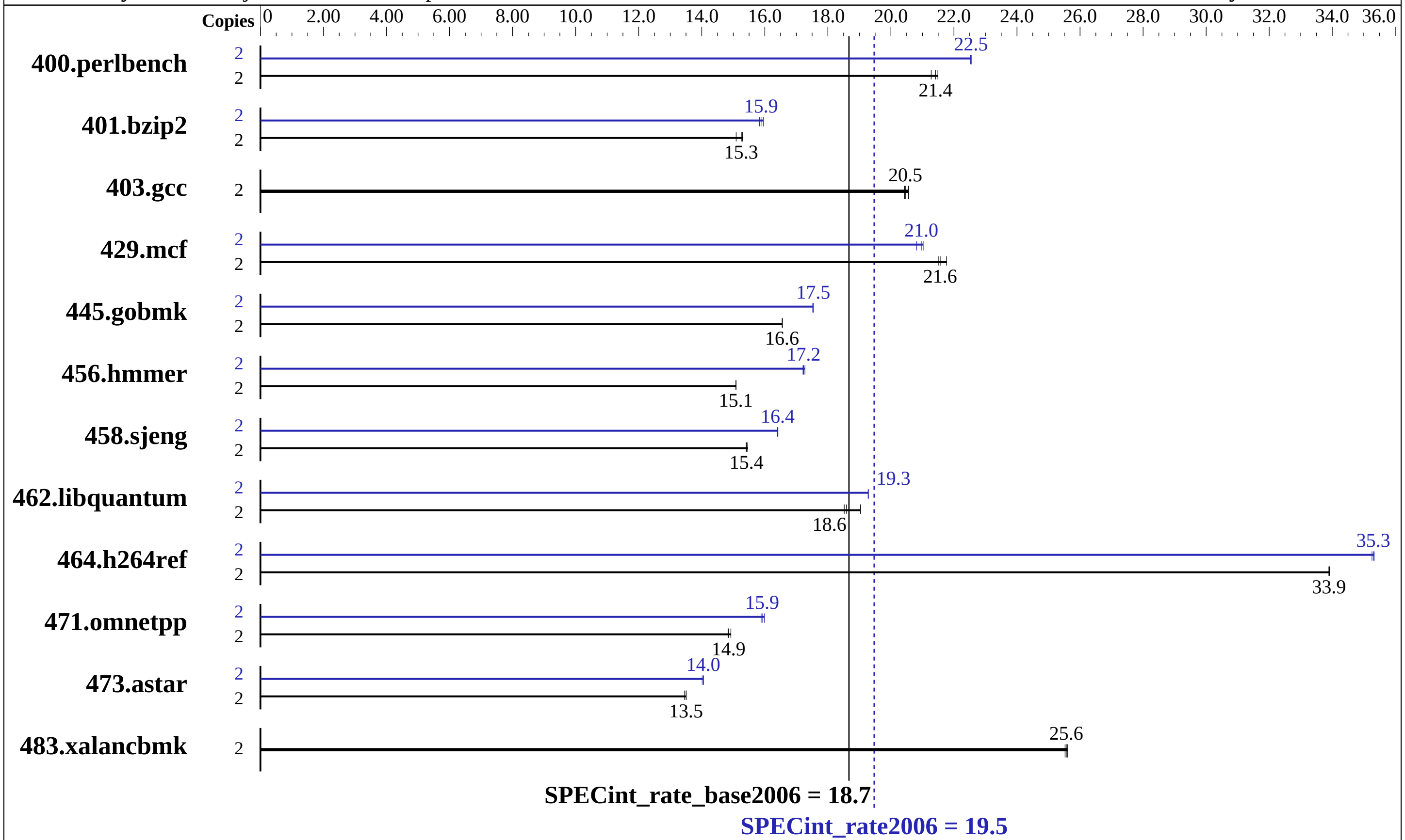
Fujitsu Siemens Computers

PRIMERGY TX150 S5, Intel Pentium D processor 925, 3.0 GHz

SPECint®_rate2006 = 19.5

SPECint_rate_base2006 = 18.7

CPU2006 license: 22		**Test date:**	Jun-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Oct-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Mar-2007



Hardware

CPU Name:	Intel Pentium D 925
CPU Characteristics:	800 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per core
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
Disk Subsystem:	SATA(160GB 7200 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ReiserFS
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	909	21.5	**912**	**21.4**	918	21.3	2	868	22.5	867	22.5	**867**	**22.5**
401.bzip2	2	1262	15.3	**1265**	**15.3**	1279	15.1	2	1210	16.0	1219	15.8	**1215**	**15.9**
403.gcc	2	783	20.6	788	20.4	**787**	**20.5**	2	783	20.6	788	20.4	**787**	**20.5**
429.mcf	2	**846**	**21.6**	848	21.5	838	21.8	2	876	20.8	**870**	**21.0**	868	21.0
445.gobmk	2	1267	16.6	**1267**	**16.6**	1268	16.6	2	1197	17.5	1196	17.5	**1196**	**17.5**
456.hmmer	2	**1237**	**15.1**	1237	15.1	1237	15.1	2	1080	17.3	**1083**	**17.2**	1084	17.2
458.sjeng	2	1566	15.5	1570	15.4	**1569**	**15.4**	2	1475	16.4	**1475**	**16.4**	1474	16.4
462.libquantum	2	2238	18.5	2177	19.0	**2229**	**18.6**	2	**2149**	**19.3**	2149	19.3	2150	19.3
464.h264ref	2	1305	33.9	**1306**	**33.9**	1306	33.9	2	1253	35.3	**1254**	**35.3**	1255	35.3
471.omnetpp	2	843	14.8	838	14.9	**842**	**14.9**	2	787	15.9	782	16.0	**785**	**15.9**
473.astar	2	1039	13.5	1043	13.5	**1040**	**13.5**	2	1002	14.0	**999**	**14.0**	999	14.1
483.xalancbmk	2	539	25.6	**540**	**25.6**	541	25.5	2	539	25.6	**540**	**25.6**	541	25.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.


For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
 icc

C++ benchmarks:
 icpc

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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

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Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo` `-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 11:18:08 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 June 2007.