



SPEC[®] CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY TX600 S3, Intel Xeon processor 7110M, 2.60 GHz

SPECint[®]_rate2006 = 55.6

SPECint_rate_base2006 = 52.3

CPU2006 license: 22

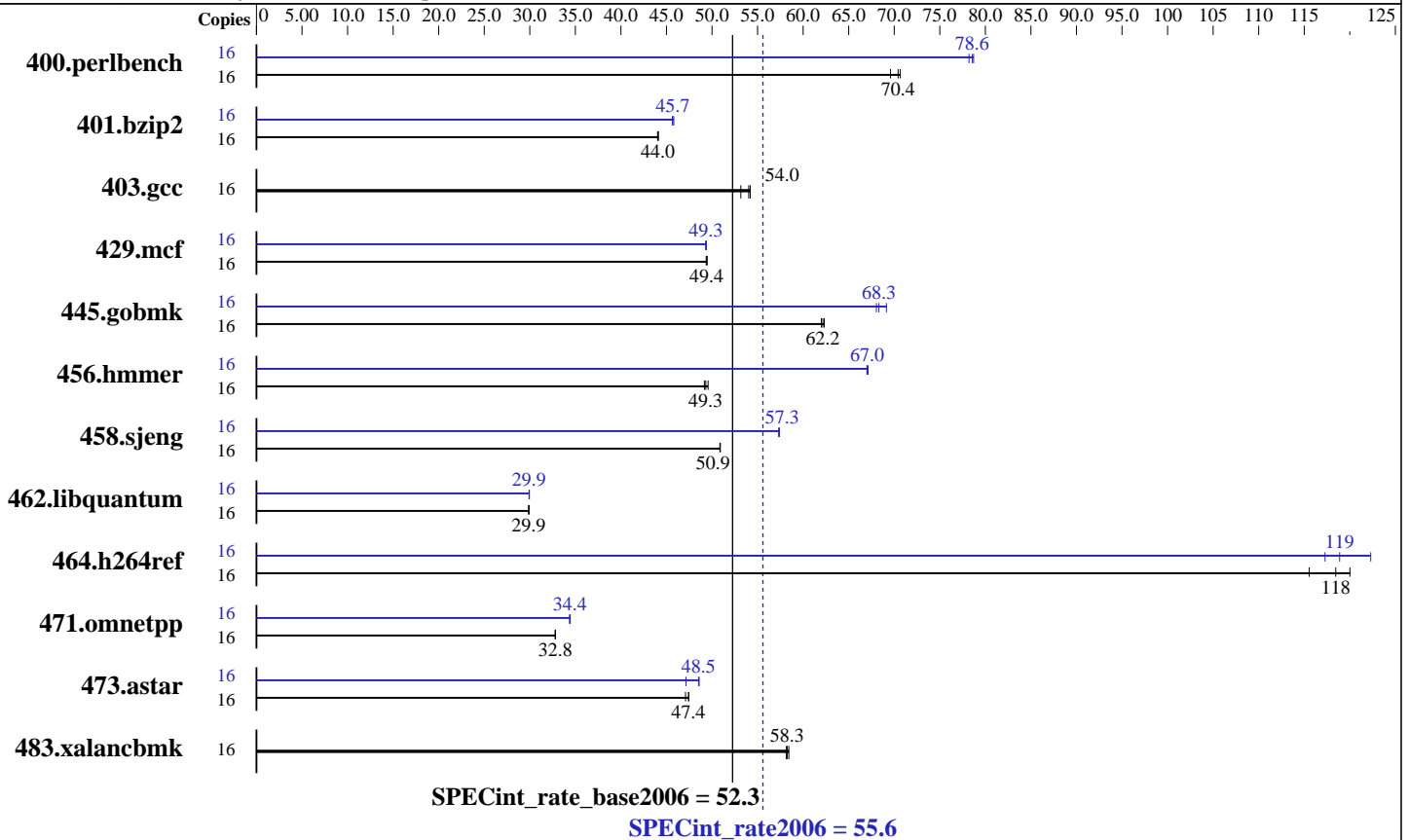
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Aug-2006

Software Availability: Mar-2007



Hardware

CPU Name: Intel Xeon 7110M
 CPU Characteristics: 800 MHz system bus
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (16x2 GB DDR2 PC2-3200R, 1 rank, CAS 3-3-3, with ECC)
 Disk Subsystem: Seagate ST973401SS (SAS 73GB 10 krpm)
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
 Auto Parallel: No
 File System: ReiserFS
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<u>2220</u>	<u>70.4</u>	2246	69.6	2212	70.7	16	1986	78.7	<u>1990</u>	<u>78.6</u>	1999	78.2
401.bzip2	16	3507	44.0	<u>3505</u>	<u>44.0</u>	3500	44.1	16	3383	45.6	<u>3379</u>	<u>45.7</u>	3371	45.8
403.gcc	16	2377	54.2	<u>2384</u>	<u>54.0</u>	2423	53.2	16	2377	54.2	<u>2384</u>	<u>54.0</u>	2423	53.2
429.mcf	16	2950	49.5	<u>2953</u>	<u>49.4</u>	2956	49.4	16	2954	49.4	2959	49.3	<u>2958</u>	<u>49.3</u>
445.gobmk	16	2693	62.3	<u>2700</u>	<u>62.2</u>	2707	62.0	16	2427	69.1	2467	68.0	<u>2457</u>	<u>68.3</u>
456.hmmmer	16	<u>3027</u>	<u>49.3</u>	3036	49.2	3012	49.6	16	<u>2227</u>	<u>67.0</u>	2224	67.1	2228	67.0
458.sjeng	16	3806	50.9	<u>3804</u>	<u>50.9</u>	3803	50.9	16	3377	57.3	<u>3376</u>	<u>57.3</u>	3373	57.4
462.libquantum	16	11095	29.9	11085	29.9	<u>11092</u>	<u>29.9</u>	16	<u>11076</u>	<u>29.9</u>	11080	29.9	11066	30.0
464.h264ref	16	2950	120	<u>2989</u>	<u>118</u>	3065	116	16	2895	122	3019	117	<u>2978</u>	<u>119</u>
471.omnetpp	16	<u>3048</u>	<u>32.8</u>	3050	32.8	3047	32.8	16	2903	34.4	<u>2906</u>	<u>34.4</u>	2910	34.4
473.astar	16	2385	47.1	<u>2368</u>	<u>47.4</u>	2368	47.4	16	2383	47.1	2312	48.6	<u>2315</u>	<u>48.5</u>
483.xalancbmk	16	<u>1895</u>	<u>58.3</u>	1899	58.1	1889	58.4	16	<u>1895</u>	<u>58.3</u>	1899	58.1	1889	58.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Enable

This result was measured on the PRIMERGY RX600 S3.
The PRIMERGY RX600 S3 and the PRIMERGY TX600 S3 are electronically equivalent.

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Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmarheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmarheap

445.gobmk: Same as 429.mcf

456.hmmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmarheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmarheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml



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