SPEC® CINT2006 Result
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Fujitsu Siemens Computers

PRIMERGY RX600 S3, Intel Xeon processor 7110M, 2.60 GHz

$SPECint^{®}_rate2006 = $ 55.6

$SPECint_rate_base2006 = $ 52.3

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: May-2007
Hardware Availability: Aug-2006
Software Availability: Mar-2007

	Copies	Value
400.perlbench	16	78.6
	16	70.4
401.bzip2	16	45.7
	16	44.0
403.gcc	16	54.0
429.mcf	16	49.3
	16	49.4
445.gobmk	16	68.3
	16	62.2
456.hmmer	16	67.0
	16	49.3
458.sjeng	16	57.3
	16	50.9
462.libquantum	16	29.9
	16	29.9
464.h264ref	16	119
	16	118
471.omnetpp	16	34.4
	16	32.8
473.astar	16	48.5
	16	47.4
483.xalancbmk	16	58.3

SPECint_rate_base2006 = 52.3

SPECint_rate2006 = 55.6

Hardware

CPU Name: Intel Xeon 7110M
CPU Characteristics: 800 MHz system bus
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable: 1,2,4 chips
Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core
L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (16x2 GB DDR2 PC2-3200R, 1 rank, CAS 3-3-3, with ECC)
Disk Subsystem: Seagate ST973401SS (SAS 73GB 10 krpm)
Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel: No
File System: ReiserFS
System State: Multiuser, Runlevel 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Smart Heap Library, Version 8.1

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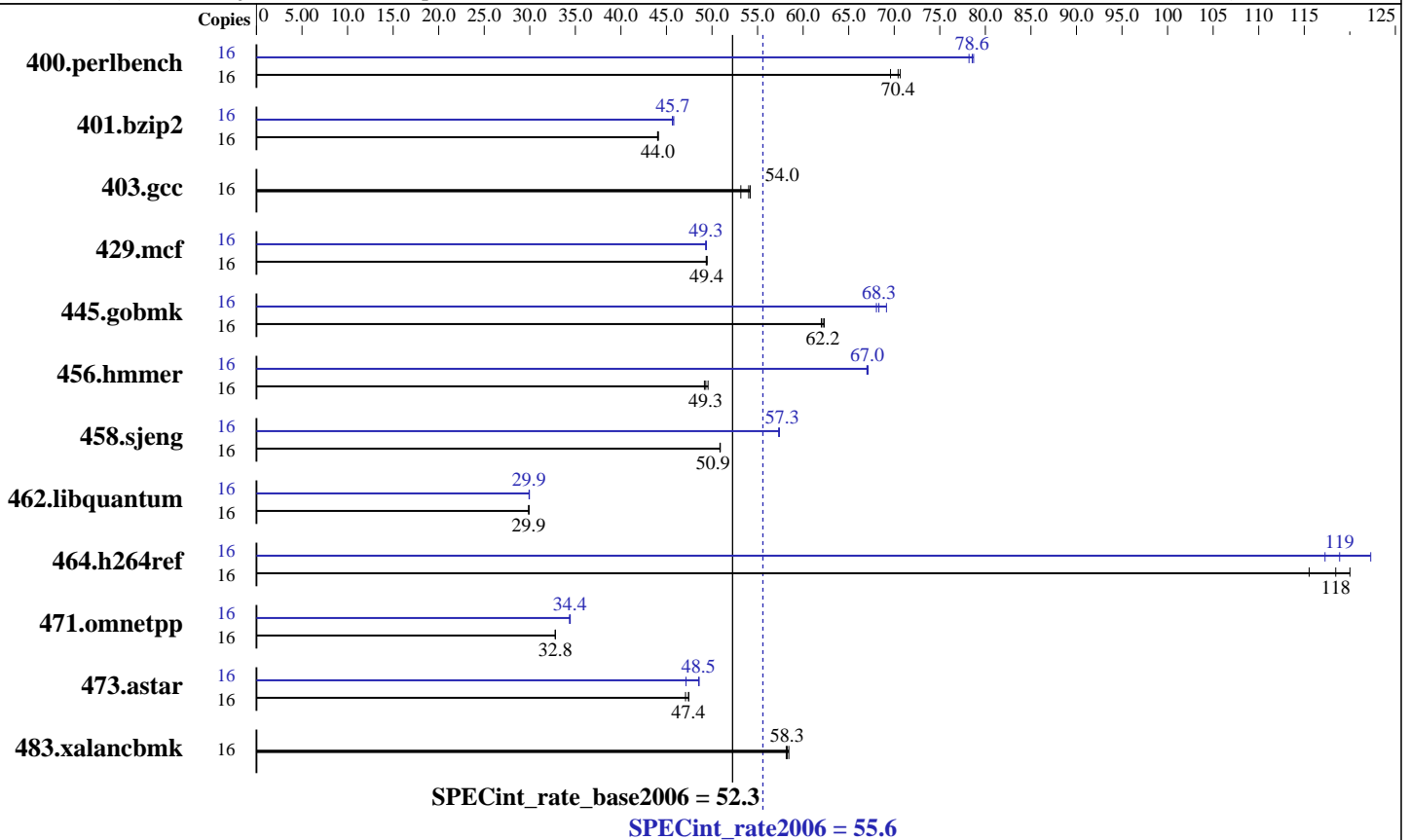
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Results Table

| Benchmark | Base | | | | | | Peak | | | | | | |
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	**2220**	**70.4**	2246	69.6	2212	70.7	16	1986	78.7	**1990**	**78.6**	1999	78.2
401.bzip2	16	3507	44.0	**3505**	**44.0**	3500	44.1	16	3383	45.6	**3379**	**45.7**	3371	45.8
403.gcc	16	2377	54.2	**2384**	**54.0**	2423	53.2	16	2377	54.2	**2384**	**54.0**	2423	53.2
429.mcf	16	2950	49.5	**2953**	**49.4**	2956	49.4	16	2954	49.4	2959	49.3	**2958**	**49.3**
445.gobmk	16	2693	62.3	**2700**	**62.2**	2707	62.0	16	2427	69.1	2467	68.0	**2457**	**68.3**
456.hmmer	16	**3027**	**49.3**	3036	49.2	3012	49.6	16	**2227**	**67.0**	2224	67.1	2228	67.0
458.sjeng	16	3806	50.9	**3804**	**50.9**	3803	50.9	16	3377	57.3	**3376**	**57.3**	3373	57.4
462.libquantum	16	11095	29.9	11085	29.9	**11092**	**29.9**	16	**11076**	**29.9**	11080	29.9	11066	30.0
464.h264ref	16	2950	120	**2989**	**118**	3065	116	16	2895	122	3019	117	**2978**	**119**
471.omnetpp	16	**3048**	**32.8**	3050	32.8	3047	32.8	16	2903	34.4	**2906**	**34.4**	2910	34.4
473.astar	16	2385	47.1	**2368**	**47.4**	2368	47.4	16	2383	47.1	2312	48.6	**2315**	**48.5**
483.xalancbmk	16	**1895**	**58.3**	1899	58.1	1889	58.4	16	**1895**	**58.3**	1899	58.1	1889	58.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
    '/usr/bin/taskset' used to bind processes to CPUs

General Notes

    The system bus runs at 800 MHz

    All binaries were built with 32-bit Intel compiler except:
    401.bzip2, 456.hmmer and 462.libquantum in peak were built with
    64-bit Intel compiler by changing the path for include and library files.

    BIOS configuration:
    Hardware Prefetch = Enable

    The PRIMERGY RX600 S3 and the PRIMERGY TX600 S3 are electronically equivalent.

    For information about Fujitsu Siemens Computers in your country please see:
    http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
        icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_X64
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -fast

C++ benchmarks:
    -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc

    401.bzip2: /opt/intel/cce/9.1.047/bin/icc
            -I/opt/intel/cce/9.1.047/include
            -L/opt/intel/cce/9.1.047/lib

    456.hmmer: /opt/intel/cce/9.1.047/bin/icc
            -I/opt/intel/cce/9.1.047/include
            -L/opt/intel/cce/9.1.047/lib

    462.libquantum: /opt/intel/cce/9.1.047/bin/icc
            -I/opt/intel/cce/9.1.047/include
            -L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
    icpc

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

        456.hmmer: `-DSPEC_CPU_LP64`
    462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
     483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

      400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

        401.bzip2: `-fast`

          403.gcc: `basepeak = yes`

          429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`
                `-L/opt/SmartHeap_8_1/lib -lsmartheap`

       445.gobmk: `Same as 429.mcf`

       456.hmmer: `Same as 400.perlbench`

        458.sjeng: `Same as 429.mcf`

    462.libquantum: `Same as 400.perlbench`

      464.h264ref: `Same as 429.mcf`

C++ benchmarks:

      471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo`
                `-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

        473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`
                `-L/opt/SmartHeap_8_1/lib -lsmartheap`

     483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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