



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu Siemens Computers

PRIMERGY BX620 S3, Intel Xeon processor E5320,  
1.86 GHz

SPECint®\_rate2006 = 36.6

SPECint\_rate\_base2006 = 35.0

CPU2006 license: 22

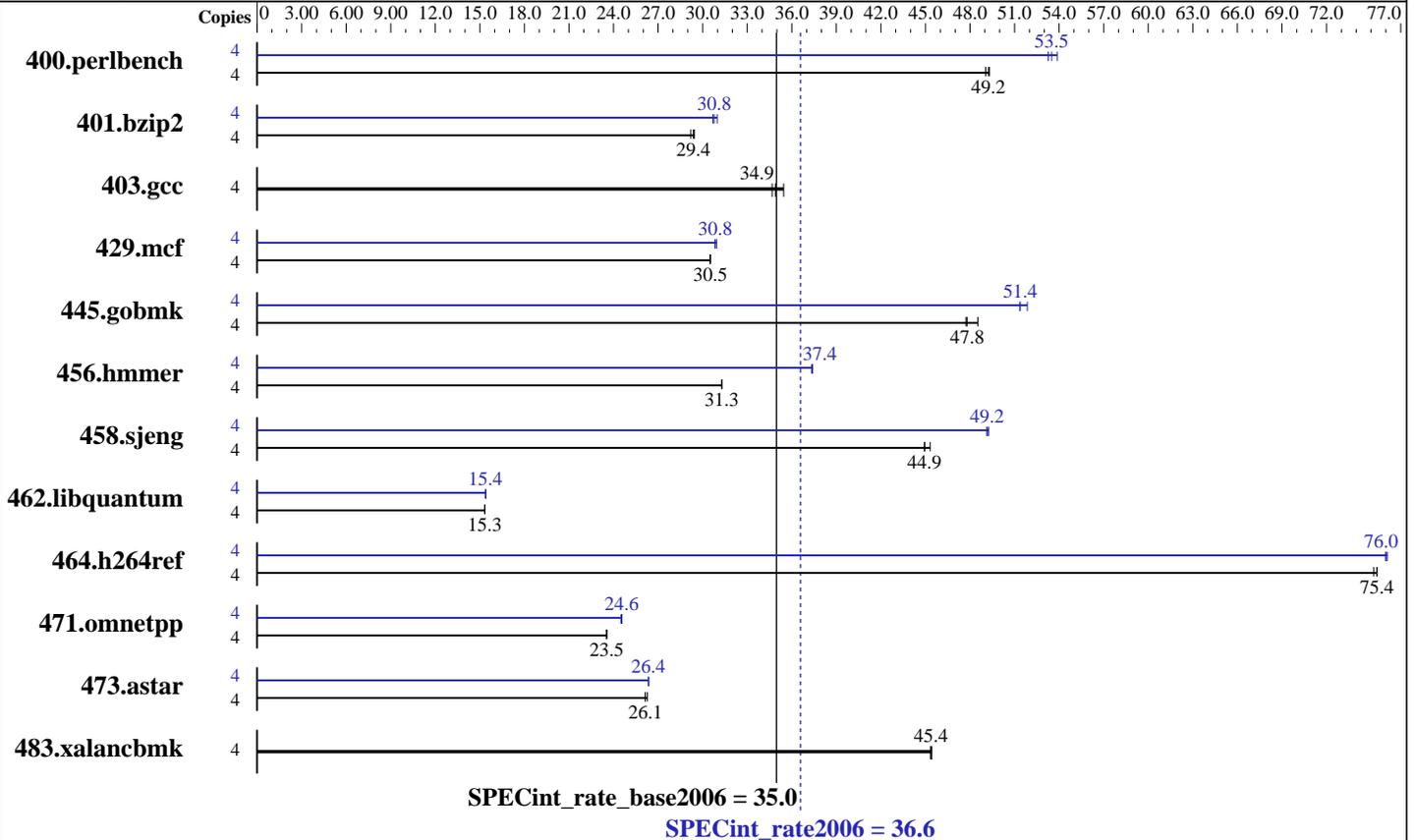
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Nov-2006

Software Availability: Mar-2007



### Hardware

CPU Name: Intel Xeon E5320  
 CPU Characteristics: 1067 MHz system bus  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: SAS (36GB 10000 rpm)  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu Siemens Computers

PRIMERGY BX620 S3, Intel Xeon processor E5320,  
1.86 GHz

SPECint\_rate2006 = 36.6

SPECint\_rate\_base2006 = 35.0

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Nov-2006

Software Availability: Mar-2007

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	793	49.3	<b>794</b>	<b>49.2</b>	797	49.1	4	<b>730</b>	<b>53.5</b>	725	53.9	734	53.3
401.bzip2	4	<b>1314</b>	<b>29.4</b>	1311	29.5	1322	29.2	4	1246	31.0	1258	30.7	<b>1254</b>	<b>30.8</b>
403.gcc	4	<b>923</b>	<b>34.9</b>	929	34.7	908	35.5	4	<b>923</b>	<b>34.9</b>	929	34.7	908	35.5
429.mcf	4	1195	30.5	<b>1195</b>	<b>30.5</b>	1197	30.5	4	1179	30.9	1183	30.8	<b>1183</b>	<b>30.8</b>
445.gobmk	4	<b>878</b>	<b>47.8</b>	865	48.5	879	47.7	4	<b>817</b>	<b>51.4</b>	817	51.3	809	51.9
456.hmmer	4	1193	31.3	<b>1193</b>	<b>31.3</b>	1193	31.3	4	<b>998</b>	<b>37.4</b>	998	37.4	1000	37.3
458.sjeng	4	<b>1077</b>	<b>44.9</b>	1077	44.9	1068	45.3	4	<b>985</b>	<b>49.2</b>	982	49.3	985	49.1
462.libquantum	4	<b>5405</b>	<b>15.3</b>	5403	15.3	5406	15.3	4	<b>5386</b>	<b>15.4</b>	5389	15.4	5378	15.4
464.h264ref	4	1174	75.4	<b>1174</b>	<b>75.4</b>	1178	75.2	4	<b>1164</b>	<b>76.0</b>	1165	76.0	1163	76.1
471.omnetpp	4	1062	23.5	<b>1062</b>	<b>23.5</b>	1063	23.5	4	1018	24.6	<b>1018</b>	<b>24.6</b>	1020	24.5
473.astar	4	<b>1074</b>	<b>26.1</b>	1075	26.1	1069	26.3	4	<b>1065</b>	<b>26.4</b>	1066	26.3	1065	26.4
483.xalancbmk	4	608	45.4	<b>608</b>	<b>45.4</b>	609	45.3	4	608	45.4	<b>608</b>	<b>45.4</b>	609	45.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:  
401.bzip2, 456.hmmer and 462.libquantum in peak were built with  
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:  
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers in your country please see:  
<http://www.fujitsu-siemens.com/countries>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu Siemens Computers**

PRIMERGY BX620 S3, Intel Xeon processor E5320,  
1.86 GHz

**SPECint\_rate2006 = 36.6**

**SPECint\_rate\_base2006 = 35.0**

**CPU2006 license:** 22

**Test sponsor:** Fujitsu Siemens Computers

**Tested by:** Fujitsu Siemens Computers

**Test date:** May-2007

**Hardware Availability:** Nov-2006

**Software Availability:** Mar-2007

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu Siemens Computers

PRIMERGY BX620 S3, Intel Xeon processor E5320,  
1.86 GHz

**SPECint\_rate2006 = 36.6**

**SPECint\_rate\_base2006 = 35.0**

**CPU2006 license:** 22

**Test sponsor:** Fujitsu Siemens Computers

**Tested by:** Fujitsu Siemens Computers

**Test date:** May-2007

**Hardware Availability:** Nov-2006

**Software Availability:** Mar-2007

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -xP -O3 -ipo  
-no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

473.astar: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 11:20:40 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 26 June 2007.