SPEC® CINT2006 Result
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Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor X5355, 2.66 GHz)

SPECint®_rate2006 =   99.9
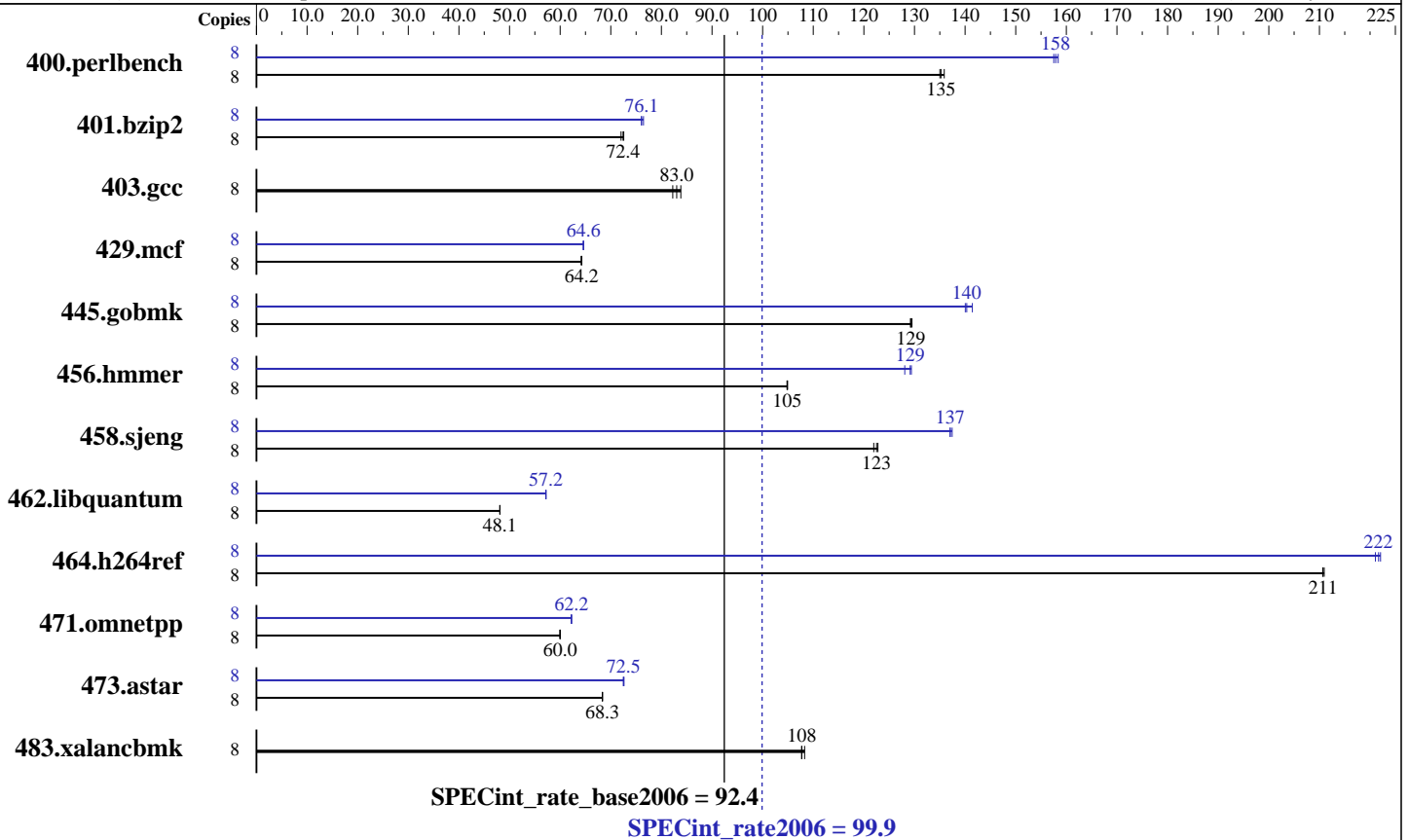
SPECint_rate_base2006 = 92.4

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: May-2007

Software Availability: May-2007

Copies

Benchmark	Base	Peak
400.perlbench	135	158
401.bzip2	72.4	76.1
403.gcc	83.0	
429.mcf	64.2	64.6
445.gobmk	129	140
456.hmmer	105	129
458.sjeng	123	137
462.libquantum	48.1	57.2
464.h264ref	211	222
471.omnetpp	60.0	62.2
473.astar	68.3	72.5
483.xalancbmk	108	

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Hardware
CPU Name: Intel Xeon X5355

CPU Characteristics: Quad Core, 2.66 GHz

CPU MHz: 2666

FPU: Integrated

CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip

CPU(s) orderable: 1,2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

L3 Cache: None

Other Cache: None

Memory: 16 GB (8 * 2GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)

Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only

Other Hardware: None

Software
Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64

Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023

Auto Parallel: No

File System: NTFS

System State: Default

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**578**	**135**	575	136	579	135	8	**495**	**158**	493	158	496	158
401.bzip2	8	1072	72.0	**1066**	**72.4**	1064	72.6	8	1015	76.1	**1015**	**76.1**	1010	76.5
403.gcc	8	768	83.8	783	82.2	**776**	**83.0**	8	768	83.8	783	82.2	**776**	**83.0**
429.mcf	8	1138	64.1	**1136**	**64.2**	1135	64.3	8	**1129**	**64.6**	1130	64.5	1129	64.6
445.gobmk	8	649	129	648	130	**649**	**129**	8	599	140	**598**	**140**	593	141
456.hmmer	8	711	105	712	105	**712**	**105**	8	**578**	**129**	577	129	583	128
458.sjeng	8	794	122	**790**	**123**	788	123	8	704	137	706	137	**706**	**137**
462.libquantum	8	3447	48.1	**3447**	**48.1**	3446	48.1	8	2897	57.2	2900	57.2	**2899**	**57.2**
464.h264ref	8	**840**	**211**	840	211	839	211	8	797	222	**799**	**222**	801	221
471.omnetpp	8	**833**	**60.0**	833	60.0	834	59.9	8	803	62.3	804	62.2	**803**	**62.2**
473.astar	8	821	68.4	822	68.3	**822**	**68.3**	8	**775**	**72.5**	773	72.6	775	72.5
483.xalancbmk	8	510	108	**512**	**108**	513	108	8	510	108	**512**	**108**	513	108

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios settings:
Hardware Prefetcher:     Disabled
Adjacent Sector Prefetch: Disabled
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
taskset was used to bind processes to cores
```

Base Compiler Invocation

C benchmarks:
    `icc`

C++ benchmarks:
    `icpc`

Base Portability Flags

    400.perlbench: `-DSPEC_CPU_LINUX_IA32`
    462.libquantum: `-DSPEC_CPU_LINUX`
    483.xalancbmk: `-DSPEC_CPU_LINUX`

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Base Optimization Flags

C benchmarks:
    -fast

C++ benchmarks:
    -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
    -L/spec/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc

    401.bzip2: /opt/intel/cce/10.0.023/bin/icc

    456.hmmer: /opt/intel/cce/10.0.023/bin/icc

C++ benchmarks:
    icpc

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
       401.bzip2: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

    400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
                -prefetch

       401.bzip2: -L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include
                -prof-gen(pass 1) -prof-use(pass 2) -fast

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Peak Optimization Flags (Continued)

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias

456.hmmer: -L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include
-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
-prefetch -opt-streaming-stores always

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.xml

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