



# SPEC® CINT2006 Result

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## Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor X5355, 2.66 GHz)

SPECint®2006 = 19.1

SPECint\_base2006 = 17.3

CPU2006 license: 13

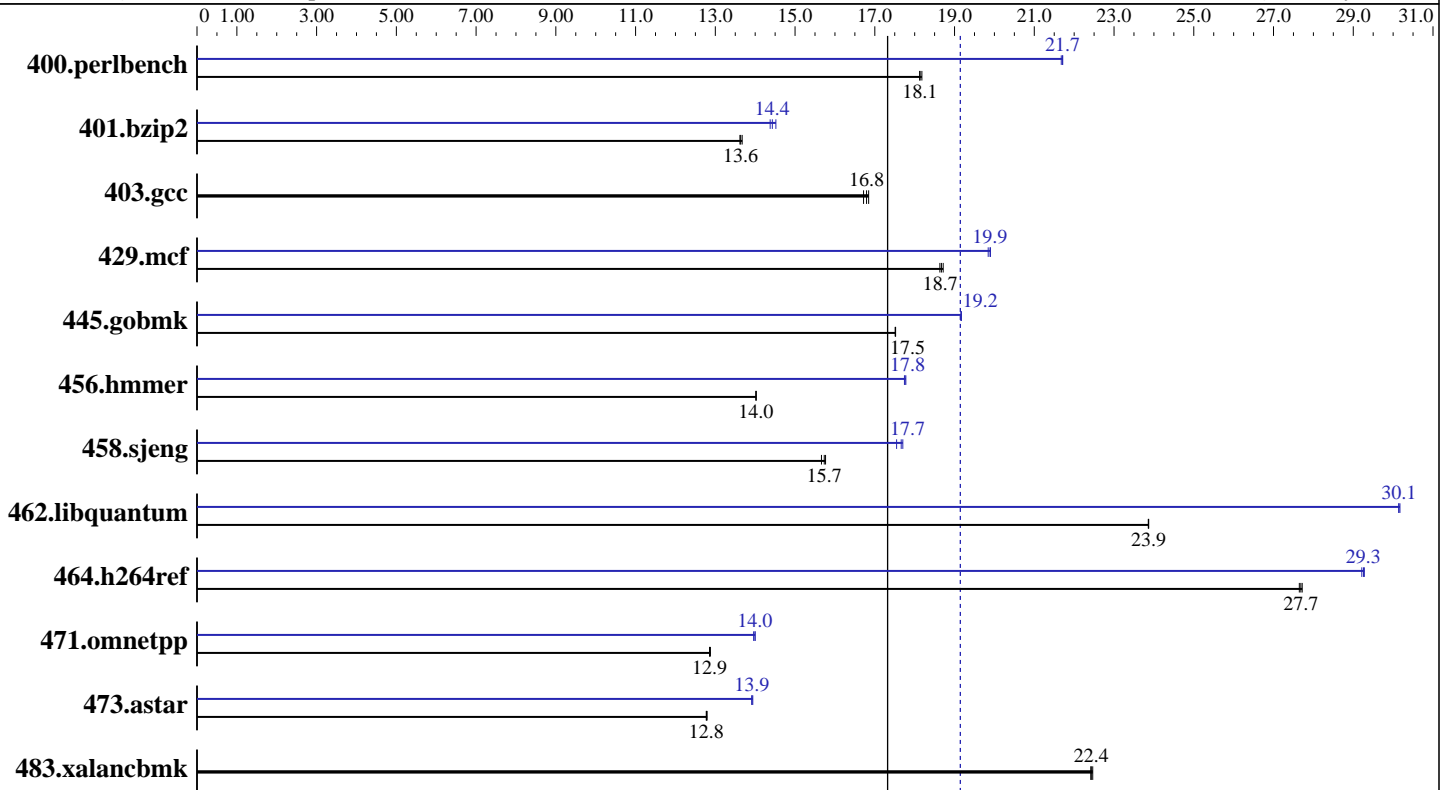
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: May-2007

Software Availability: May-2007



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### Hardware

CPU Name: Intel Xeon X5355  
 CPU Characteristics: Quad Core, 2.66 GHz  
 CPU MHz: 2666  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 \* 2GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86\_64  
 Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l\_cc\_p\_10.0.023  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	539	18.1	537	18.2	<u>539</u>	<u>18.1</u>	<u>450</u>	<u>21.7</u>	451	21.7	450	21.7
401.bzip2	706	13.7	<u>708</u>	<u>13.6</u>	709	13.6	665	14.5	671	14.4	<u>669</u>	<u>14.4</u>
403.gcc	<u>479</u>	<u>16.8</u>	482	16.7	478	16.8	<u>479</u>	<u>16.8</u>	482	16.7	478	16.8
429.mcf	<u>488</u>	<u>18.7</u>	489	18.6	487	18.7	<u>459</u>	<u>19.9</u>	458	19.9	460	19.8
445.gobmk	599	17.5	599	17.5	<u>599</u>	<u>17.5</u>	547	19.2	547	19.2	<u>547</u>	<u>19.2</u>
456.hmmer	<u>665</u>	<u>14.0</u>	665	14.0	666	14.0	525	17.8	526	17.7	<u>525</u>	<u>17.8</u>
458.sjeng	<u>769</u>	<u>15.7</u>	768	15.8	773	15.7	684	17.7	690	17.5	<u>685</u>	<u>17.7</u>
462.libquantum	868	23.9	<u>868</u>	<u>23.9</u>	868	23.9	687	30.2	<u>687</u>	<u>30.1</u>	687	30.1
464.h264ref	<u>799</u>	<u>27.7</u>	800	27.6	798	27.7	756	29.3	<u>756</u>	<u>29.3</u>	758	29.2
471.omnetpp	<u>486</u>	<u>12.9</u>	485	12.9	486	12.9	447	14.0	<u>447</u>	<u>14.0</u>	448	14.0
473.astar	550	12.8	549	12.8	<u>549</u>	<u>12.8</u>	<u>504</u>	<u>13.9</u>	505	13.9	504	13.9
483.xalanbmk	<u>308</u>	<u>22.4</u>	307	22.5	308	22.4	<u>308</u>	<u>22.4</u>	307	22.5	308	22.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Bios settings:  
Hardware Prefetcher: Enabled  
Adjacent Sector Prefetch: Disabled  
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc

456.hmmer: /opt/intel/cce/10.0.023/bin/icc

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include  
-prof-gen(pass 1) -prof-use(pass 2) -fast

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## Peak Optimization Flags (Continued)

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec\_div -ansi-alias

456.hmmer: -L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include  
-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0  
-prefetch -opt-streaming-stores always

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec\_div -ansi-alias -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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