



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY TX600 S3, Intel Xeon processor 7130M, 3.20 GHz

SPECint®\_rate2006 = 70.5

SPECint\_rate\_base2006 = 65.7

CPU2006 license: 22

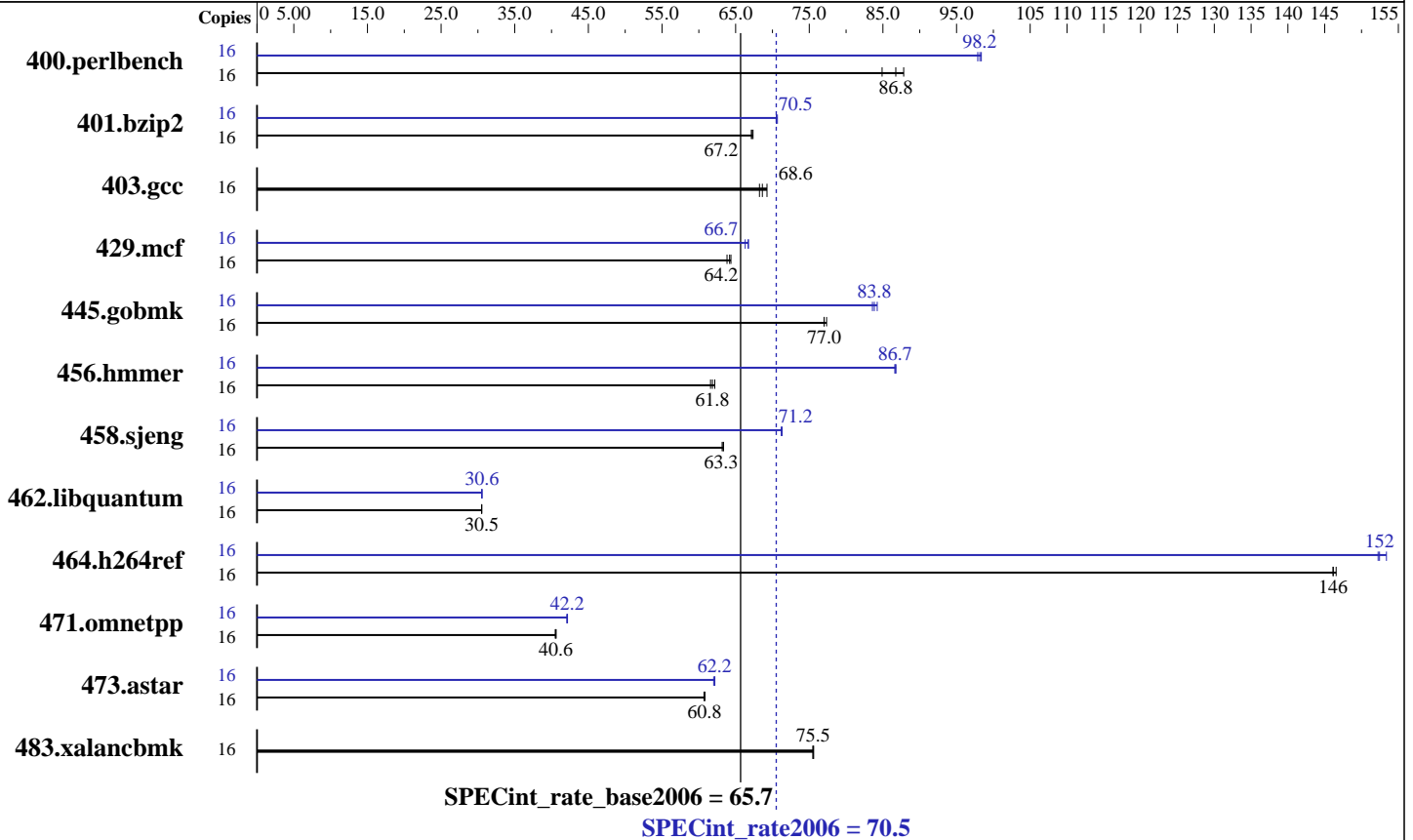
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Aug-2006

Software Availability: Mar-2007



### Hardware

CPU Name: Intel Xeon 7130M  
 CPU Characteristics: 800 MHz system bus  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,4 chips  
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (16x2 GB DDR2 PC2-3200R, 2 rank, CAS 3-3-3, with ECC)  
 Disk Subsystem: Seagate ST973401SS (SAS 73GB 10 krpm)  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multiuser Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<b><u>1802</u></b>	<b><u>86.8</u></b>	1841	84.9	1780	87.8	16	1597	97.9	1589	98.4	<b><u>1592</u></b>	<b><u>98.2</u></b>
401.bzip2	16	2300	67.1	<b><u>2296</u></b>	<b><u>67.2</u></b>	2292	67.4	16	2189	70.5	2185	70.7	<b><u>2189</u></b>	<b><u>70.5</u></b>
403.gcc	16	<b><u>1877</u></b>	<b><u>68.6</u></b>	1860	69.3	1888	68.2	16	<b><u>1877</u></b>	<b><u>68.6</u></b>	1860	69.3	1888	68.2
429.mcf	16	2286	63.8	<b><u>2274</u></b>	<b><u>64.2</u></b>	2267	64.4	16	<b><u>2188</u></b>	<b><u>66.7</u></b>	2201	66.3	2186	66.7
445.gobmk	16	<b><u>2179</u></b>	<b><u>77.0</u></b>	2180	77.0	2170	77.3	16	1993	84.2	<b><u>2002</u></b>	<b><u>83.8</u></b>	2008	83.6
456.hmmmer	16	2424	61.6	2401	62.2	<b><u>2414</u></b>	<b><u>61.8</u></b>	16	<b><u>1723</u></b>	<b><u>86.7</u></b>	1723	86.6	1720	86.8
458.sjeng	16	<b><u>3059</u></b>	<b><u>63.3</u></b>	3065	63.2	3056	63.4	16	2718	71.2	2715	71.3	<b><u>2718</u></b>	<b><u>71.2</u></b>
462.libquantum	16	<b><u>10864</u></b>	<b><u>30.5</u></b>	10868	30.5	10863	30.5	16	<b><u>10850</u></b>	<b><u>30.6</u></b>	10866	30.5	10850	30.6
464.h264ref	16	<b><u>2422</u></b>	<b><u>146</u></b>	2416	147	2423	146	16	2308	153	2326	152	<b><u>2323</u></b>	<b><u>152</u></b>
471.omnetpp	16	2463	40.6	2469	40.5	<b><u>2463</u></b>	<b><u>40.6</u></b>	16	<b><u>2372</u></b>	<b><u>42.2</u></b>	2372	42.2	2377	42.1
473.astar	16	1850	60.7	<b><u>1848</u></b>	<b><u>60.8</u></b>	1846	60.8	16	<b><u>1807</u></b>	<b><u>62.2</u></b>	1811	62.0	1806	62.2
483.xalancbmk	16	1462	75.5	1460	75.6	<b><u>1462</u></b>	<b><u>75.5</u></b>	16	1462	75.5	1460	75.6	<b><u>1462</u></b>	<b><u>75.5</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:  
401.bzip2, 456.hmmmer and 462.libquantum in peak were built with  
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:  
Hardware Prefetch = Enable

This result was measured on the PRIMERGY RX600 S3. The PRIMERGY RX600 S3 and  
the PRIMERGY TX600 S3 are electronically equivalent.

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## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast

C++ benchmarks:  
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmarheap

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -xP -O3 -ipo  
-no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

473.astar: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)



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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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