SPEC® CINT2006 Result

Fujitsu Siemens Computers

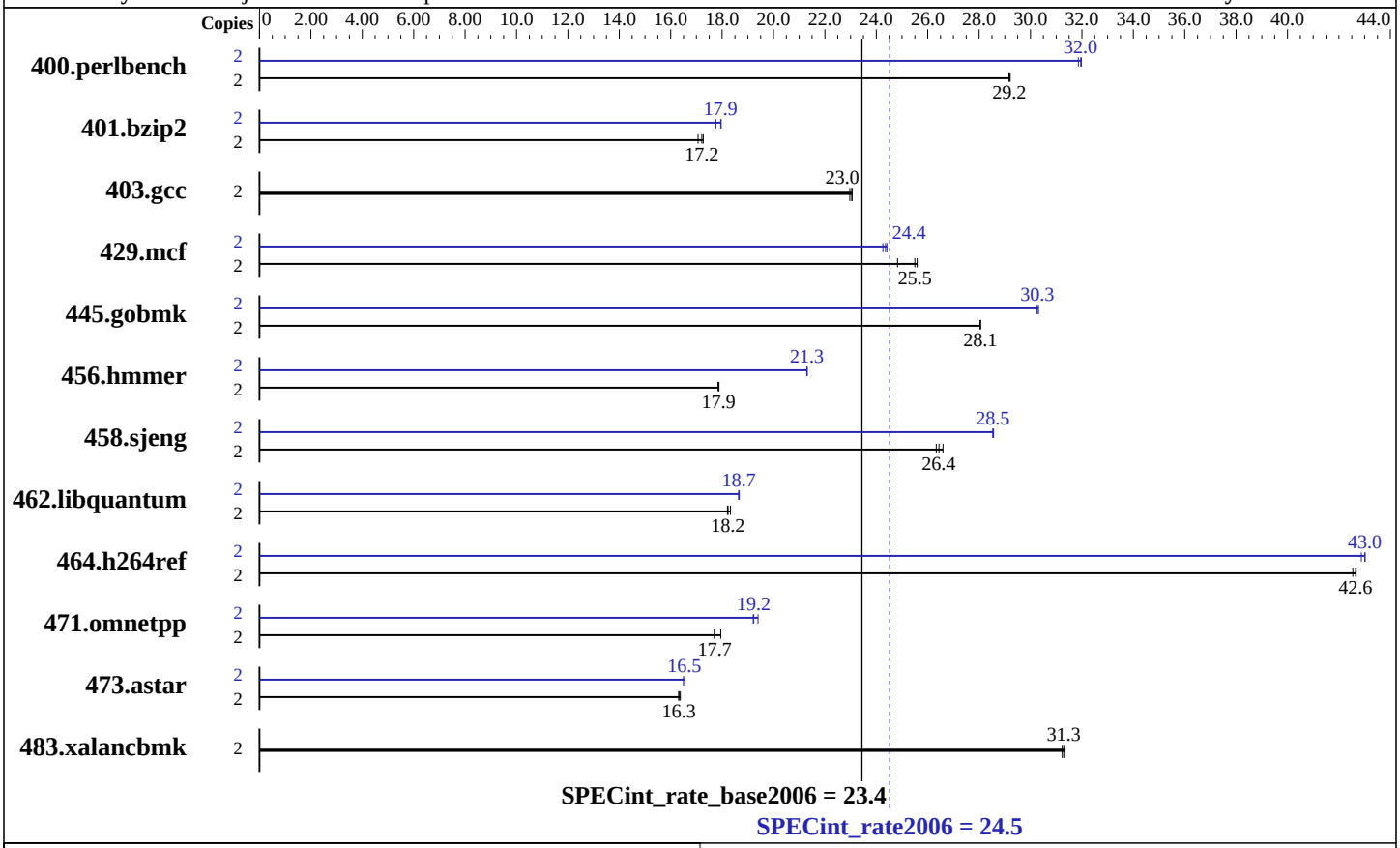
PRIMERGY RX100 S4, Intel Xeon processor 3050, 2.13 GHz

SPECint®_rate2006 = 24.5

SPECint_rate_base2006 = 23.4

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: May-2007
Hardware Availability: Sep-2006
Software Availability: Mar-2007



Hardware

CPU Name:	Intel Xeon 3050
CPU Characteristics:	1067 MHz system bus
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
Disk Subsystem:	SATA (160 GB, 7200 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ReiserFS
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

SPEC CINT2006 Result

Fujitsu Siemens Computers

PRIMERGY RX100 S4, Intel Xeon processor 3050, 2.13 GHz

SPECint_rate2006 = 24.5

SPECint_rate_base2006 = 23.4

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: May-2007
Hardware Availability: Sep-2006
Software Availability: Mar-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	669	29.2	670	29.2	**670**	**29.2**	2	613	31.9	**611**	**32.0**	611	32.0
401.bzip2	2	1118	17.3	1131	17.1	**1121**	**17.2**	2	1074	18.0	1087	17.8	**1076**	**17.9**
403.gcc	2	701	23.0	**699**	**23.0**	698	23.1	2	701	23.0	**699**	**23.0**	698	23.1
429.mcf	2	735	24.8	**715**	**25.5**	713	25.6	2	752	24.3	**749**	**24.4**	747	24.4
445.gobmk	2	748	28.1	748	28.0	**748**	**28.1**	2	692	30.3	693	30.3	**693**	**30.3**
456.hmmer	2	1046	17.8	1044	17.9	**1044**	**17.9**	2	**876**	**21.3**	876	21.3	875	21.3
458.sjeng	2	910	26.6	919	26.3	**915**	**26.4**	2	848	28.5	**848**	**28.5**	847	28.6
462.libquantum	2	**2273**	**18.2**	2261	18.3	2276	18.2	2	2222	18.6	2220	18.7	**2222**	**18.7**
464.h264ref	2	1037	42.7	**1038**	**42.6**	1040	42.5	2	1029	43.0	1032	42.9	**1029**	**43.0**
471.omnetpp	2	706	17.7	696	17.9	**706**	**17.7**	2	**650**	**19.2**	644	19.4	651	19.2
473.astar	2	861	16.3	**860**	**16.3**	857	16.4	2	848	16.6	851	16.5	**849**	**16.5**
483.xalancbmk	2	440	31.3	**441**	**31.3**	442	31.2	2	440	31.3	**441**	**31.3**	442	31.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 429.mcf`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 429.mcf`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 429.mcf`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 11:02:30 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 June 2007.