# SPEC® CINT2006 Result



## NEC Corporation

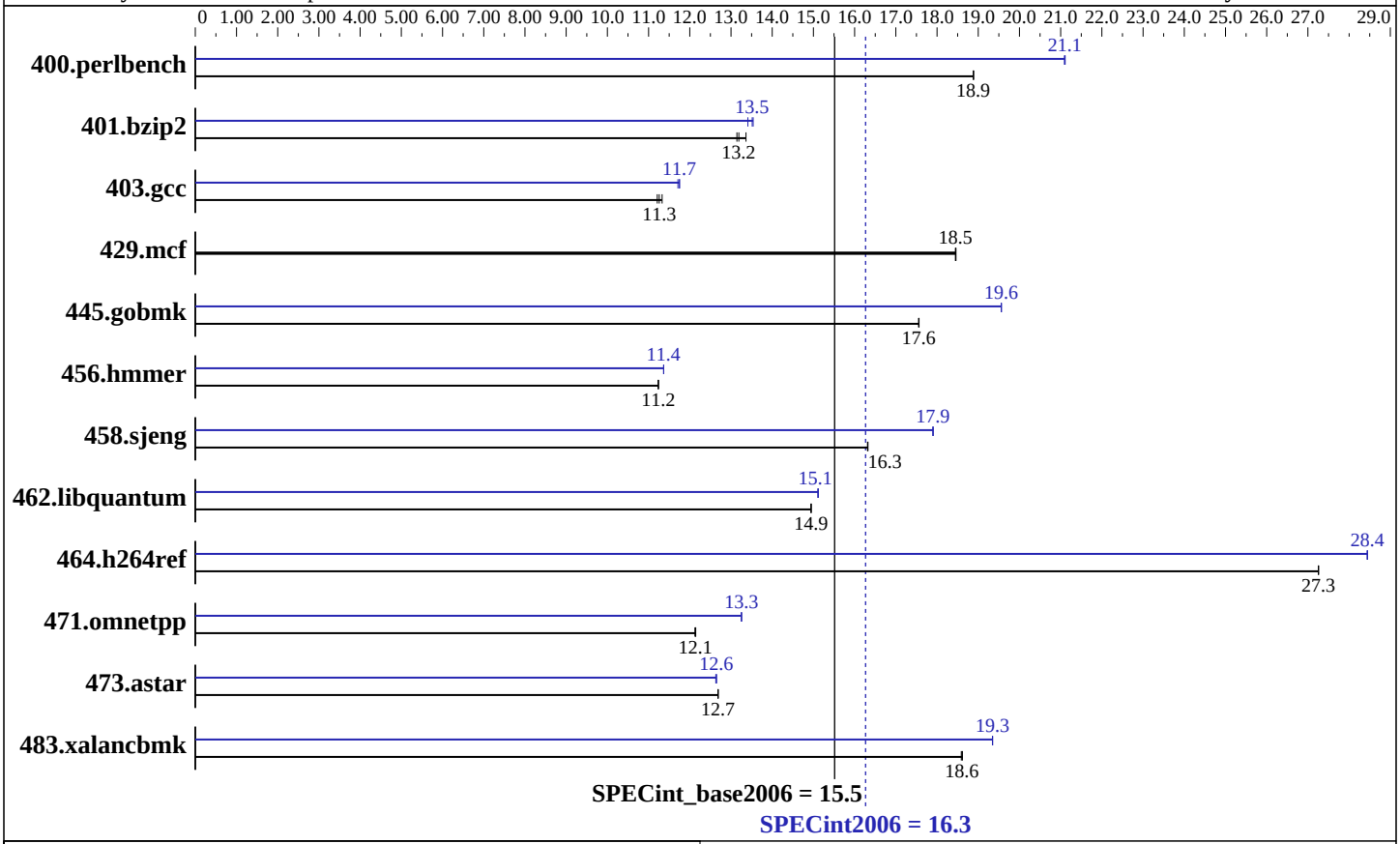
Express5800/120Ri-2
(Intel Xeon processor X5355)

SPECint®2006 = 16.3

SPECint_base2006 = 15.5

**CPU2006 license:** 9006
**Test sponsor:** NEC Corporation
**Tested by:** NEC Corporation

**Test date:** May-2007
**Hardware Availability:** Jan-2007
**Software Availability:** Jan-2007




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5355 |
| CPU Characteristics: | 2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus |
| CPU MHz: | 2666 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 8 MB I+D on chip per chip, 4 MB shared / 2 cores |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 8 GB (8x1 GB DDR2 5300F, 2 rank, CL5-5-5, ECC) |
| Disk Subsystem: | 73.2GB SAS, 15000RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Windows Server 2003, Enterprise x64 Edition |
| Compiler: | Intel C++ Compiler for 32bit version 9.1<br>Build 20070109, Package-ID W_CC_C_9.1.034<br>Microsoft Visual Studio 2005 (libr. & linker) |
| Auto Parallel: | No |
| File System: | NTFS |
| System State: | Default |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32-bit |
| Other Software: | MicroQuill SmartHeap Library 8.1 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | **517** | **18.9** | 517 | 18.9 | 517 | 18.9 | **463** | **21.1** | 463 | 21.1 | 463 | 21.1 |
| 401.bzip2 | **732** | **13.2** | 734 | 13.1 | 722 | 13.4 | 720 | 13.4 | **714** | **13.5** | 713 | 13.5 |
| 403.gcc | **715** | **11.3** | 718 | 11.2 | 711 | 11.3 | **685** | **11.7** | 685 | 11.8 | 687 | 11.7 |
| 429.mcf | 494 | 18.5 | 494 | 18.4 | **494** | **18.5** | 494 | 18.5 | 494 | 18.4 | **494** | **18.5** |
| 445.gobmk | 598 | 17.6 | 597 | 17.6 | **597** | **17.6** | **536** | **19.6** | 536 | 19.6 | 536 | 19.6 |
| 456.hmmer | **830** | **11.2** | 830 | 11.2 | 831 | 11.2 | **821** | **11.4** | 821 | 11.4 | 821 | 11.4 |
| 458.sjeng | 742 | 16.3 | 741 | 16.3 | **741** | **16.3** | 676 | 17.9 | 676 | 17.9 | **676** | **17.9** |
| 462.libquantum | 1386 | 14.9 | **1387** | **14.9** | 1387 | 14.9 | 1371 | 15.1 | 1372 | 15.1 | **1371** | **15.1** |
| 464.h264ref | 812 | 27.3 | 812 | 27.3 | **812** | **27.3** | 778 | 28.4 | **778** | **28.4** | 778 | 28.4 |
| 471.omnetpp | **515** | **12.1** | 515 | 12.1 | 515 | 12.1 | 472 | 13.3 | 472 | 13.3 | **472** | **13.3** |
| 473.astar | 553 | 12.7 | 553 | 12.7 | **553** | **12.7** | 555 | 12.6 | **555** | **12.6** | 556 | 12.6 |
| 483.xalancbmk | **371** | **18.6** | 371 | 18.6 | 371 | 18.6 | 357 | 19.3 | **357** | **19.3** | 357 | 19.3 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

```
The Express5800/120Rg-1 and the Express5800/120Ri-2 models are
electronically equivalent.
The results have been measured on a Express5800/120Ri-2 model.
```

## Base Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

## Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

## Base Optimization Flags

C benchmarks:
```
-fast -F512000000 shlW32M.lib            -link -FORCE:MULTIPLE
```

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## Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx-features -F512000000 shlW32M.lib
-link -FORCE:MULTIPLE

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

## Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

Continued on next page

## Peak Optimization Flags (Continued)

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
-F512000000 shlW32M.lib -link -FORCE:MULTIPLE

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/NEC-cpu2006-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/NEC-cpu2006-ic91-flags.xml



For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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