



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 30.6

PRIMERGY TX150 S5, Intel Xeon processor 3070, 2.66 GHz

SPECint_rate_base2006 = 29.1

CPU2006 license: 22

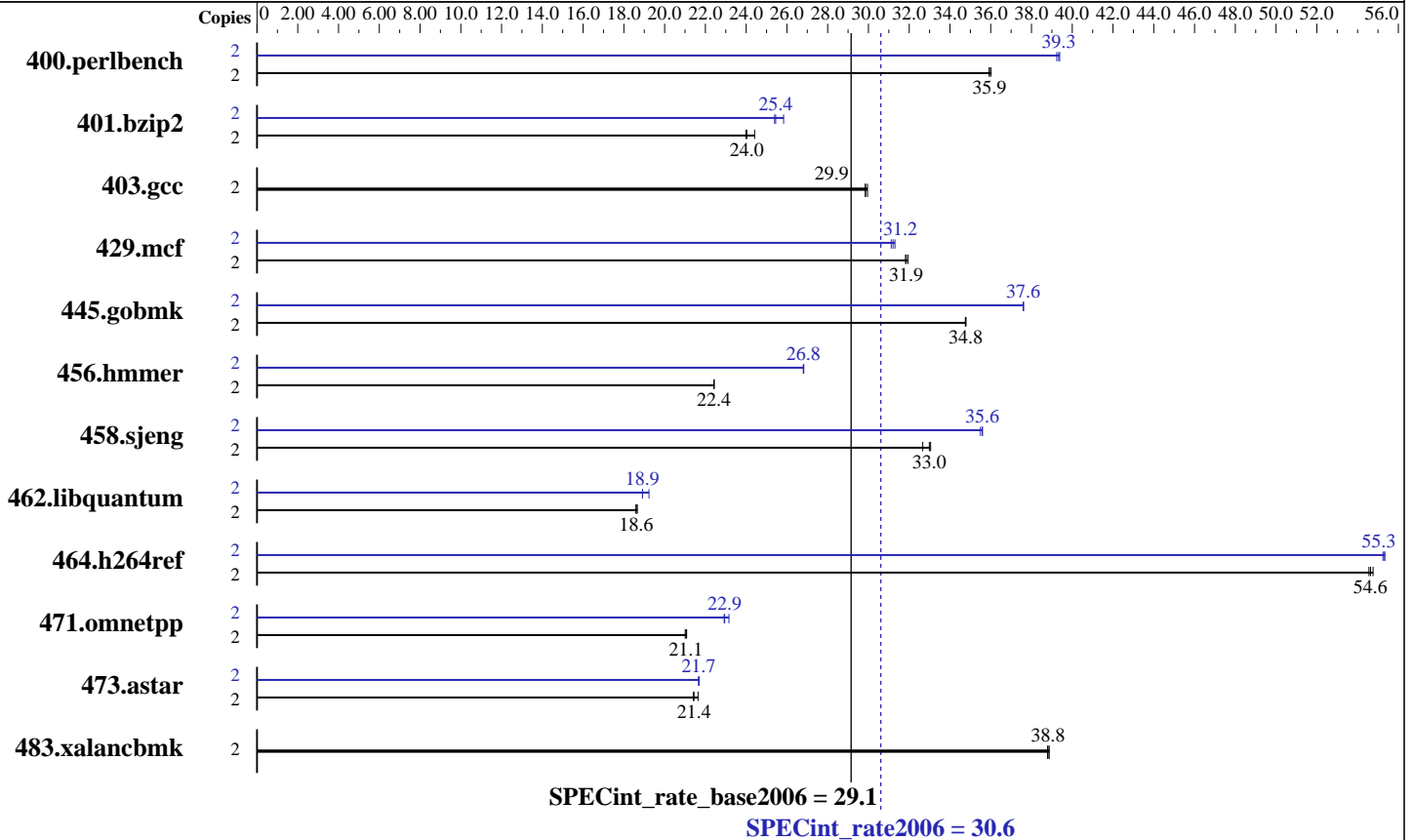
Test date: May-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: May-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Feb-2007



Hardware

CPU Name: Intel Xeon 3070
 CPU Characteristics: 1067 MHz system bus
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
 Disk Subsystem: SATA (160 GB 7200 rpm)
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
 Auto Parallel: No
 File System: ReiserFS
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | | | |
|----------------|--------|------------|-------------|-------------|-------------|------------|-------------|--------|---------|-------|------------|-------------|-------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 2 | 544 | 35.9 | 542 | 36.0 | 544 | 35.9 | 2 | 498 | 39.2 | 496 | 39.4 | 497 | 39.3 |
| 401.bzip2 | 2 | 805 | 24.0 | 803 | 24.0 | 791 | 24.4 | 2 | 747 | 25.8 | 760 | 25.4 | 758 | 25.4 |
| 403.gcc | 2 | 539 | 29.8 | 539 | 29.9 | 537 | 30.0 | 2 | 539 | 29.8 | 539 | 29.9 | 537 | 30.0 |
| 429.mcf | 2 | 573 | 31.8 | 573 | 31.9 | 571 | 31.9 | 2 | 583 | 31.3 | 584 | 31.2 | 586 | 31.1 |
| 445.gobmk | 2 | 603 | 34.8 | 603 | 34.8 | 604 | 34.8 | 2 | 558 | 37.6 | 558 | 37.6 | 558 | 37.6 |
| 456.hmmer | 2 | 832 | 22.4 | 832 | 22.4 | 832 | 22.4 | 2 | 696 | 26.8 | 696 | 26.8 | 696 | 26.8 |
| 458.sjeng | 2 | 732 | 33.1 | 734 | 33.0 | 741 | 32.7 | 2 | 680 | 35.6 | 682 | 35.5 | 680 | 35.6 |
| 462.libquantum | 2 | 2221 | 18.7 | 2227 | 18.6 | 2229 | 18.6 | 2 | 2155 | 19.2 | 2191 | 18.9 | 2191 | 18.9 |
| 464.h264ref | 2 | 808 | 54.8 | 811 | 54.6 | 810 | 54.6 | 2 | 800 | 55.3 | 800 | 55.3 | 801 | 55.2 |
| 471.omnetpp | 2 | 593 | 21.1 | 595 | 21.0 | 594 | 21.1 | 2 | 540 | 23.2 | 545 | 22.9 | 545 | 22.9 |
| 473.astar | 2 | 649 | 21.6 | 655 | 21.4 | 656 | 21.4 | 2 | 647 | 21.7 | 648 | 21.7 | 648 | 21.7 |
| 483.xalancbmk | 2 | 355 | 38.9 | 356 | 38.8 | 356 | 38.8 | 2 | 355 | 38.9 | 356 | 38.8 | 356 | 38.8 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

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For other inquiries, please contact webmaster@spec.org.

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