SPEC® CINT2006 Result

Bull SAS

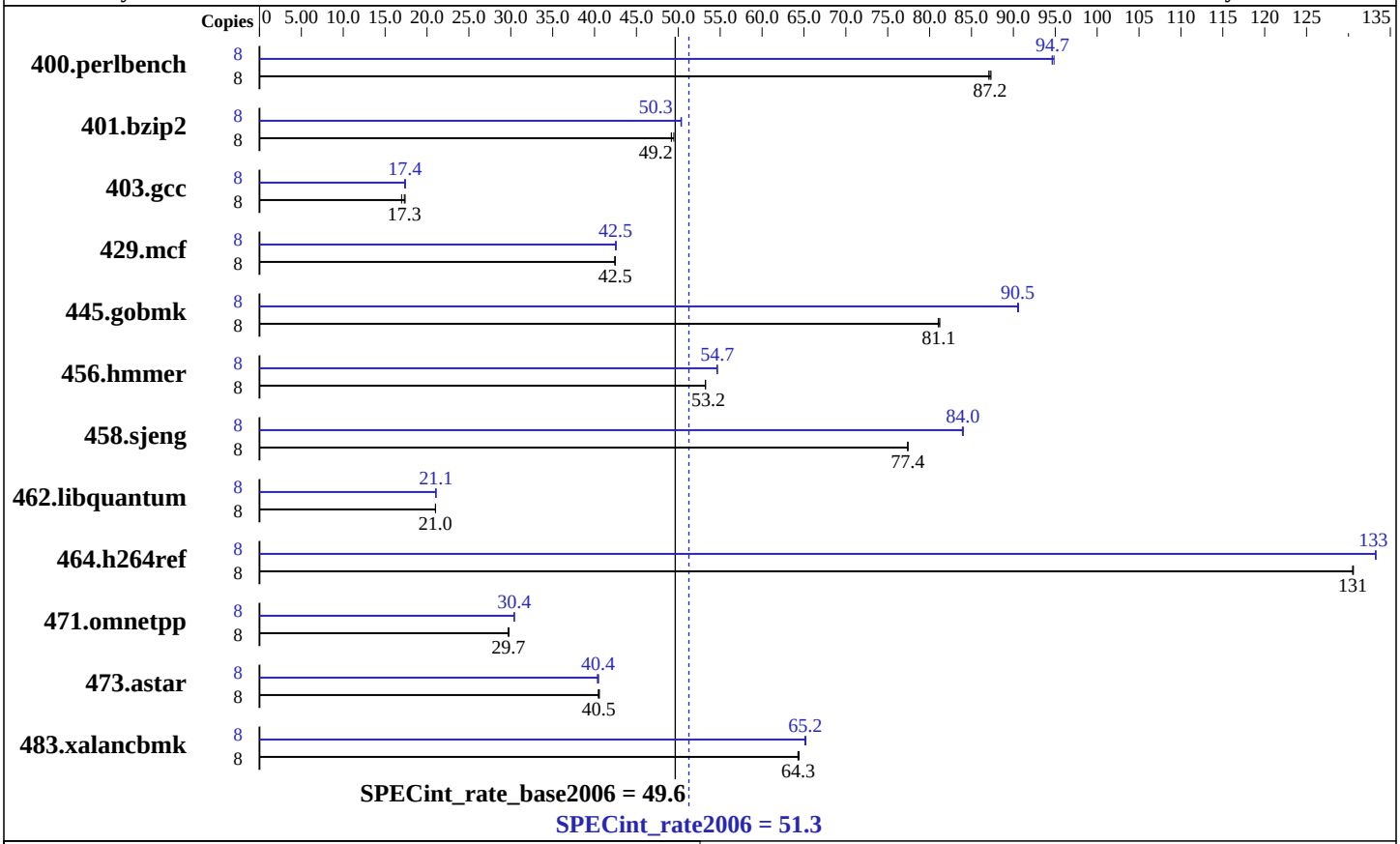
NovaScale B260 (Intel Xeon processor E5310,1.60GHz)

SPECint®_rate2006 = 51.3

SPECint_rate_base2006 = 49.6

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Apr-2007
Hardware Availability: Jan-2007
Software Availability: Dec-2006



Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	1.60 GHz, 8 MB L2, 1066 MHz bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1 to 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem:	1x73 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition (32 bits) Service Pack1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.033 Build no 20061103Z Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	898	87.1	895	87.3	**896**	**87.2**	8	**826**	**94.7**	826	94.6	824	94.9
401.bzip2	8	**1570**	**49.2**	1561	49.5	1570	49.2	8	**1534**	**50.3**	1532	50.4	1534	50.3
403.gcc	8	**3724**	**17.3**	3703	17.4	3796	17.0	8	3696	17.4	**3706**	**17.4**	3715	17.3
429.mcf	8	1720	42.4	1717	42.5	**1719**	**42.5**	8	1713	42.6	**1716**	**42.5**	1716	42.5
445.gobmk	8	1036	81.0	1033	81.2	**1035**	**81.1**	8	927	90.5	926	90.6	**927**	**90.5**
456.hmmer	8	1401	53.3	1403	53.2	**1402**	**53.2**	8	1366	54.7	**1366**	**54.7**	1366	54.6
458.sjeng	8	1251	77.4	1250	77.4	**1251**	**77.4**	8	1154	83.9	**1152**	**84.0**	1152	84.0
462.libquantum	8	**7889**	**21.0**	7890	21.0	7886	21.0	8	**7869**	**21.1**	7863	21.1	7879	21.0
464.h264ref	8	**1356**	**131**	1357	131	1356	131	8	**1329**	**133**	1328	133	1329	133
471.omnetpp	8	1683	29.7	1679	29.8	**1682**	**29.7**	8	1643	30.4	**1644**	**30.4**	1645	30.4
473.astar	8	**1386**	**40.5**	1389	40.4	1384	40.6	8	1387	40.5	1392	40.3	**1389**	**40.4**
483.xalancbmk	8	859	64.3	**858**	**64.3**	857	64.4	8	848	65.1	846	65.2	**847**	**65.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/flags.20090714.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/flags.20090714.00.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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