



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

SPECint®\_rate2006 = 28.7

PRIMERGY RX100 S4, Intel Xeon processor 3060, 2.40 GHz

SPECint\_rate\_base2006 = 27.3

CPU2006 license: 22

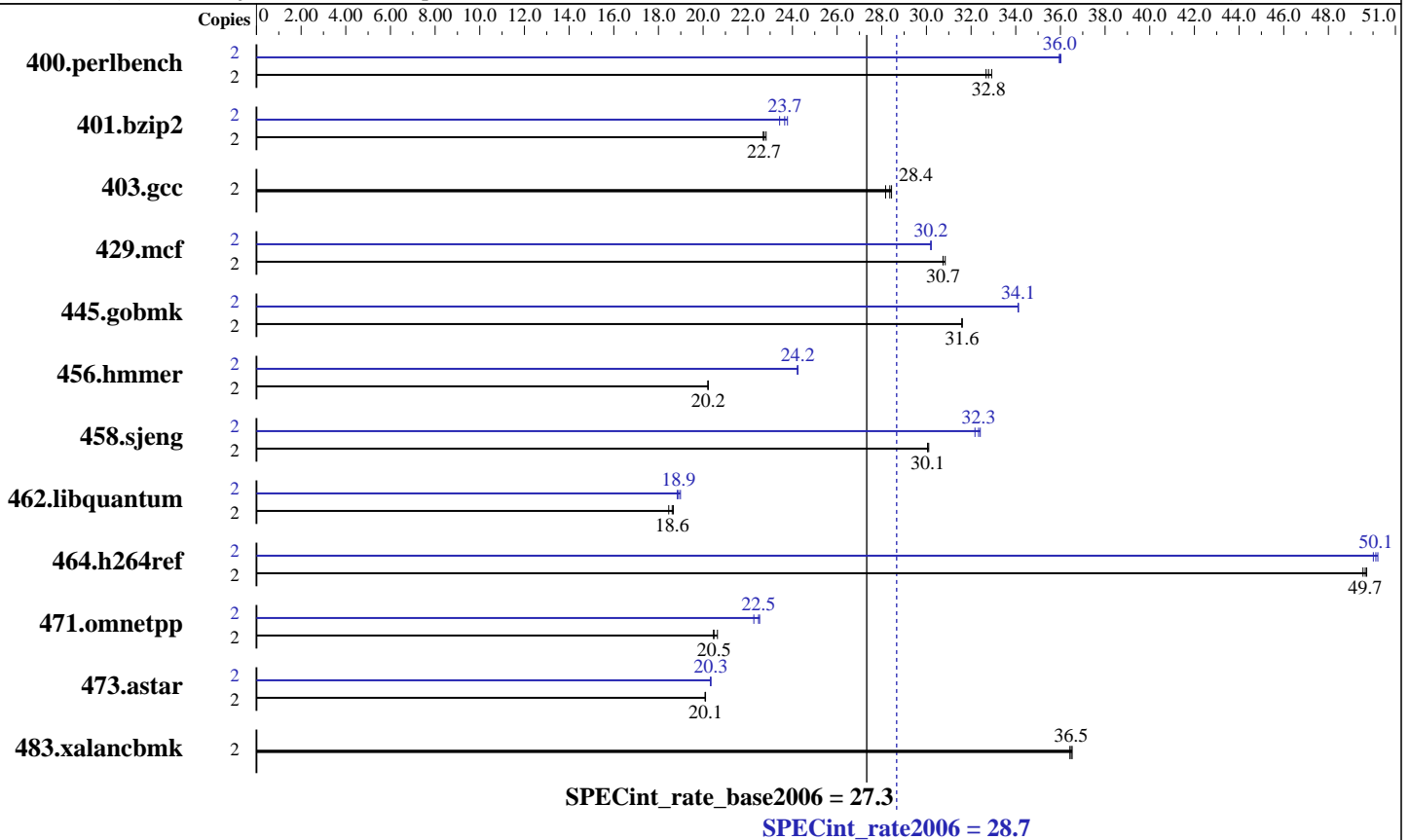
Test date: Apr-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Sep-2006

Tested by: Fujitsu Siemens Computers

Software Availability: Feb-2007



### Hardware

CPU Name: Intel Xeon 3060  
 CPU Characteristics: 1067 MHz system bus  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)  
 Disk Subsystem: SATA (160 GB, 7200 rpm)  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	598	32.7	<u>596</u>	<u>32.8</u>	594	32.9	2	542	36.0	<u>543</u>	<u>36.0</u>	544	36.0
401.bzip2	2	<u>849</u>	<u>22.7</u>	851	22.7	846	22.8	2	812	23.8	824	23.4	<u>816</u>	<u>23.7</u>
403.gcc	2	571	28.2	<u>568</u>	<u>28.4</u>	566	28.4	2	571	28.2	<u>568</u>	<u>28.4</u>	566	28.4
429.mcf	2	593	30.7	<u>593</u>	<u>30.7</u>	591	30.8	2	<u>604</u>	<u>30.2</u>	604	30.2	604	30.2
445.gobmk	2	664	31.6	<u>664</u>	<u>31.6</u>	664	31.6	2	<u>615</u>	<u>34.1</u>	615	34.1	615	34.1
456.hmmer	2	922	20.2	<u>923</u>	<u>20.2</u>	923	20.2	2	770	24.2	770	24.2	<u>770</u>	<u>24.2</u>
458.sjeng	2	<u>805</u>	<u>30.1</u>	805	30.1	804	30.1	2	747	32.4	752	32.2	<u>748</u>	<u>32.3</u>
462.libquantum	2	<u>2226</u>	<u>18.6</u>	2245	18.5	2220	18.7	2	<u>2192</u>	<u>18.9</u>	2199	18.8	2183	19.0
464.h264ref	2	<u>891</u>	<u>49.7</u>	893	49.5	890	49.7	2	<u>883</u>	<u>50.1</u>	885	50.0	881	50.2
471.omnetpp	2	611	20.5	<u>610</u>	<u>20.5</u>	606	20.6	2	561	22.3	555	22.5	<u>556</u>	<u>22.5</u>
473.astar	2	699	20.1	698	20.1	<u>698</u>	<u>20.1</u>	2	690	20.3	690	20.3	<u>690</u>	<u>20.3</u>
483.xalancbmk	2	378	36.5	379	36.4	<u>378</u>	<u>36.5</u>	2	378	36.5	379	36.4	<u>378</u>	<u>36.5</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:  
401.bzip2, 456.hmmer and 462.libquantum in peak were built with  
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:  
Adjacent Sector Prefetch = Disable

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## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast

C++ benchmarks:  
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -xP -O3 -ipo  
-no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

473.astar: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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