SPEC® CINT2006 Result

Fujitsu Siemens Computers

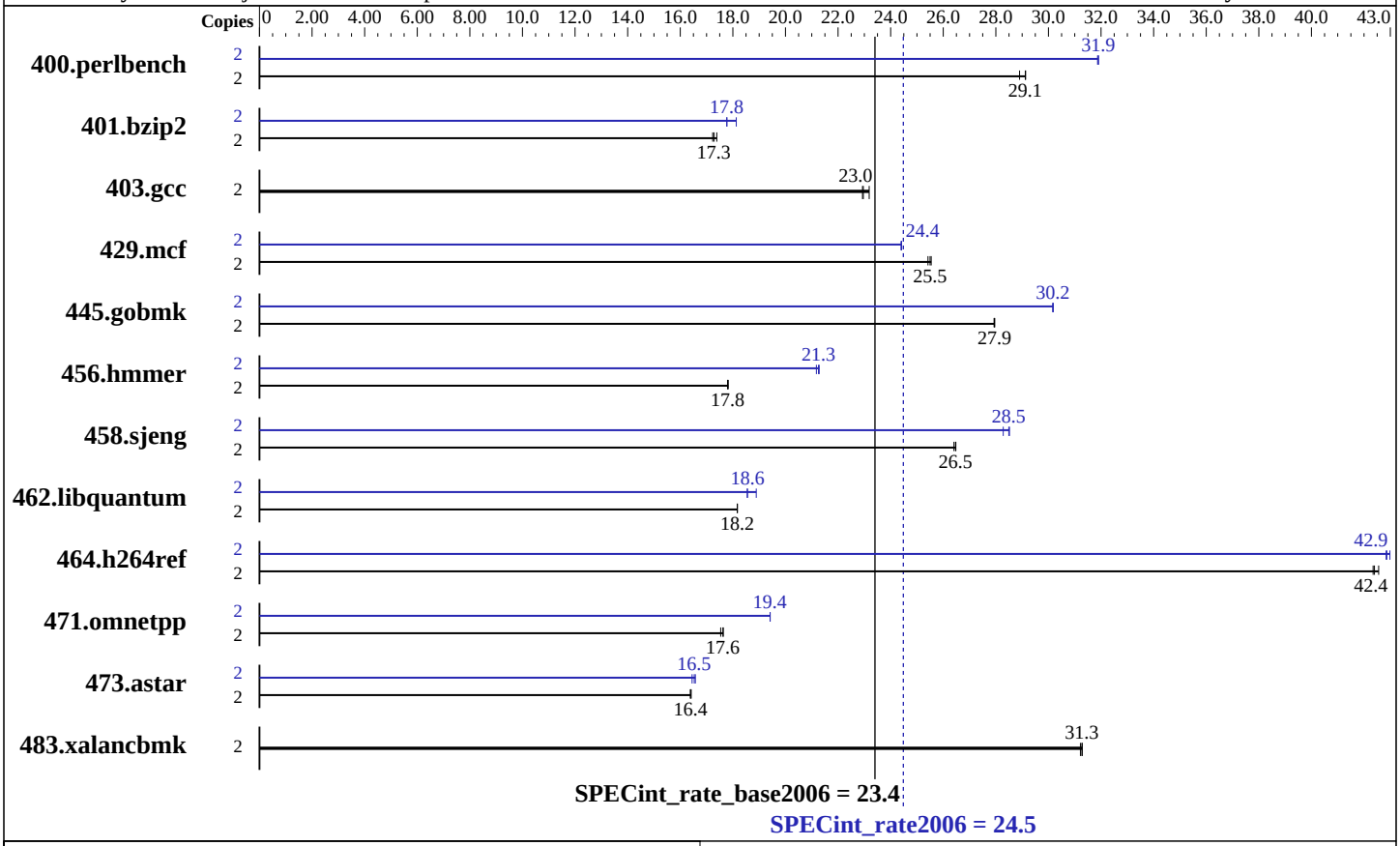
PRIMERGY TX150 S5, Intel Xeon processor 3050, 2.13 GHz

SPECint®_rate2006 = 24.5

SPECint_rate_base2006 = 23.4

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Apr-2007
Hardware Availability: Sep-2006
Software Availability: Feb-2007



Hardware

CPU Name:	Intel Xeon 3050
CPU Characteristics:	1067 MHz system bus
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
Disk Subsystem:	SATA (160 GB 7200 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ReiserFS
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	676	28.9	671	29.1	**671**	**29.1**	2	**613**	**31.9**	613	31.9	612	31.9
401.bzip2	2	**1117**	**17.3**	1120	17.2	1110	17.4	2	1065	18.1	**1086**	**17.8**	1086	17.8
403.gcc	2	**701**	**23.0**	694	23.2	702	22.9	2	**701**	**23.0**	694	23.2	702	22.9
429.mcf	2	**715**	**25.5**	714	25.5	718	25.4	2	748	24.4	**747**	**24.4**	747	24.4
445.gobmk	2	750	28.0	**751**	**27.9**	751	27.9	2	696	30.2	695	30.2	**695**	**30.2**
456.hmmer	2	1048	17.8	**1048**	**17.8**	1047	17.8	2	881	21.2	877	21.3	**878**	**21.3**
458.sjeng	2	914	26.5	916	26.4	**914**	**26.5**	2	856	28.3	**849**	**28.5**	849	28.5
462.libquantum	2	2281	18.2	**2281**	**18.2**	2280	18.2	2	2194	18.9	2235	18.5	**2233**	**18.6**
464.h264ref	2	1045	42.3	**1044**	**42.4**	1040	42.6	2	1033	42.8	**1032**	**42.9**	1030	43.0
471.omnetpp	2	709	17.6	713	17.5	**709**	**17.6**	2	**644**	**19.4**	644	19.4	644	19.4
473.astar	2	857	16.4	855	16.4	**857**	**16.4**	2	854	16.4	**849**	**16.5**	847	16.6
483.xalancbmk	2	441	31.3	**441**	**31.3**	442	31.2	2	441	31.3	**441**	**31.3**	442	31.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

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http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 429.mcf`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 429.mcf`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 429.mcf`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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