



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor 5120,
1.86 GHz

SPECint®_rate2006 = 41.0

SPECint_rate_base2006 = 39.1

CPU2006 license: 22

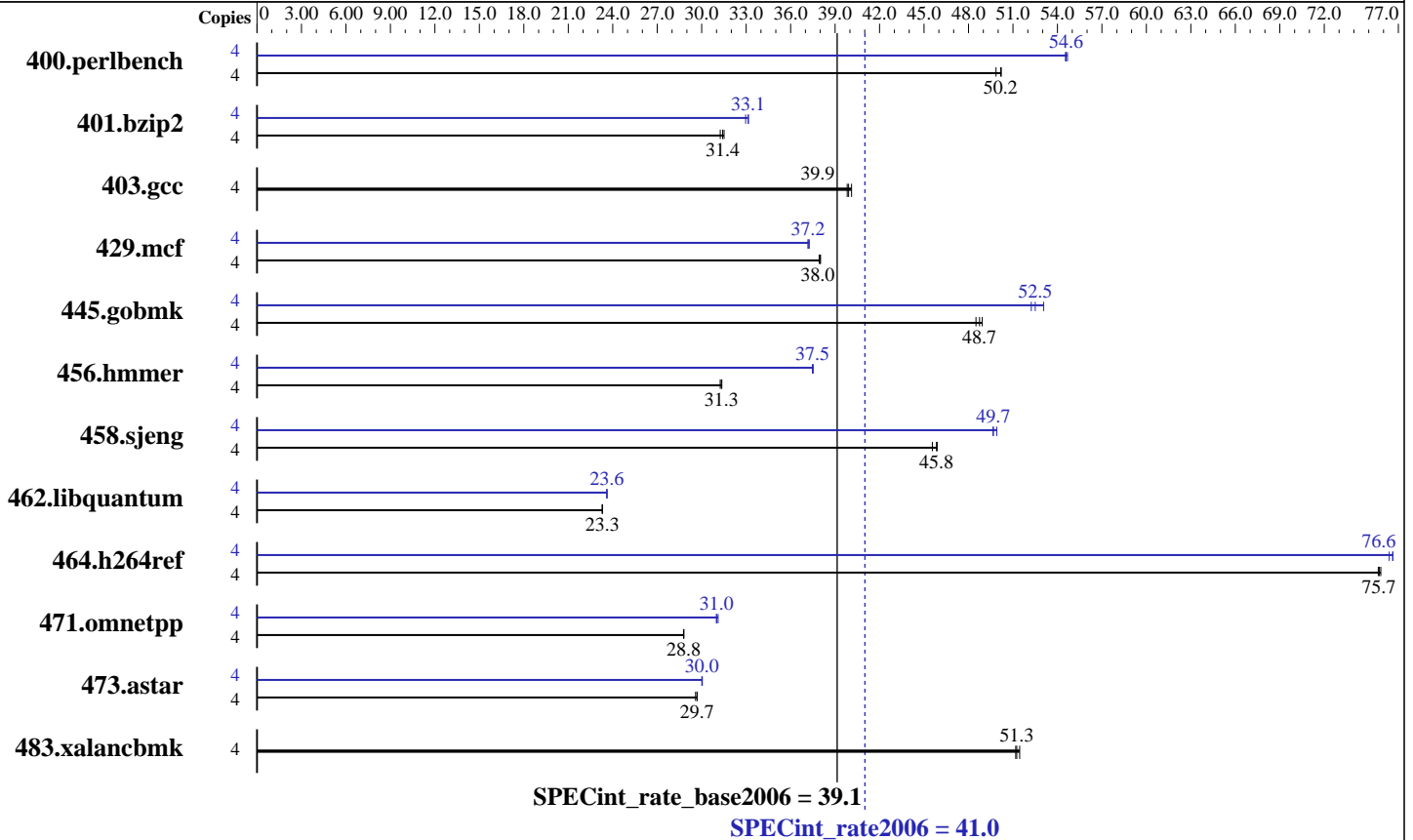
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Jul-2006

Software Availability: Feb-2007



Hardware

CPU Name: Intel Xeon 5120
 CPU Characteristics: 1067 MHz system bus
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: SAS (73GB 15400 rpm)
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<u>779</u>	<u>50.2</u>	784	49.9	778	50.2	4	715	54.7	<u>716</u>	<u>54.6</u>	717	54.5
401.bzip2	4	<u>1229</u>	<u>31.4</u>	1235	31.3	1225	31.5	4	<u>1165</u>	<u>33.1</u>	1165	33.1	1171	33.0
403.gcc	4	809	39.8	<u>807</u>	<u>39.9</u>	803	40.1	4	809	39.8	<u>807</u>	<u>39.9</u>	803	40.1
429.mcf	4	962	37.9	<u>960</u>	<u>38.0</u>	960	38.0	4	<u>981</u>	<u>37.2</u>	979	37.3	981	37.2
445.gobmk	4	858	48.9	<u>861</u>	<u>48.7</u>	865	48.5	4	791	53.1	803	52.2	<u>799</u>	<u>52.5</u>
456.hmmer	4	1194	31.2	<u>1191</u>	<u>31.3</u>	1190	31.4	4	995	37.5	996	37.5	<u>996</u>	<u>37.5</u>
458.sjeng	4	<u>1056</u>	<u>45.8</u>	1055	45.9	1062	45.6	4	975	49.7	970	49.9	<u>974</u>	<u>49.7</u>
462.libquantum	4	3558	23.3	3556	23.3	<u>3557</u>	<u>23.3</u>	4	3512	23.6	<u>3511</u>	<u>23.6</u>	3510	23.6
464.h264ref	4	1168	75.8	1170	75.6	<u>1169</u>	<u>75.7</u>	4	<u>1155</u>	<u>76.6</u>	1155	76.6	1159	76.4
471.omnetpp	4	<u>869</u>	<u>28.8</u>	869	28.8	868	28.8	4	<u>806</u>	<u>31.0</u>	807	31.0	804	31.1
473.astar	4	<u>946</u>	<u>29.7</u>	949	29.6	945	29.7	4	935	30.0	935	30.0	<u>935</u>	<u>30.0</u>
483.xalancbmk	4	536	51.5	<u>538</u>	<u>51.3</u>	539	51.2	4	536	51.5	<u>538</u>	<u>51.3</u>	539	51.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and
the PRIMERGY TX300 S3 are electronically equivalent.

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Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmarheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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