SPEC® CINT2006 Result
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Acer Incorporated

Acer Altos R910 (Intel Xeon 7140M, 3.4GHz)

SPECint®_rate2006 =   39.9

SPECint_rate_base2006 = 36.5
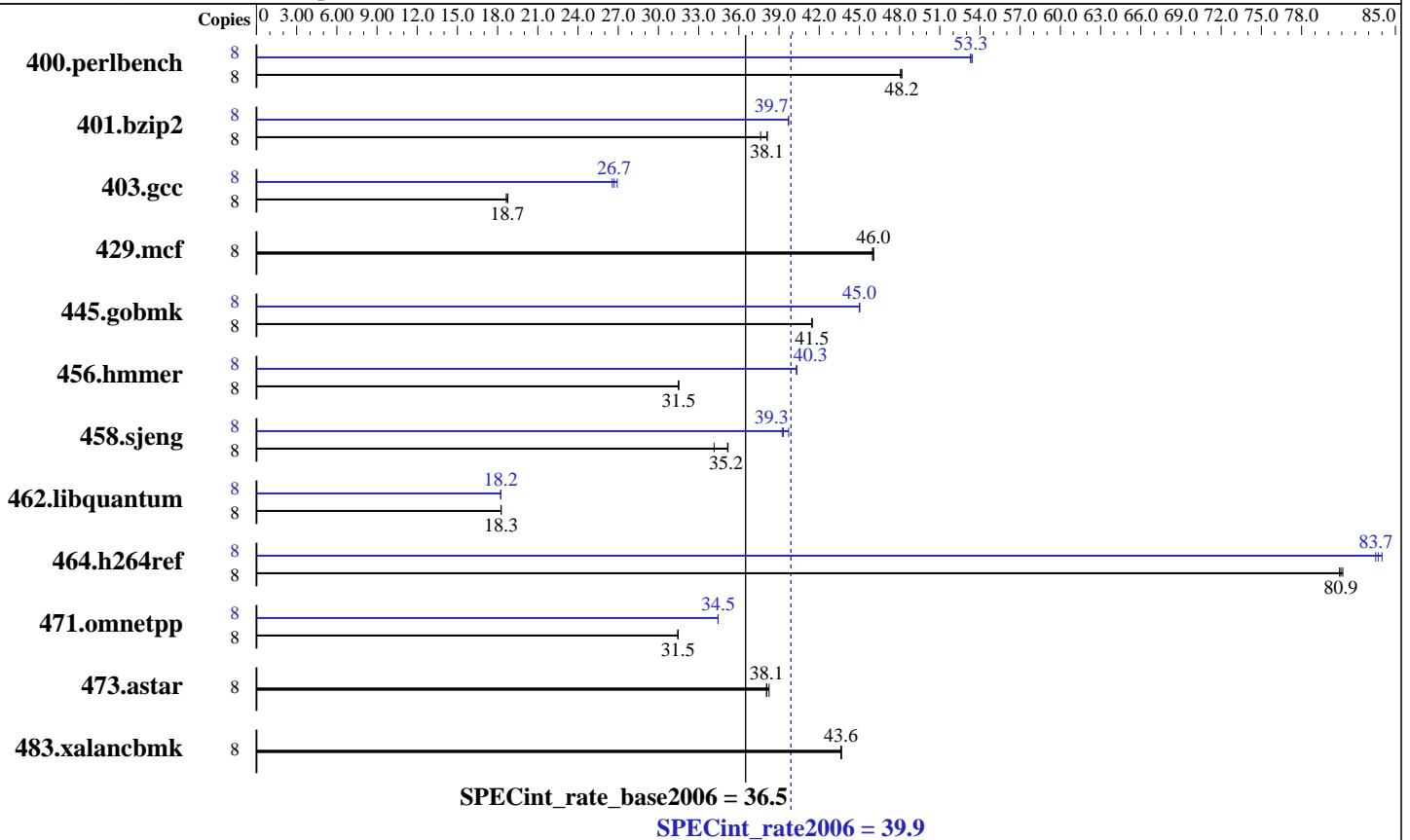
CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Apr-2007
Hardware Availability: Aug-2006
Software Availability: Jul-2006

Copies

Benchmark	Peak	Base
400.perlbench	53.3	48.2
401.bzip2	39.7	38.1
403.gcc	26.7	18.7
429.mcf		46.0
445.gobmk	45.0	41.5
456.hmmer	40.3	31.5
458.sjeng	39.3	35.2
462.libquantum	18.2	18.3
464.h264ref	83.7	80.9
471.omnetpp	34.5	31.5
473.astar		38.1
483.xalancbmk		43.6

(All copies = 8)

SPECint_rate_base2006 = 36.5

SPECint_rate2006 = 39.9

Hardware

CPU Name: Intel Xeon 7140M
CPU Characteristics: 800MHz system bus
CPU MHz: 3400
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable: 1,2,3,4 chips
Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core
L3 Cache: 16 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (16 x 1024MB ECC DDR2-400 SDRAM CL3-3-3)
Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1
Compiler: Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.028 Build 20060706Z Microsoft Visual Studio 2003 .Net (libr. & linker)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill Smart Heap Library, Version 7.4

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	1623	48.2	**1623**	**48.2**	1626	48.1	8	1467	53.3	**1466**	**53.3**	1463	53.4
401.bzip2	8	2052	37.6	2025	38.1	**2026**	**38.1**	8	1944	39.7	1945	39.7	**1944**	**39.7**
403.gcc	8	3454	18.6	3432	18.8	**3439**	**18.7**	8	**2412**	**26.7**	2425	26.6	2392	26.9
429.mcf	8	1584	46.1	1587	46.0	**1585**	**46.0**	8	1584	46.1	1587	46.0	**1585**	**46.0**
445.gobmk	8	2024	41.5	**2024**	**41.5**	2023	41.5	8	**1864**	**45.0**	1865	45.0	1864	45.0
456.hmmer	8	2369	31.5	2369	31.5	**2369**	**31.5**	8	**1851**	**40.3**	1851	40.3	1851	40.3
458.sjeng	8	2750	35.2	2834	34.2	**2753**	**35.2**	8	2467	39.2	**2462**	**39.3**	2437	39.7
462.libquantum	8	9075	18.3	**9081**	**18.3**	9081	18.3	8	**9090**	**18.2**	9091	18.2	9090	18.2
464.h264ref	8	2184	81.1	2190	80.8	**2188**	**80.9**	8	2108	84.0	2119	83.5	**2115**	**83.7**
471.omnetpp	8	**1589**	**31.5**	1590	31.4	1589	31.5	8	1452	34.4	**1451**	**34.5**	1451	34.5
473.astar	8	1475	38.1	**1475**	**38.1**	1468	38.3	8	1475	38.1	**1475**	**38.1**	1468	38.3
483.xalancbmk	8	**1265**	**43.6**	1264	43.7	1266	43.6	8	**1265**	**43.6**	1264	43.7	1266	43.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Disable hardware prefetching and disable adjacent cache line prefetch BIOS setting

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

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Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
          shlw32m.lib               -link /FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
                -link /FORCE:MULTIPLE
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

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Peak Optimization Flags (Continued)

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib   -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Acer-CPU2006-ic91-flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Acer-CPU2006-ic91-flags.20090714.00.xml

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