SPEC® CINT2006 Result

IBM Corporation

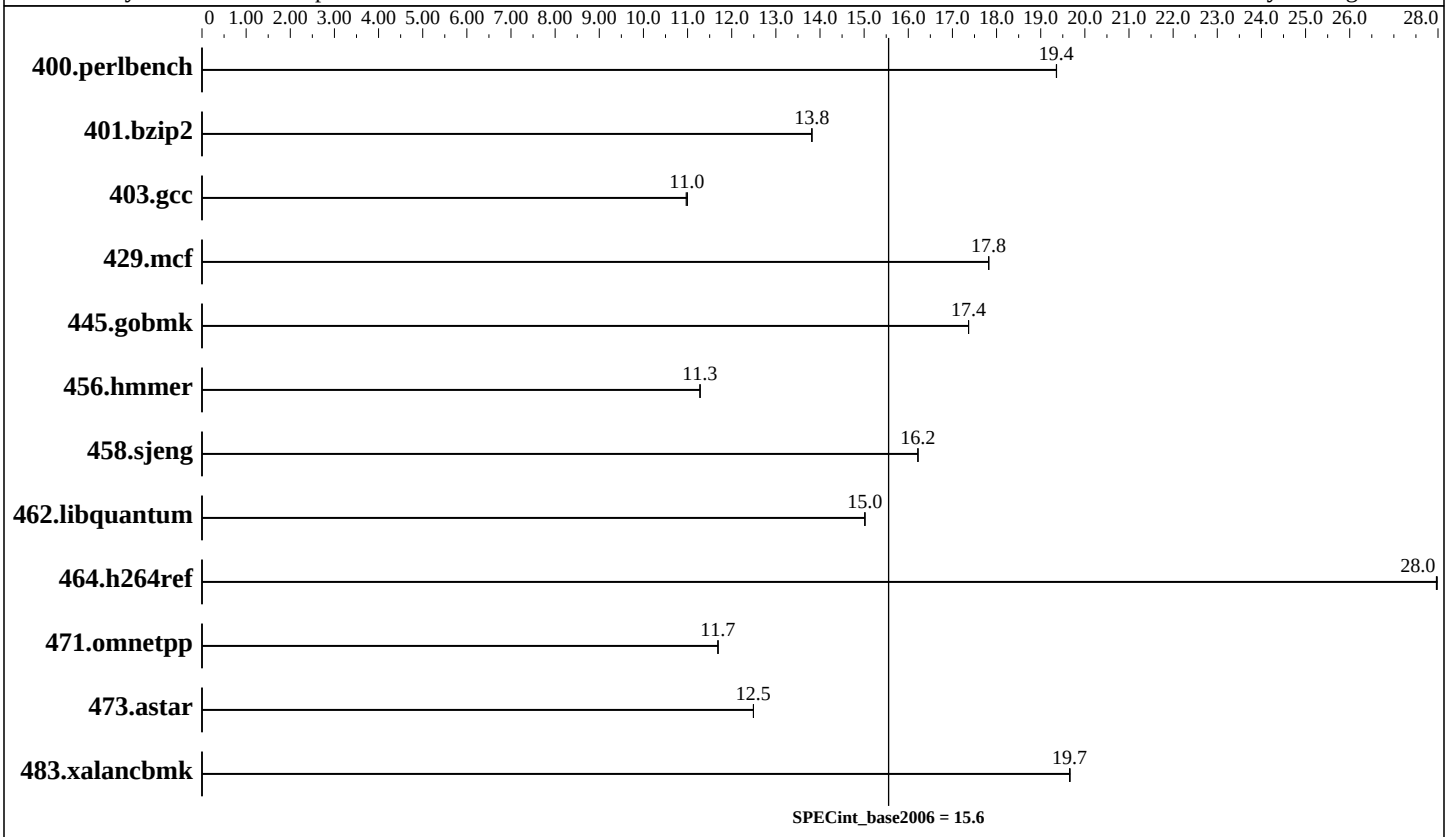
IBM BladeCenter HS21 (Intel Xeon X5355)

SPECint®2006 = Not Run

SPECint_base2006 = 15.6

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Apr-2007
Hardware Availability: Apr-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	1333MHz system bus
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8 x 1GB DDR2-5300F ECC)
Disk Subsystem:	1 x 74 GB SAS, 10000 RPM
Other Hardware:	Memory and I/O Expansion Unit (P/N 42C1600)

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**505**	**19.4**	505	19.4	505	19.4						
401.bzip2	699	13.8	698	13.8	**698**	**13.8**						
403.gcc	734	11.0	732	11.0	**733**	**11.0**						
429.mcf	512	17.8	**512**	**17.8**	512	17.8						
445.gobmk	604	17.4	604	17.4	**604**	**17.4**						
456.hmmer	**827**	**11.3**	827	11.3	827	11.3						
458.sjeng	**746**	**16.2**	746	16.2	746	16.2						
462.libquantum	**1380**	**15.0**	1380	15.0	1380	15.0						
464.h264ref	**791**	**28.0**	791	28.0	791	28.0						
471.omnetpp	535	11.7	**535**	**11.7**	535	11.7						
473.astar	562	12.5	562	12.5	**562**	**12.5**						
483.xalancbmk	**351**	**19.7**	351	19.7	351	19.7						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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