



SPEC[®] CINT2006 Result

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IBM Corporation

SPECint[®]_rate2006 = Not Run

IBM BladeCenter HS21 (Intel Xeon X5355)

SPECint_rate_base2006 = 78.4

CPU2006 license: 11

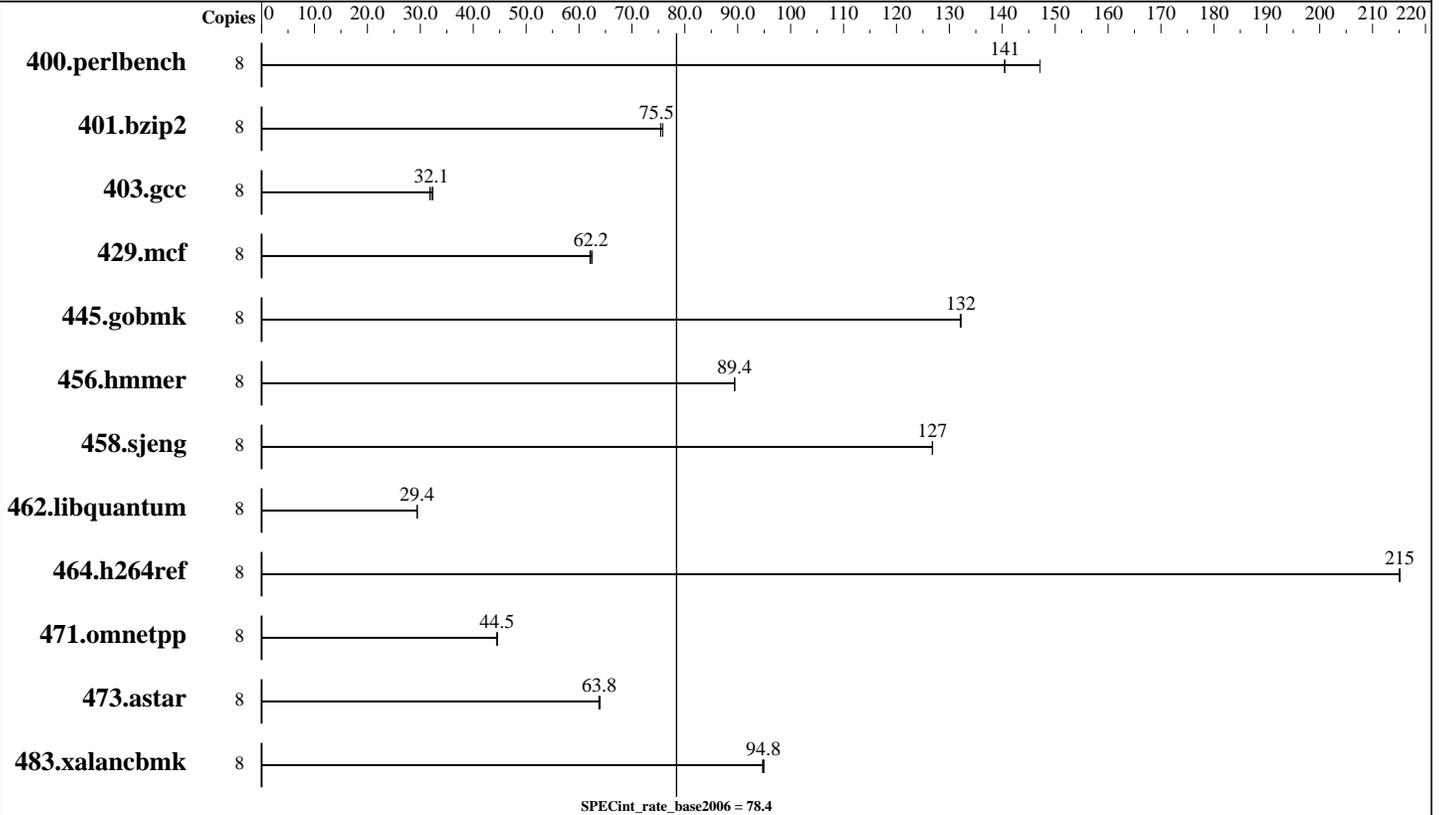
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Apr-2007

Hardware Availability: Apr-2007

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8 x 1GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 74 GB SAS, 10000 RPM
 Other Hardware: Memory and I/O Expansion Unit (P/N 42C1600)

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	531	147	557	140	556	141							
401.bzip2	8	1018	75.8	1023	75.4	1023	75.5							
403.gcc	8	1991	32.4	2005	32.1	2024	31.8							
429.mcf	8	1167	62.5	1175	62.1	1173	62.2							
445.gobmk	8	635	132	635	132	635	132							
456.hammer	8	835	89.4	835	89.4	835	89.4							
458.sjeng	8	764	127	764	127	763	127							
462.libquantum	8	5638	29.4	5638	29.4	5638	29.4							
464.h264ref	8	823	215	823	215	823	215							
471.omnetpp	8	1123	44.5	1124	44.5	1122	44.6							
473.astar	8	878	64.0	881	63.8	880	63.8							
483.xalancbmk	8	583	94.7	581	95.0	582	94.8							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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