



SPEC® CINT2006 Result

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Dell Inc.

PowerEdge 2900 (Intel Xeon processor E5320, 1.86 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 60.4

CPU2006 license: 55

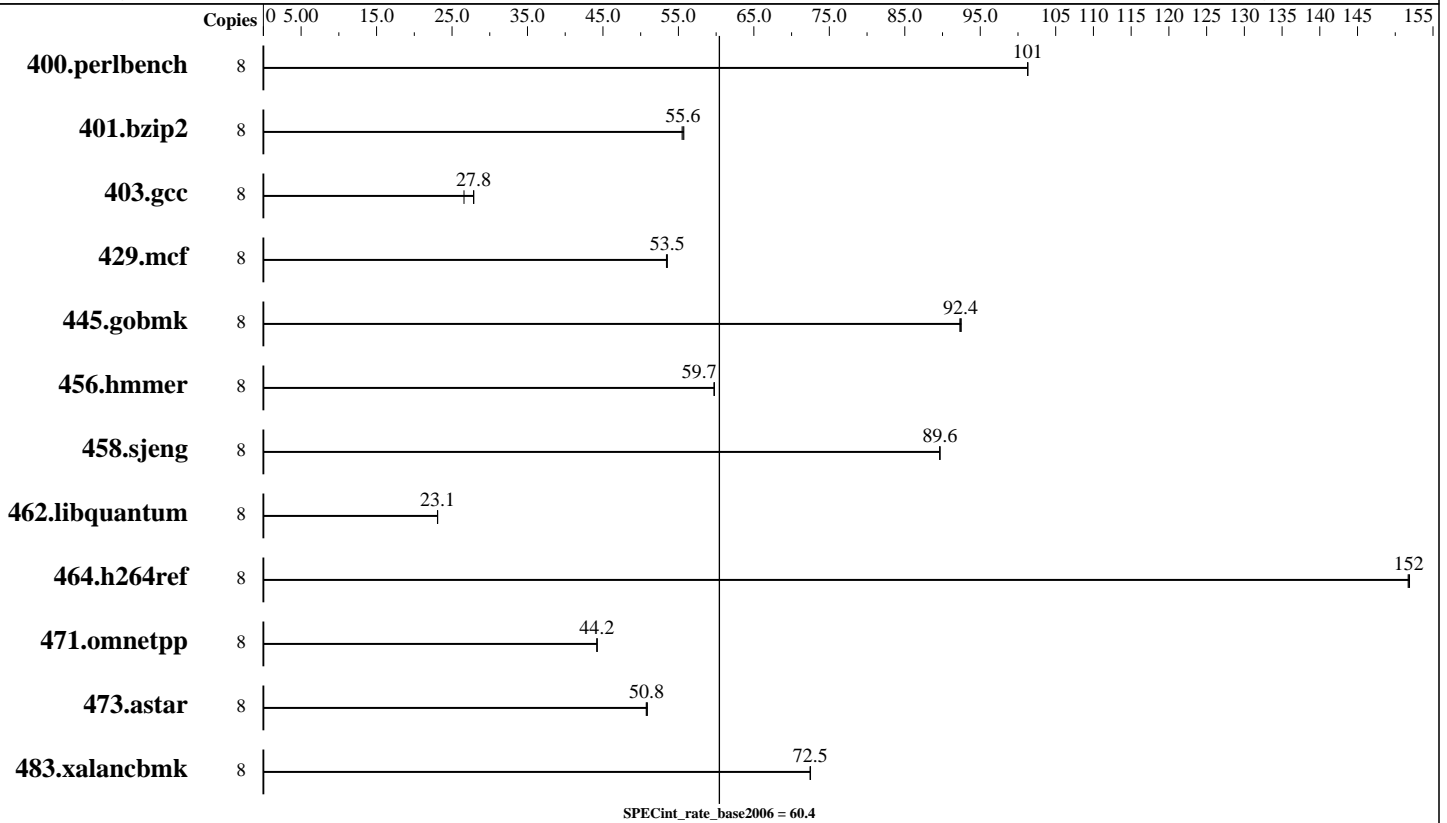
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Mar-2007

Hardware Availability: Dec-2006

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1066 MHz Bus Speed
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)
 Disk Subsystem: 1 x 73 GB SAS 15k RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler 9.1 for IA32 (20061103Z) / Microsoft Visual Studio 2005
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: MicroQuill SmartHeap Library 8.0



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	772	101	772	101	<u>772</u>	<u>101</u>							
401.bzip2	8	1391	55.5	1386	55.7	<u>1388</u>	<u>55.6</u>							
403.gcc	8	<u>2313</u>	<u>27.8</u>	2422	26.6	2309	27.9							
429.mcf	8	1363	53.5	<u>1363</u>	<u>53.5</u>	1366	53.4							
445.gobmk	8	907	92.5	909	92.3	<u>908</u>	<u>92.4</u>							
456.hammer	8	1249	59.8	<u>1249</u>	<u>59.7</u>	1249	59.7							
458.sjeng	8	1080	89.6	<u>1080</u>	<u>89.6</u>	1080	89.7							
462.libquantum	8	7185	23.1	<u>7181</u>	<u>23.1</u>	7177	23.1							
464.h264ref	8	1166	152	<u>1166</u>	<u>152</u>	1167	152							
471.omnetpp	8	1131	44.2	1131	44.2	<u>1131</u>	<u>44.2</u>							
473.astar	8	<u>1105</u>	<u>50.8</u>	1104	50.9	1107	50.7							
483.xalanbmk	8	762	72.4	<u>761</u>	<u>72.5</u>	761	72.5							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Hardware Prefetcher disabled in BIOS
Adjacent Cache Line Prefetch disabled in BIOS

Base Compiler Invocation

C benchmarks:
icl -Qc99

C++ benchmarks:
icl

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalanbmk: -Qoption, cpp, --no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32M.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlW32M.lib  
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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